



*Personal Computer*

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# **IBM Enhanced Graphics Adapter**

IBM ENHANCED GRAPHICS ADAPTER



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# Description

The IBM Enhanced Graphics Adapter (EGA) is a graphics controller that supports both color and monochrome direct drive displays in a variety of modes. In addition to the direct drive port, a light pen interface is provided. Advanced features on the adapter include bit-mapped graphics in four planes and a RAM (Random Access Memory) loadable character generator. Design features in the hardware substantially reduce the software overhead for many graphics functions.

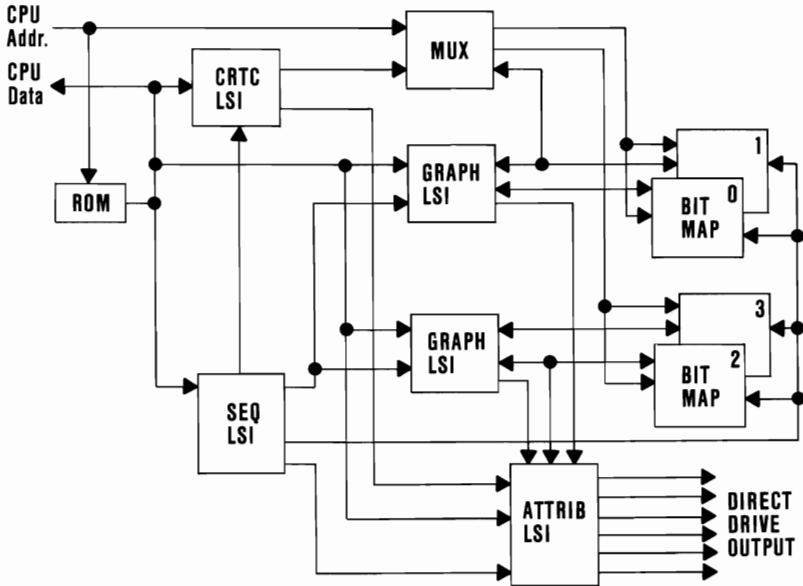
The Enhanced Graphics Adapter provides Basic Input Output System (BIOS) support for both alphanumeric (A/N) modes and all-points-addressable (APA) graphics modes, including all modes supported by the Monochrome Display Adapter and the Color/Graphics Monitor Adapter. Other modes provide APA 640x350 pel graphics support for the IBM Monochrome Display, full 16 color support in both 320x200 pel and 640x200 pel resolutions for the IBM Color Display, and both A/N and APA support with resolution of 640x350 for the IBM Enhanced Color Display. In alphanumeric modes, characters are formed from one of two ROM (Read Only Memory) character generators on the adapter. One character generator defines 7x9 characters in a 9x14 character box. For Enhanced Color Display support, the 9x14 character set is modified to provide an 8x14 character set. The second character generator defines 7x7 characters in an 8x8 character box. These generators contain dot patterns for 256 different characters. The character sets are identical to those provided by the IBM Monochrome Display Adapter and the IBM Color/Graphics Monitor Adapter.

The adapter contains 64K bytes of storage configured as four 16K byte bit planes. Memory expansion options are available to expand the adapter memory to 128K bytes or 256K bytes.

The adapter is packaged on a single 13-1/8 inch (333.50 mm) card. The direct drive port is a right-angle mounted connector at the rear of the adapter and extends through the rear panel of the system unit. Also on the card are five large scale integration (LSI) modules custom designed for this controller.

Located on the adapter is a feature connector that provides access to internal functions through a 32-pin berg connector. A separate 64-pin connector provides an interface for graphics memory expansion.

The following is a block diagram of the Enhanced Graphics Adapter:



**Enhanced Graphics Adapter Block Diagram**

# Major Components

## CRT Controller

The CRT (Cathode Ray Tube) Controller (CRTC) generates horizontal and vertical synchronous timings, addressing for the regenerative buffer, cursor and underline timings, and refresh addressing for the dynamic RAMs.

## Sequencer

The Sequencer generates basic memory timings for the dynamic RAMs and the character clock for controlling regenerative memory fetches. It allows the processor to access memory during active display intervals by inserting dedicated processor memory cycles periodically between the display memory cycles. Map mask registers are available to protect entire memory maps from being changed.

## Graphics Controller

The Graphics Controller directs the data from the memory to the attribute controller and the processor. In graphics modes, memory data is sent in serialized form to the attribute chip. In alpha modes the memory data is sent in parallel form, bypassing the graphics controller. The graphics controller formats the data for compatible modes and provides color comparators for use in color painting modes. Other hardware facilities allow the processor to write 32 bits in a single memory cycle, (8 bits per plane) for quick color presetting of the display areas, and additional logic allows the processor to write data to the display on non-byte boundaries.

## Attribute Controller

The Attribute Controller provides a color palette of 16 colors, each of which may be specified separately. Six color outputs are

available for driving a display. Blinking and underlining are controlled by this chip. This chip takes data from the display memory and formats it for display on the CRT screen.

## **Display Buffer**

The display buffer on the adapter consists of 64K bytes of dynamic read/write memory configured as four 16K byte video bit planes. Two options are available for expanding the graphics memory. The Graphics Memory Expansion Card plugs into the memory expansion connector on the adapter, and adds one bank of 16K to each of the four bit planes, increasing the graphics memory to 128K bytes. The expansion card also provides DIP sockets for further memory expansion. Populating the DIP sockets with the Graphics Memory Module Kit adds two additional 16K banks to each bit plane, bringing the graphics memory to its maximum of 256K bytes.

The address of the display buffer can be changed to remain compatible with other video cards and application software. Four locations are provided. The buffer can be configured at segment address hex A0000 for a length of 128K bytes, at hex A0000 for a length of 64K bytes, at hex B0000 for a length of 32K bytes, or at hex B8000 for a length of 32K bytes.

## **BIOS**

A read-only memory (ROM) Basic Input Output System (BIOS) module on the adapter is linked to the system BIOS. This ROM BIOS contains character generators and control code and is mapped into the processor address at hex C0000 for a length of 16K bytes.

## **Support Logic**

The logic on the card surrounding the LSI modules supports the modules and creates latch buses for the CRT controller, the

processor, and character generator. Two clock sources (14 MHz and 16 MHz) provide the dot rate. The clock is multiplexed under processor I/O control. Four I/O registers also resident on the card are not part of the LSI devices.

## Modes of Operation

### IBM Color Display

The following table describes the modes supported by BIOS on the IBM Color Display:

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
0	A/N	16	40x25	B8000	8x8	8	320x200
1	A/N	16	40x25	B8000	8x8	8	320x200
2	A/N	16	80x25	B8000	8x8	8	640x200
3	A/N	16	80x25	B8000	8x8	8	640x200
4	APA	4	40x25	B8000	8x8	1	320x200
5	APA	4	40x25	B8000	8x8	1	320x200
6	APA	2	80x25	B8000	8x8	1	640x200
D	APA	16	40x25	A0000	8x8	2/4/8	320x200
E	APA	16	80x25	A0000	8x8	1/2/4	640x200

Modes 0 through 6 emulate the support provided by the IBM Color/Graphics monitor Adapter.

Modes 0,2 and 5 are identical to modes 1,3 and 4 respectively at the adapter's direct drive interface.

The Maximum Pages fields for modes D and E indicate the number of pages supported when 64K, 128K or 256K bytes of graphics memory is installed, respectively.

## IBM Monochrome Display

The following table describes the modes supported by BIOS on the IBM Monochrome Display.

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
7	A/N	4	80x25	B0000	9x14	8	720x350
F	APA	4	80x25	A0000	8x14	1/2	640x350

Mode 7 emulates the support provided by the IBM Monochrome Display Adapter.

## IBM Enhanced Color Display

The Enhanced Graphics Adapter supports attachment of the IBM Enhanced Color Display. The IBM Enhanced Color Display is capable of running at the standard television frequency of 15.75 KHz as well as running 21.85 KHz. The table below summarizes the characteristics of the IBM Enhanced Color Display:

Parameter	TV Frequency	High Resolution
Horiz Scan Rate	15.75 KHz.	21.85 KHz.
Vertical Scan Rate	60 Hz.	60 Hz.
Video Bandwidth	14.318 MHz.	16.257 MHz.
Displayable Colors	16 Maximum	16 or 64
Character Size	7 by 7 Pels	7 by 9 Pels
Character Box Size	8 by 8 Pels	8 by 14 Pels
Maximum Resolution	640x200 Pels	640 by 350 Pels
Alphanumeric Modes	0,1,2,3	0,1,2,3
Graphics Modes	4,5,6,D,E	10

In the television frequency mode, the IBM Enhanced Color Display displays information identical in color and resolution to the IBM Color Display.

In the high resolution mode, the adapter provides enhanced alphanumeric character support. This enhanced alphanumeric support consists of transforming the 8 by 8 character box into an 8 by 14 character box, and providing 16 colors out of a palette of

64 possible display colors. Display colors are changed by altering the programming of the color palette registers in the Attribute Controller. In alphanumeric modes, any 16 of 64 colors are displayable. the screen resolution is 320x350 for modes 0 and 1, and 640x350 for modes 2 and 3.

The resolution displayed on the IBM Enhanced Color Display is selected by the switch settings on the Enhanced Graphics Adapter.

The Enhanced Color Display is compatible with all modes listed for the IBM Color Display. the following table describes additional modes supported by BIOS for the IBM Enhanced Color Display:

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
0*	A/N	16/64	40x25	B8000	8x14	8	320x350
1*	A/N	16/64	40x25	B8000	8x14	8	320x350
2*	A/N	16/64	80x25	B8000	8x14	8	640x350
3*	A/N	16/64	80x25	B8000	8x14	8	640x350
10*	APA	4/16 16/64	80x25	A0000	8x14	1/2	640x350

\* Note that modes 0, 1, 2, and 3, are also listed for IBM Color Display support. BIOS provides enhanced support for these modes when an Enhanced Color Display is attached.

The values in the "COLORS" field indicate 16 colors of a 64 color palette or 4 colors of a sixteen color palette.

In mode 10, The dual values for the "COLORS" field and the "MAX. PAGES" field indicate the support provided when 64K or when greater than 64K of graphics memory is installed, respectively.

# Basic Operations

## Alphanumeric Modes

The data format for alphanumeric modes on the Enhanced Graphics Adapter is the same as the data format on the IBM Color/Graphics Monitor Adapter and the IBM Monochrome Display Adapter. As an added function, bit three of the attribute byte may be redefined by the Character Map Select register to act as a switch between character sets. This gives the programmer access to 512 characters at one time. This function is valid only when memory has been expanded to 128K bytes or more.

When an alphanumeric mode is selected, the BIOS transfers character patterns from the ROM to bit plane 2. The processor stores the character data in bit plane 0, and the attribute data in bit plane 1. The programmer can view bit planes 0 and 1 as a single buffer in alphanumeric modes. The CRTC generates sequential addresses, and fetches one character code byte and one attribute byte at a time. The character code and row scan count address bit plane 2, which contains the character generators. The appropriate dot patterns are then sent to the palette in the attribute chip, where color is assigned according to the attribute data.

## Graphics Modes

### 320x200 Two and Four Color Graphics (Modes 4 and 5)

Addressing, mapping and data format are the same as the 320x200 pel mode of the Color/Graphics Monitor Adapter. The display buffer is configured at hex B8000. Bit image data is stored in bit planes 0 and 1.

### 640x200 Two Color Graphics (Mode 6)

Addressing, mapping and data format are the same as the 640x200 pel black and white mode of the Color/Graphics



Monitor Adapter. The display buffer is configured at hex B8000. Bit image data is stored in bit plane 0.

## 640x350 Monochrome Graphics (Mode F )

This mode supports graphics on the IBM Monochrome Display with the following attributes: black, video, blinking video, and intensified video. Resolution of 640x350 requires 56K bytes to support four attributes. By chaining maps 0 and 1, then maps 2 and 3 together, two 32K bit planes can be formed. This chaining is done only when necessary (less than 128K of graphics memory). The first map is the video bit plane, and the second map is the intensity bit plane. Both planes reside at hex address A0000.

Two bits, one from each bit plane, define one picture element (pel) on the screen. The bit definitions for the pels are given in the following table. The video bit plane is denoted by C0 and the Intensity Bit Plane is denoted by C2.

C2	C0	Pixel Color	Valid Attributes
0	0	Black	0
0	1	Video	3
1	0	Blinking Video	C
1	1	Intensified Video	F

The byte organization in memory is sequential. The first eight pels on the screen are defined by the contents of memory in location A000:0H, the second eight pels by location A000:1H, and so on. The first pel within any one byte is defined by bit 7 in the byte. The last pel within the byte is defined by bit 0 in the byte.

Monochrome graphics works in odd/even mode, which means that even CPU addresses go into even bit planes and odd CPU addresses go into odd bit planes. Since both bit planes reside at address A0000, the user must select which plane or planes he desires to update. This is accomplished by the map mask register of the sequencer. (See the table above for valid attributes).

## 16/64 Color Graphics Modes (Mode 10)

These modes support graphics in 16 colors on either a medium or high resolution monitor. The memory in these modes consists of using all four bit planes. Each bit plane represents a color as shown below. The bit planes are denoted as C0,C1,C2 and C3 respectively.

C0 = Blue Pels  
C1 = Green Pels  
C2 = Red Pels  
C3 = Intensified Pels

Four bits (one from each plane) define one pel on the screen. The color combinations are illustrated in the following table:

I	R	G	B	Color
0	0	0	0	Black
0	0	0	1	Blue
0	0	1	0	Green
0	0	1	1	Cyan
0	1	0	0	Red
0	1	0	1	Magenta
0	1	1	0	Brown
0	1	1	1	White
1	0	0	0	Dark Gray
1	0	0	1	Light Blue
1	0	1	0	Light Green
1	0	1	1	Light Cyan
1	1	0	0	Light Red
1	1	0	1	Light Magenta
1	1	1	0	Yellow
1	1	1	1	Intensified White

The display buffer resides at address A0000. The map mask register of the sequencer is used to select any or all of the bit planes to be updated when a memory write to the display buffer is executed by the CPU.

### Color Mapping

The Enhanced Graphics Adapter supports 640x350 Graphics for both the IBM Monochrome and the IBM Enhanced Color

Displays. Four color capability is supported on the EGA without the Graphics Memory Expansion Card (base 64 KB), and sixteen colors are supported when the Graphics Memory Expansion Card is installed on the adapter (128 KB or above). This section describes the differences in the colors displayed depending upon the graphics memory available. Note that colors 0H, 1H, 4H, and 7H map directly regardless of the graphics memory available.

<b>Character Attribute</b>	<b>Monochrome</b>	<b>Mode 10H 64KB</b>	<b>Mode 10H &gt;64KB</b>
00H*	Black	Black	Black
01H*	Video	Blue	Blue
02H	Black	Black	Green
03H	Video	Blue	Cyan
04H*	Blinking	Red	Red
05H	Intensified	White	Magenta
06H	Blinking	Red	Brown
07H*	Intensified	White	White
08H	Black	Black	Dark Gray
09H	Video	Blue	Light Blue
0AH	Black	Black	Light Green
0BH	Video	Blue	Light Cyan
0CH	Blinking	Red	Light Red
0DH	Intensified	White	Light Magenta
0EH	Blinking	Red	Yellow
0FH	Intensified	White	Intensified White

\* Graphics character attributes which map directly regardless of the graphics memory available.

# Registers

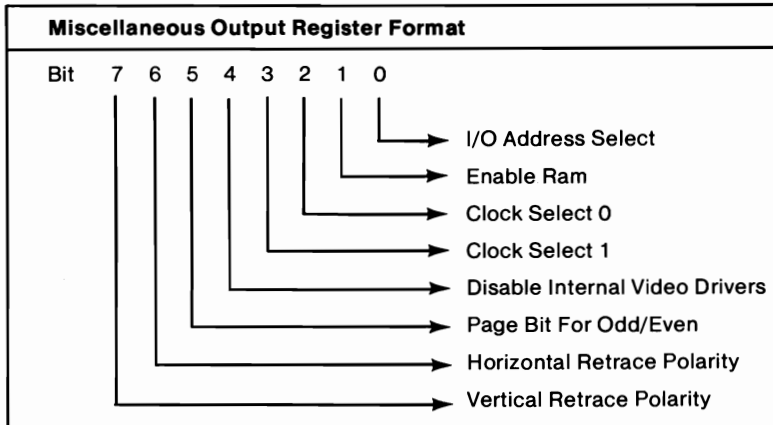
## External Registers

This section contains descriptions of the registers of the Enhanced Graphics Adapter that are not contained in an LSI device.

Name	Port	Index
Miscellaneous Output Register	3C2	-
Feature Control Register	3?A	-
Input Status Register 0	3C2	-
Input Status Register 1	3?2	-
? = B in Monochrome Modes		? = D in Color Modes

### Miscellaneous Output Register

This is a write-only register. The processor output port address is hex 3C2. A hardware reset causes all bits to reset to zero.



**Bit 0**                    3BX/3DX CRTIC I/O Address—This bit maps the CRTIC I/O addresses for IBM Monochrome or Color/Graphics Monitor Adapter emulation. A logical 0 sets CRTIC addresses to 3BX and Input Status Register 1 's address to 3BA for Monochrome emulation. A logical 1 sets CRTIC

addresses to 3DX and Input Status Register 1's address to 3DA for Color/Graphics Monitor Adapter emulation.

**Bit 1** Enable RAM—A logical 0 disables RAM from the processor; a logical 1 enables RAM to respond at addresses designated by the Control Data Select value programmed into the Graphics Controllers.

**Bit 2–Bit 3** Clock Select—These two bits select the clock source according to the following table:

**Bits**

**3 2**

**0 0-** Selects 14 MHz clock from the processor I/O channel

**0 1-** Selects 16 MHz clock on-board oscillator

**1 0-** Selects external clock source from the feature connector.

**1 1-** Not used

**Bit 4** Disable Internal Video Drivers—A logical 0 activates internal video drivers; a logical 1 disables internal video drivers. When the internal video drivers are disabled, the source of the direct drive color output becomes the feature connector direct drive outputs.

**Bit 5** Page Bit For Odd/Even—Selects between two 64K pages of memory when in the Odd/Even modes (0,1,2,3,7). A logical 0 selects the low page of memory; a logical 1 selects the high page of memory.

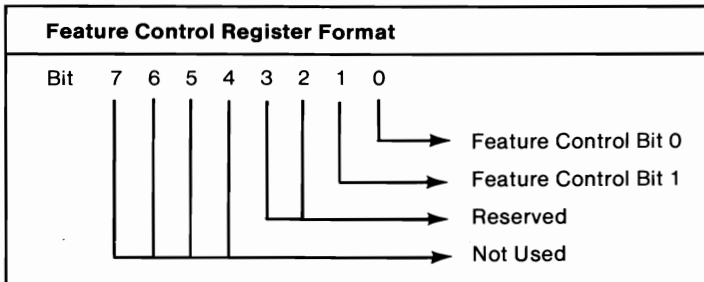
**Bit 6** Horizontal Retrace Polarity—A logical 0 selects positive horizontal retrace; a logical 1 selects negative horizontal retrace.

**Bit 7** Vertical Retrace Polarity—A logical 0 selects positive vertical retrace; a logical 1 selects

negative vertical retrace. The IBM Monochrome display requires a negative vertical retrace polarity.

## Feature Control Register

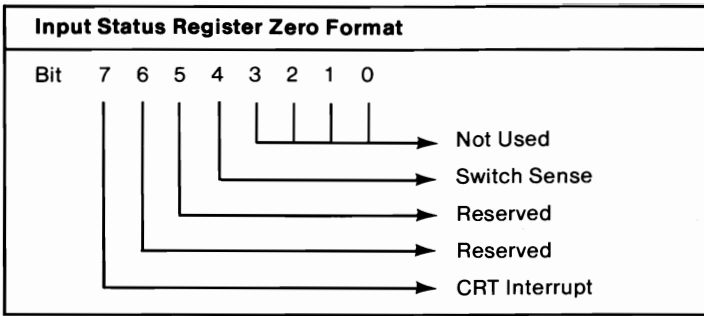
This is a write-only register. The processor output register is hex 3BA or 3DA.



**Bits 0 and 1** Feature Control Bits—These bits are used to convey information to the feature connector. The output of these bits goes to the FEAT 0 (pin 19) and FEAT 1 (pin 17) of the feature connector.

## Input Status Register Zero

This is a read-only register. The processor input port address is hex 3C2.



**Bit 4**      Switch Sense—When set to 1, this bit allows the processor to read the four configuration switches on the board. The setting of the CLKSEL field determines which switch is being read. The switch configuration can be determined by reading byte 40:88H in RAM.

Bit 3: Switch 4 ; Logical 0 = switch closed

Bit 2: Switch 3 ; Logical 0 = switch closed

Bit 1: Switch 2 ; Logical 0 = switch closed

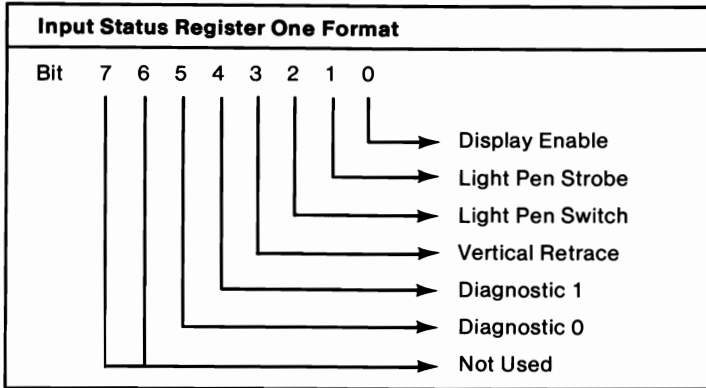
Bit 0: Switch 1 ; Logical 0 = switch closed

**Bits 5 and 6**      Feature Code—These bits are input from the Feat (0) and Feat (1) pins on the feature connector.

**Bit 7**      CRT Interrupt—A logical 1 indicates video is being displayed on the CRT screen; a logical 0 indicates that vertical retrace is occurring.

### Input Status Register One

This is a read-only register. The processor port address is hex 3BA or hex 3DA.



- Bit 0**      **Display Enable**—Logical 0 indicates the CRT raster is in a horizontal or vertical retrace interval. This bit is the real time status of the display enable signal. Some programs use this status bit to restrict screen updates to inactive display intervals. The Enhanced Graphics Adapter does not require the CPU to update the screen buffer during inactive display intervals to avoid glitches in the display image.
- Bit 1**      **Light Pen Strobe**—A logical 0 indicates that the light pen trigger has not been set; a logical 1 indicates that the light pen trigger has been set.
- Bit 2**      **Light Pen Switch**—A logical 0 indicates that the light pen switch is closed; a logical 1 indicates that the light pen switch is open.
- Bit 3**      **Vertical Retrace**—A logical 0 indicates that video information is being displayed on the CRT screen; a logical 1 indicates the CRT is in a vertical retrace interval. This bit can be programmed to interrupt the processor on interrupt level 2 at the start of the vertical retrace. This is done through bits 4 and 5 of the Vertical Retrace End Register of the CRTC.
- Bits 4 and 5**      **Diagnostic Usage**—These bits are selectively connected to two of the six color outputs of the



Attribute Controller. The Color Plane Enable register controls the multiplexer for the video wiring. The following table illustrates the combinations available and the color output wiring.

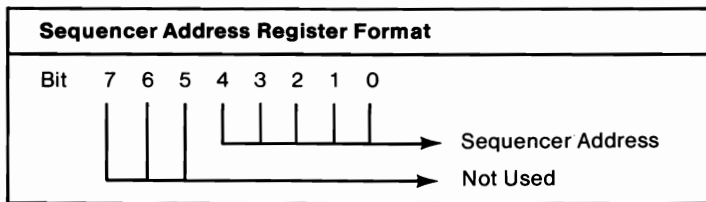
Color Plane Register		Input Status Register One	
Bit 5	Bit 4	Bit 5	Bit 4
0	0	Red	Blue
0	1	Secondary Blue	Green
1	0	Secondary Red	Secondary Green
1	1	Not Used	Not Used

# Sequencer Registers

Name	Port	Index
Address	3C4	-
Reset	3C5	00
Clocking Mode	3C5	01
Map Mask	3C5	02
Character Map Select	3C5	03
Memory Mode	3C5	04

## Sequencer Address Register

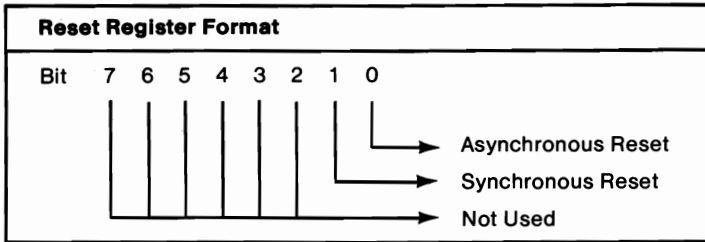
The Address Register is a pointer register located at address hex 3C4. This register is loaded with a binary value that points to the sequencer data register where data is to be written. This value is referred to as "Index" in the table above.



**Bit 0-Bit 3** Sequencer Address Bits—A binary value pointing to the register where data is to be written.

## Reset Register

This is a write-only register pointed to when the value in the address register is hex 00. The output port address for this register is hex 3C5.

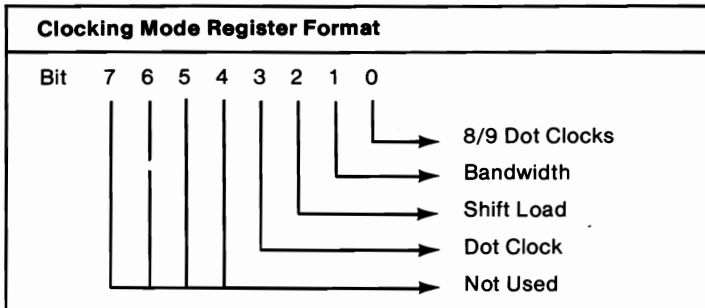


**Bit 0**      Asynchronous Reset—A logical 0 commands the sequencer to asynchronous clear and halt. All outputs are placed in the high impedance state when this bit is a 0. A logical 1 commands the sequencer to run unless bit 1 is set to zero. Resetting the sequencer with this bit can cause data loss in the dynamic RAMs.

**Bit 1**      Synchronous Reset—A logical 0 commands the sequencer to synchronous clear and halt. Bits 1 and 0 must both be ones to allow the sequencer to operate. Reset the sequencer with this bit before changing the Clocking Mode Register, if memory contents are to be preserved.

### Clocking Mode Register

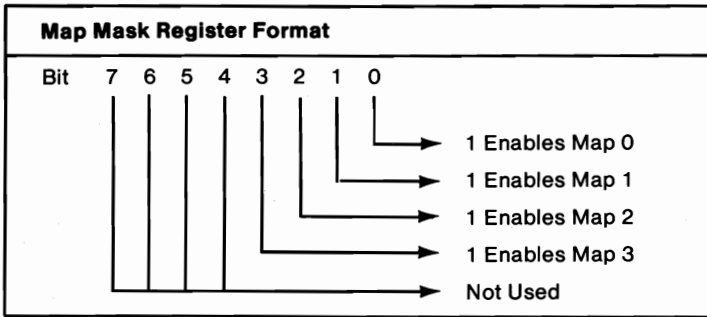
This is a write-only register pointed to when the value in the address register is hex 01. The output port address for this register is hex 3C5.



- Bit 0**      8/9 Dot Clocks—A logical 0 directs the sequencer to generate character clocks 9 dots wide; a logical 1 directs the sequencer to generate character clocks 8 dots wide. Monochrome alphanumeric mode (07H) is the only mode that uses character clocks 9 dots wide. All other modes must use 8 dots per character clock.
- Bit 1**      Bandwidth—A logical 0 makes CRT memory cycles occur on 4 out of 5 available memory cycles; a logical 1 makes CRT memory cycles occur on 2 out of 5 available memory cycles. Medium resolution modes require less data to be fetched from the display buffer during the horizontal scan time. This allows the CPU greater access time to the display buffer. All high resolution modes must provide the CRTC with 4 out of 5 memory cycles in order to refresh the display image.
- Bit 2**      Shift Load—When set to 0, the video serializers are reloaded every character clock; when set to 1, the video serializers are loaded every other character clock. This mode is useful when 16 bits are fetched per cycle and chained together in the shift registers.
- Bit 3**      Dot Clock—A logical 0 selects normal dot clocks derived from the sequencer master clock input. When this bit is set to 1, the master clock will be divided by 2 to generate the dot clock. All the other timings will be stretched since they are derived from the dot clock. Dot clock divided by two is used for 320x200 modes (0, 1, 4, 5) to provide a pixel rate of 7 MHz, (9 MHz for mode D).

### **Map Mask Register**

This is a write-only register pointed to when the value in the address register is hex 02. The output port address for this register is hex 3C5.

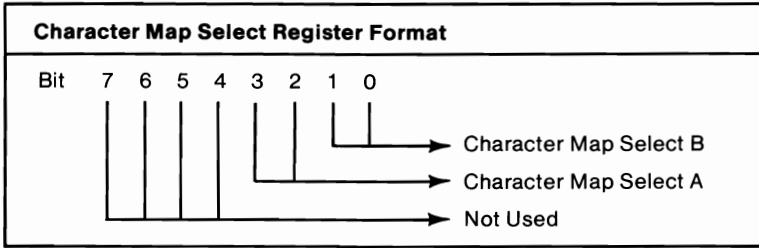


### Bit 0–Bit 3

**Map Mask**—A logical 1 in bits 3 through 0 enables the processor to write to the corresponding maps 3 through 0. If this register is programmed with a value of 0FH, the CPU can perform a 32-bit write operation with only one memory cycle. This substantially reduces the overhead on the CPU during display update cycles in graphics modes. Data scrolling operations are also enhanced by setting this register to a value of 0FH and writing the display buffer address with the data stored in the CPU data latches. This is a read-modify-write operation. When odd/even modes are selected, maps 0 and 1 and maps 2 and 3 should have the same map mask value.

### Character Map Select Register

This is a write-only register pointed to when the value in the address register is hex 03. The output port address for this register is 3C5.



**Bit 0–Bit 1** Character Map Select B—Selects the map used to generate alpha characters when attribute bit 3 is a 0, according to the following table:

Bits		Map Selected	Table Location
1	0		
Value			
0	0	0	1st 8K of Plane 2 Bank 0
0	1	1	2nd 8K of Plane 2 Bank 1
1	0	2	3rd 8K of Plane 2 Bank 2
1	1	3	4th 8K of Plane 2 Bank 3

**Bit 2–Bit 3** Character Map Select A—Selects the map used to generate alpha characters when attribute bit 3 is a 1, according to the following table:

Bits		Map Selected	Table Location
3	2		
Value			
0	0	0	1st 8K of Plane 2 Bank 0
0	1	1	2nd 8K of Plane 2 Bank 1
1	0	2	3rd 8K of Plane 2 Bank 2
1	1	3	4th 8K of Plane 2 Bank 3

In alphanumeric modes, bit 3 of the attribute byte normally has the function of turning the foreground intensity on or off. This bit however may be redefined as a switch between character sets. This function is enabled when there is a difference between the value in Character Map Select A and the value in Character Map Select B. Whenever these two values are the same, the character select function is disabled. The memory mode register bit 1 must be a 1 (indicates the memory extension card is installed in the unit) to enable this function; otherwise, bank 0 is always selected.



## CRT Controller Registers

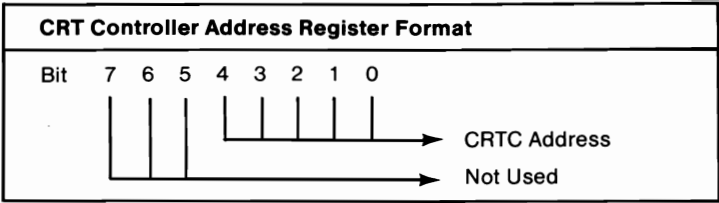
Name	Port	Index
Address Register	3?4	-
Horizontal Total	3?5	00
Horizontal Display End	3?5	01
Start Horizontal Blank	3?5	02
End Horizontal Blank	3?5	03
Start Horizontal Retrace	3?5	04
End Horizontal Retrace	3?5	05
Vertical Total	3?5	06
Overflow	3?5	07
Preset Row Scan	3?5	08
Max Scan Line	3?5	09
Cursor Start	3?5	0A
Cursor End	3?5	0B
Start Address High	3?5	0C
Start Address Low	3?5	0D
Cursor Location High	3?5	0E
Cursor Location Low	3?5	0F
Vertical Retrace Start	3?5	10
Light Pen High	3?5	10
Vertical Retrace End	3?5	11
Light Pen Low	3?5	11
Vertical Display End	3?5	12
Offset	3?5	13
Underline Location	3?5	14
Start Vertical Blank	3?5	15
End Vertical Blank	3?5	16
Mode Control	3?5	17
Line Compare	3?5	18

? = B in Monochrome Modes and D in Color Modes

### CRT Controller Address Register

The Address register is a pointer register located at hex 3B4 or hex 3D4. If an IBM Monochrome Display is attached to the adapter, address 3B4 is used. If a color display is attached to the adapter, address 3D4 is used. This register is loaded with a binary value that points to the CRT Controller data register where data is to be written. This value is referred to as "Index" in the table above.

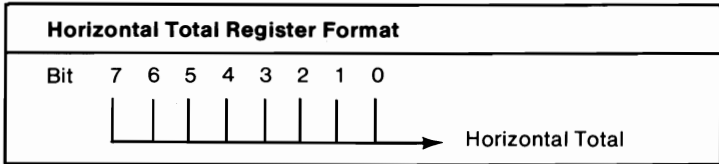




**Bit 0–Bit 4** CRT Controller Address Bits—A binary value pointing to the CRT Controller register where data is to be written.

### Horizontal Total Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 00. The processor output port address for this register is hex 3B5 or hex 3D5.

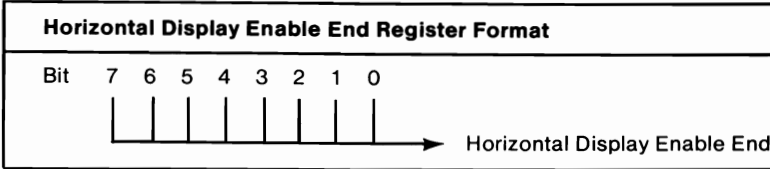


This register defines the total number of characters in the horizontal scan interval including the retrace time. The value directly controls the period of the horizontal retrace output signal. An internal horizontal character counter counts character clock inputs to the CRT Controller, and all horizontal and vertical timings are based upon the horizontal register. Comparators are used to compare register values with horizontal character values to provide horizontal timings.

**Bit 0–Bit 7** Horizontal Total—The total number of characters less 2.

## Horizontal Display Enable End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 01. The processor output port address for this register is hex 3B5 or hex 3D5.

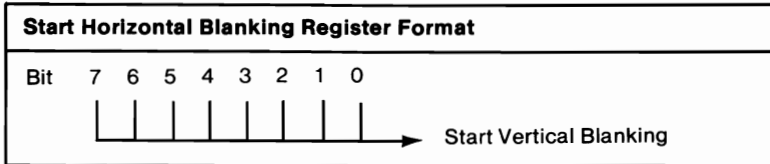


This register defines the length of the horizontal display enable signal. It determines the number of displayed character positions per horizontal line.

**Bit 0–Bit 7** Horizontal display enable end —A value one less than the total number of displayed characters.

## Start Horizontal Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 02. The processor output port address for this register is hex 3B5 or hex 3D5.

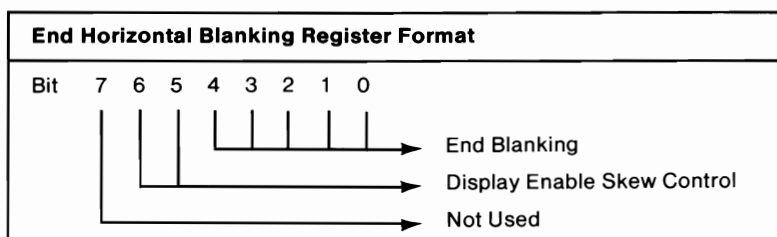


This register determines when the horizontal blanking output signal becomes active. The row scan address and underline scan line decode outputs are multiplexed on the memory address outputs and cursor outputs respectively during the blanking interval. These outputs are latched external to the CRT Controller with the falling edge of the BLANK output signal. The row scan address and underline signals remain on the output signals for one character count beyond the end of the blanking signal.

**Bit 0–Bit 7** Start Horizontal Blanking—The horizontal blanking signal becomes active when the horizontal character counter reaches this value.

### End Horizontal Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 03. The processor output port address for this register is hex 3B5 or hex 3D5.



This register determines when the horizontal blanking output signal becomes inactive. The row scan address and underline scan line decode outputs are multiplexed on the memory address outputs and the cursor outputs respectively during the blanking interval. These outputs are latched external to the CRT Controller with the falling edge of the BLANK output signal. The row scan address and underline signals remain on the output signals for one character count beyond the end of the blanking signal.

**Bit 0–Bit 4** End Horizontal Blanking—A value equal to the five least significant bits of the horizontal character counter value at which time the horizontal blanking signal becomes inactive (logical 0). To obtain a blanking signal of width  $W$ , the following algorithm is used: Value of Start Blanking Register + Width of Blanking signal in character clock units = 5-bit result to be programmed into the End Horizontal Blanking Register.

**Bit 5–Bit 6**

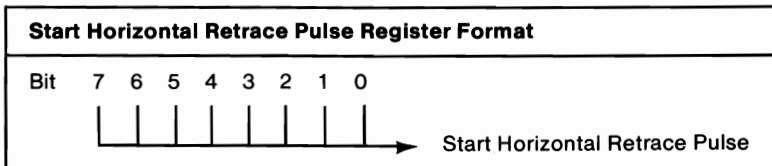
Display Enable Skew Control—These two bits determine the amount of display enable skew. Display enable skew control is required to provide sufficient time for the CRT Controller to access the display buffer to obtain a character and attribute code, access the character generator font, and then go through the Horizontal Pel Panning Register in the Attribute Controller. Each access requires the display enable signal to be skewed one character clock unit so that the video output is in synchronization with the horizontal and vertical retrace signals. The bit values and amount of skew are shown in the following table:

**Bits****6 5**

<b>0 0</b>	Zero character clock skew
<b>0 1</b>	One character clock skew
<b>1 0</b>	Two character clock skew
<b>1 1</b>	Three character clock skew

**Start Horizontal Retrace Pulse Register**

This is a write-only register pointed to when the value in the CRT Controller address register is hex 04. The processor output port address for this register is hex 3B5 or hex 3D5.

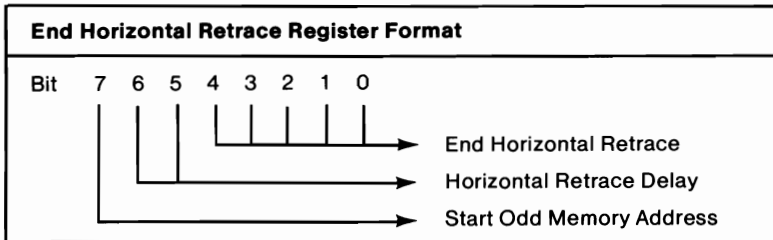


This register is used to center the screen horizontally, and to specify the character position at which the Horizontal Retrace Pulse becomes active.

**Bit 0–Bit 7** Start Horizontal Retrace Pulse—The value programmed is a binary count of the character position number at which the signal becomes active.

## End Horizontal Retrace Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 05. The processor output port address for this register is hex 3B5 or hex 3D5.



This register specifies the character position at which the Horizontal Retrace Pulse becomes inactive (logical 0).

**Bit 0–Bit 4** End Horizontal Retrace—A value equal to the five least significant bits of the horizontal character counter value at which time the horizontal retrace signal becomes inactive (logical 0). To obtain a retrace signal of width  $W$ , the following algorithm is used: Value of Start Retrace Register + width of horizontal retrace signal in character clock units = 5-bit result to be programmed into the End Horizontal Retrace Register.

**Bit 5–Bit 6** Horizontal Retrace Delay—These bits control the skew of the horizontal retrace signal. Binary 00 equals no Horizontal Retrace Delay. For some modes, it is necessary to provide a horizontal retrace signal that takes up the entire blanking interval. Some internal timings are generated by the falling edge of the horizontal retrace signal. To guarantee the signals are

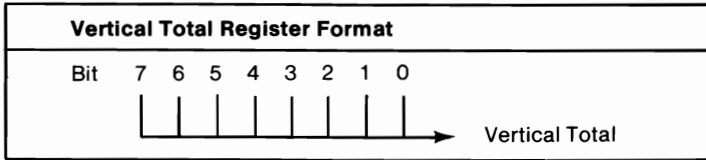
latched properly, the retrace signal is started before the end of the display enable signal, and then skewed several character clock times to provide the proper screen centering.

### Bit 7

**Start Odd/Even Memory Address**—This bit controls whether the first CRT memory address output after a horizontal retrace begins with an even or an odd address. A logical 0 selects even addresses; a logical 1 selects odd addresses. This bit is used for horizontal pel panning applications. Generally, this bit should be set to a logical 0.

## Vertical Total Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 06. The processor output port address for this register is hex 3B5 or 3D5.

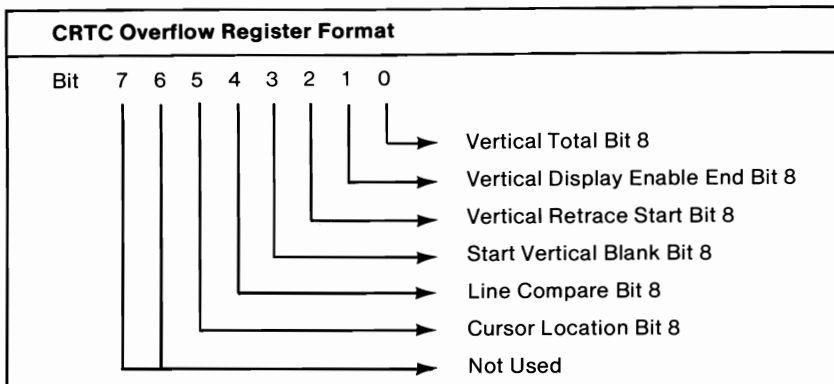


### Bit 0–Bit 7

**Vertical Total**—This is the low-order eight bits of a nine-bit register. The binary value represents the number of horizontal raster scans on the CRT screen, including vertical retrace. The value in this register determines the period of the vertical retrace signal. Bit 8 of this register is contained in the CRT Controller Overflow Register hex 07 bit 0.

## CRT Controller Overflow Register

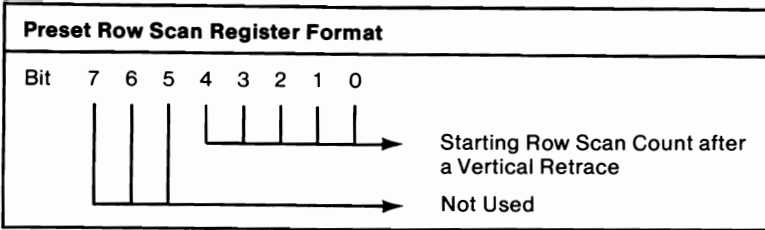
This is a write-only register pointed to when the value in the CRT Controller Address Register is hex 07. The processor output port address for this register is hex 3B5 or hex 3D5.



- Bit 0** Vertical Total—Bit 8 of the Vertical Total register (index hex 06).
- Bit 1** Vertical Display Enable End—Bit 8 of the Vertical Display Enable End register (index hex 12).
- Bit 2** Vertical Retrace Start—Bit 8 of the Vertical Retrace Start register (index hex 10).
- Bit 3** Start Vertical Blank—Bit 8 of the Start Vertical Blank register (index hex 15).
- Bit 4** Line Compare—Bit 8 of the Line Compare register (index hex 18).
- Bit 5** Cursor Location—Bit 8 of the Cursor Location register (index hex 0A).

### Preset Row Scan Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 08. The processor output port address for this register is hex 3B5 or hex 3D5.

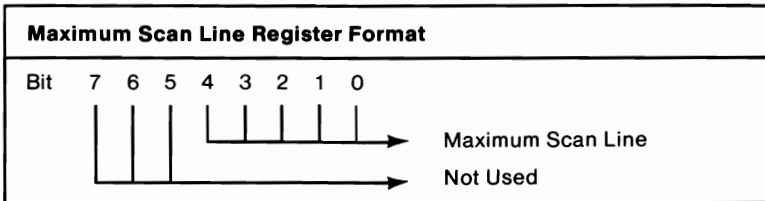


This register is used for pel scrolling.

**Bit 0–Bit 4** Preset Row Scan (Pel Scrolling)—This register specifies the starting row scan count after a vertical retrace. The row scan counter increments each horizontal retrace time until a maximum row scan occurs. At maximum row scan compare time the row scan is cleared (not preset).

### Maximum Scan Line Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 09. The processor output port address for this register is hex 3B5 or hex 3D5.



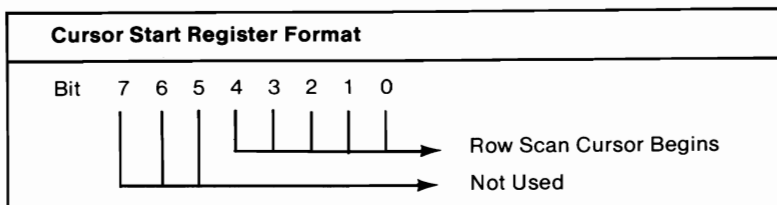
**Bit 0–Bit 4** Maximum Scan Line—This register specifies the number of scan lines per character row. The number to be programmed is the maximum row scan number minus one.

### Cursor Start Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 0A. The processor output port



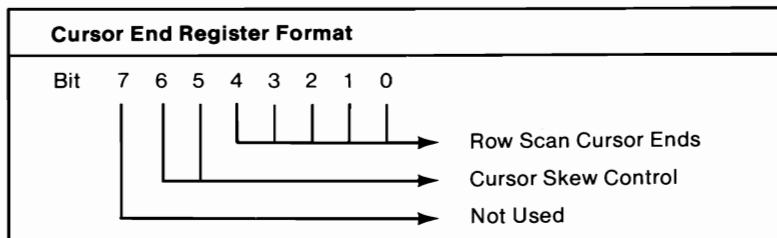
address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 4**      **Cursor Start**—This register specifies the row scan of a character line where the cursor is to begin. The number programmed should be one less than the starting cursor row scan.

### Cursor End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 0B. The processor output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 4**      **Cursor End**—These bits specify the row scan where the cursor is to end.

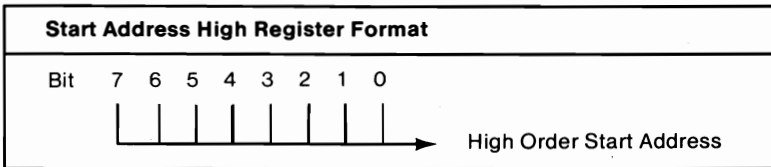
**Bit 5-Bit 6**      **Cursor Skew**—These bits control the skew of the cursor signal.

**Bits****6 5**

<b>0 0</b>	Zero character clock skew
<b>0 1</b>	One character clock skew
<b>1 0</b>	Two character clock skew
<b>1 1</b>	Three character clock skew

**Start Address High Register**

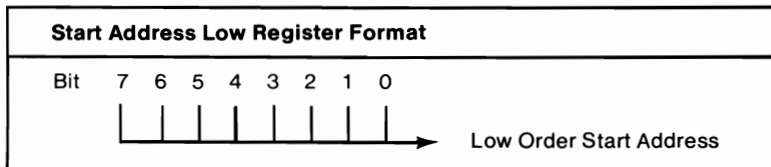
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0C. The processor input/output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7** Start Address High—These are the high-order eight bits of the start address. The 16-bit value, from the high-order and low-order start address registers, is the first address after the vertical retrace on each screen refresh.

**Start Address Low Register**

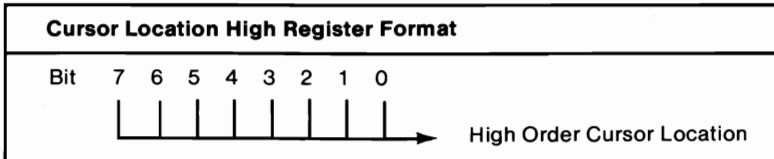
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0D. The processor input/output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7** Start Address Low—These are the low-order 8 bits of the start address.

### Cursor Location High Register

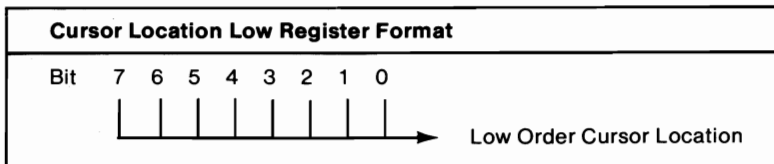
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0E. The processor input/output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7** Cursor Location High—These are the high-order 8 bits of the cursor location.

### Cursor Location Low Register

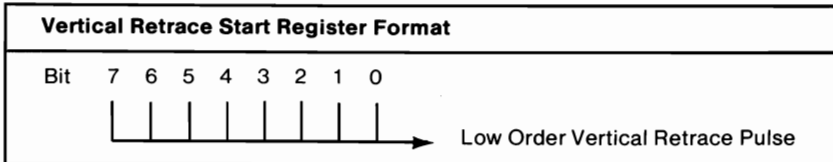
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0F. The processor input/output port address for this register is hex 3B5 or Hex 3D5.



**Bit 0-Bit 7** Cursor Location Low— These are the low-order 8 bits of the cursor location.

## Vertical Retrace Start Register

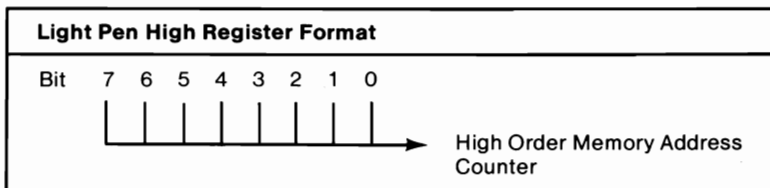
This is a write-only register pointed to when the value in the CRT Controller address register is hex 10. The processor output port address for this register is hex 3B5 or hex 3D5.



**Bit 0–Bit 7** Vertical Retrace Start—This is the low-order 8 bits of the vertical retrace pulse start position programmed in horizontal scan lines. Bit 8 is in the overflow register location hex 07.

## Light Pen High Register

This is a read-only register pointed to when the value in the CRT Controller address register is hex 10. The processor input port address for this register is hex 3B5 or hex 3D5.

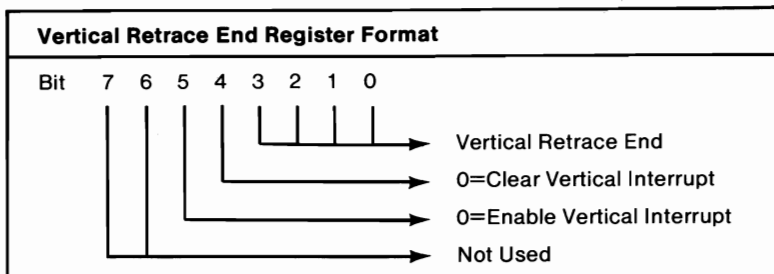


**Bit 0–Bit 7** Light Pen High—This is the high order 8 bits of the memory address counter at the time the light pen was triggered.

## Vertical Retrace End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 11. The processor output port

address for this register is hex 3B5 or hex 3D5.



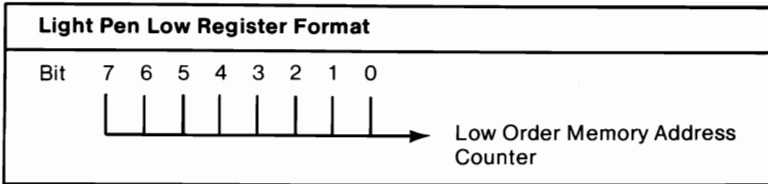
**Bit 0–Bit 3**      **Vertical Retrace End**—These bits determine the horizontal scan count value when the vertical retrace output signal becomes inactive. The register is programmed in units of horizontal scan lines. To obtain a vertical retrace signal of width  $W$ , the following algorithm is used: Value of Start Vertical Retrace Register + width of vertical retrace signal in horizontal scan units = 4-bit result to be programmed into the End Horizontal Retrace Register.

**Bit 4**              **Clear Vertical Interrupt**—A logical 0 will clear a vertical interrupt.

**Bit 5**              **Enable Vertical Interrupt**—A logical 0 will enable vertical interrupt.

### Light Pen Low Register

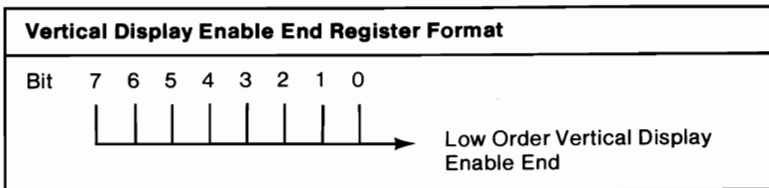
This is a read-only register pointed to when the value in the CRT Controller address register is hex 11. The processor input port address for this register is hex 3B5 or 3D5.



**Bit 0–Bit 7** Light Pen Low—This is the low-order 8 bits of the memory address counter at the time the light pen was triggered.

### Vertical Display Enable End Register

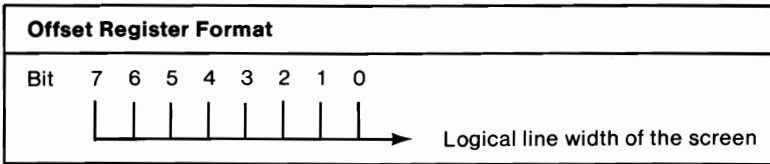
This is a write-only register pointed to when the value in the CRT Controller address register is hex 12. The processor output port address for this register is hex 3B5 or hex 3D5.



**Bit 0–Bit 7** Vertical Display Enable End—These are the low-order 8 bits of the vertical display enable end position. This address specifies which scan line ends the active video area of the screen. Bit 8 is in the overflow register location hex 07.

### Offset Register

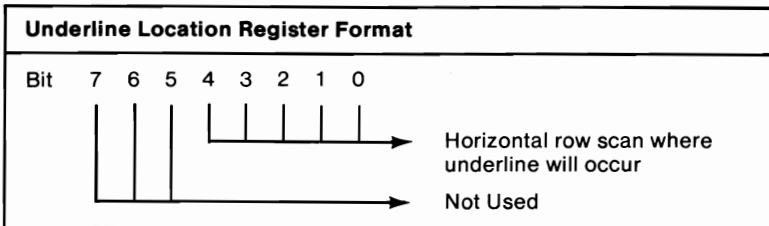
This is a write-only register pointed to when the value in the CRT Controller address register is hex 13. The processor output port address for this register is hex 3B5 or hex 3D5.



**Bit 0-Bit 7** Offset—This register specifies the logical line width of the screen. The starting memory address for the next character row is larger than the current character row by this amount. The Offset Register is programmed with a word address. Depending upon the method of clocking the CRT Controller, this word address is either a word or double word address.

### Underline Location Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 14. The processor output port address for this register is hex 3B5 or hex 3D5.

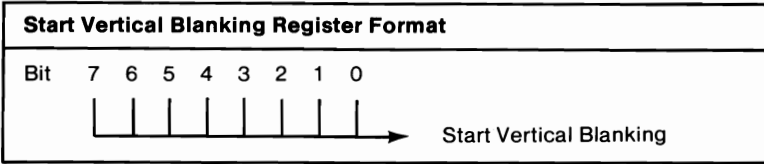


**Bit 0-Bit 4** Underline Location—This register specifies the horizontal row scan on which underline will occur. The value programmed is one less than the scan line number desired.

### Start Vertical Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 15. The processor output port

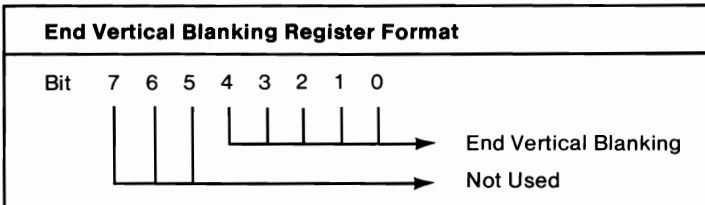
address for this register is hex 3B5 or hex 3D5.



**Bit 0–Bit 7** Start Vertical Blank—These are the low 8 bits of the horizontal scan line count, at which the vertical blanking signal becomes active. Bit 8 bit is in the overflow register hex 07.

### End Vertical Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 16. The processor output port address for this register is hex 3B5 or hex 3D5.

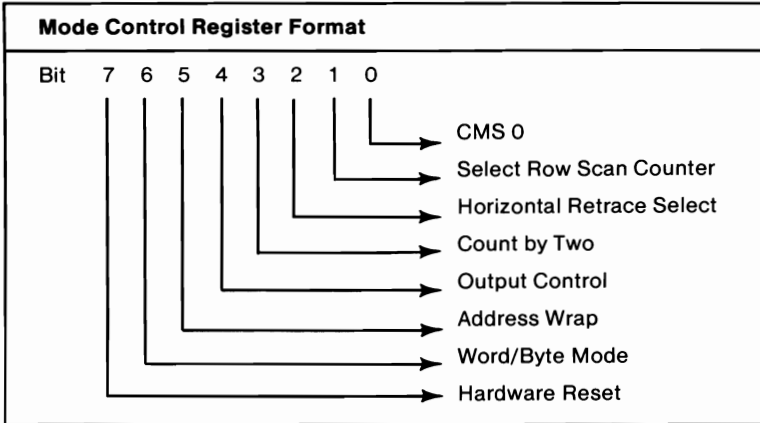


**Bit 0–Bit 4** End Vertical Blank—This register specifies the horizontal scan count value when the vertical blank output signal becomes inactive. The register is programmed in units of horizontal scan lines. To obtain a vertical blank signal of width  $W$ , the following algorithm is used: Value of Start Vertical Blank Register + width of vertical blank signal in horizontal scan units = 5-bit result to be programmed into the End Vertical Blank Register.



## Mode Control Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 17. The processor output port address for this register is hex 3B5 or hex 3D5.



### Bit 0

**Compatibility Mode Support**— When this bit is a logical 0, the row scan address bit 0 is substituted for memory address bit 13 during active display time. A logical 1 enables memory address bit 13 to appear on the memory address output bit 13 signal of the CRT Controller. The CRT Controller used on the IBM Color/Graphics Monitor Adapter is the 6845. The 6845 has 128 horizontal scan line address capability. To obtain 640 by 200 graphics resolution, the CRTC was programmed for 100 horizontal scan lines with 2 row scan addresses per character row. Row scan address bit 0 became the most significant address bit to the display buffer. Successive scan lines of the display image were displaced in memory by 8K bytes. This bit allows compatibility with the 6845 and Color Graphics APA modes of operation.

- Bit 1**            Select Row Scan Counter—A logical 0 selects row scan counter bit 1 on MA 14 output pin. A logical 1 selects MA 14 counter bit on MA 14 output pin.
- Bit 2**            Horizontal Retrace Select—This bit selects Horizontal Retrace or Horizontal Retrace divided by 2 as the clock that controls the vertical timing counter. This bit can be used to effectively double the vertical resolution capability of the CRT Controller. The vertical counter has a maximum resolution of 512 scan lines due to the 9-bit wide Vertical Total Register. If the vertical counter is clocked with the horizontal retrace divided by 2 clock, then the vertical resolution is doubled to 1024 horizontal scan lines. A logical 0 selects HRTC and a logical 1 selects HRTC divided by 2.
- Bit 3**            Count By Two— When this bit is set to 0, the memory address counter is clocked with the character clock input. A logical 1 clocks the memory address counter with the character clock input divided by 2. This bit is used to create either a byte or word refresh address for the display buffer.
- Bit 4**            Output Control—A logical 0 enables the module output drivers. A logical 1 forces all outputs into high impedance state.
- Bit 5**            Address Wrap—This bit selects Memory Address counter bit MA 13 or bit MA 15, and it appears on the MA 0 output pin in the word address mode. If you are not in the word address mode, MA 0 counter output appears on the MA 0 output pin. A logical 1 selects MA 15. In odd/even mode, bit MA 13 should be selected when the 64K memory is installed on the board. Bit MA 15 should be selected when greater than 64K memory is installed. This function is used to implement Color Graphics Monitor Adapter compatibility.

**Bit 6**

**Word Mode or Byte Mode**—When this bit is a logical 0, the Word Mode shifts all memory address counter bits down one bit, and the most significant bit of the counter appears on the least significant bit of the memory address outputs. See table below for address output details. A logical 1 selects the Byte Address mode.

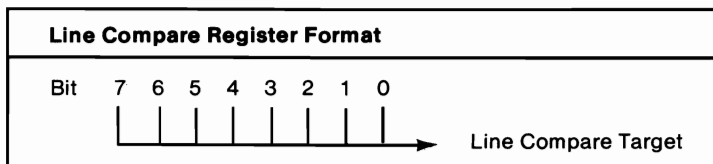
Internal Memory Address Counter Wiring to the Output Multiplexer		
CRTC Out Pin	Byte Address Mode	Word Address Mode
MA 0/RFA 0	MA 0	MA 15 or MA 13
MA 1/RFA 1	MA 1	MA 0
MA 2/RFA 2	MA 2	MA 1
MA 3/RFA 3	MA 3	MA 2
*	*	*
*	*	*
*	*	*
MA 14/RS 3	MA 14	MA 13
MA 15/RS 4	MA 15	MA 14

**Bit 7**

**Hardware Reset**—A logical 0 forces horizontal and vertical retrace to clear. A logical 1 forces horizontal and vertical retrace to be enabled.

**Line Compare Register**

This is a write-only register pointed to when the value in the CRT Controller address register is hex 18. The processor output port address for this register is hex 3B5 or hex 3D5.

**Bit 0–Bit 7**

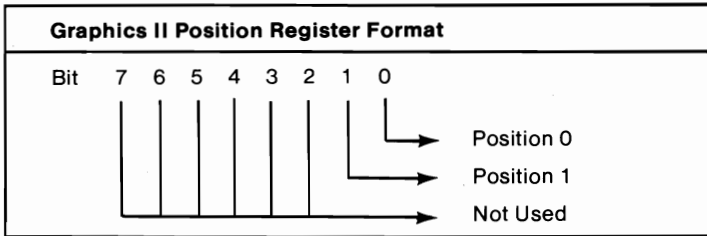
**Line Compare**—This register is the low-order 8 bits of the compare target. When the vertical

counter reaches this value, the internal start of the line counter is cleared. This allows an area of the screen to be immune to scrolling. Bit 8 of this register is in the overflow register hex 07.



## Graphics 2 Position Register

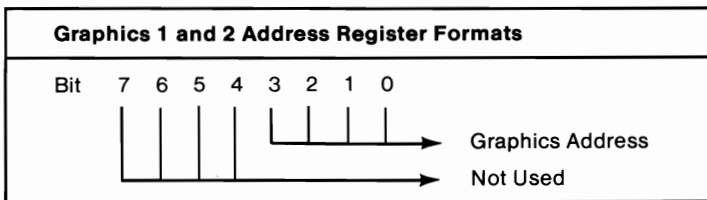
This is a write-only register. The processor output port address for this register is hex 3CA.



**Bit 0–Bit 1** Position—These 2 bits are binary encoded hierarchy bits for the graphics chips. The position register controls which 2 bits of the processor data bus to which each chip responds. Graphics 2 must be programmed with a position register value of 1 for this card.

## Graphics 1 and 2 Address Register

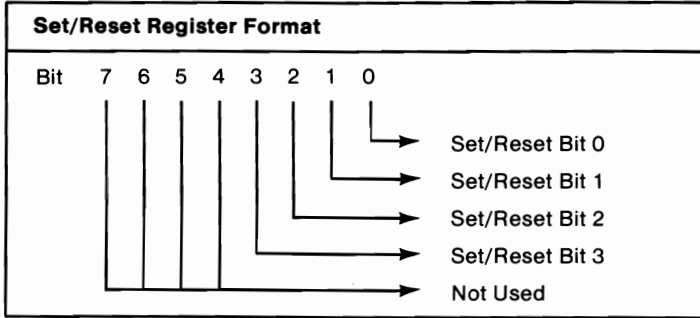
This is a write-only register and the processor output port address for this register is hex 3CE.



**Bit 0–Bit 3** Graphics 1 and 2 Address Bits—This output loads the address register in both graphics chips simultaneously. This register points to the data register of the graphics chips.

## Set/Reset Register

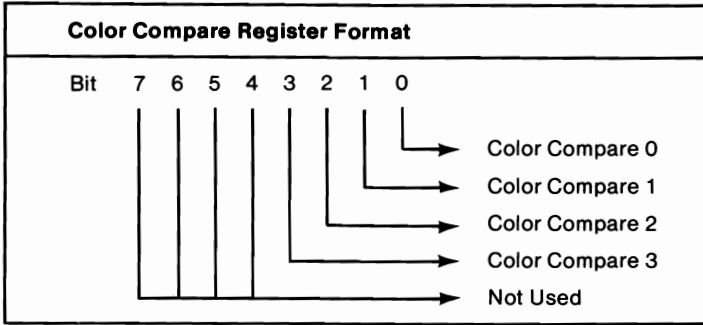
This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 00 before writing can take place. The processor output port address for this register is hex 3CF.



**Bit 0–Bit 3** Set/Reset—These bits represent the value written to the respective memory planes when the processor does a memory write with write mode 0 selected and set/reset mode is enabled. Set/Reset can be enabled on a plane by plane basis with separate OUT commands to the Set/Reset register.

## Enable Set/Reset Register

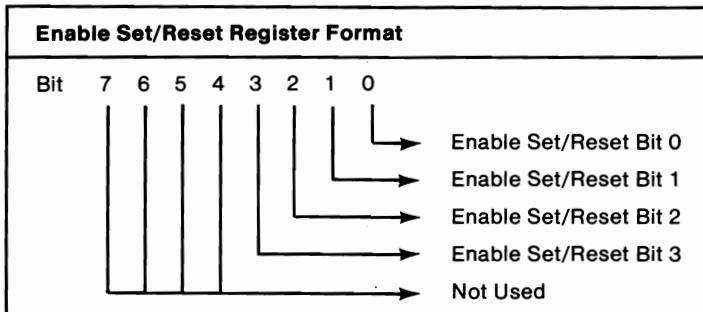
This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 01 before writing can take place. The processor output port for this register is hex 3CF.



**Bit 0-Bit 3** Enable Set/Reset—These bits enable the set/reset function. The respective memory plane is written with the value of the Set/Reset register provided the write mode is 0. When write mode is 0 and Set/Reset is not enabled on a plane, that plane is written with the value of the processor data.

### Color Compare Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 02 before writing can take place. The processor output port address for this register is hex 3CF.



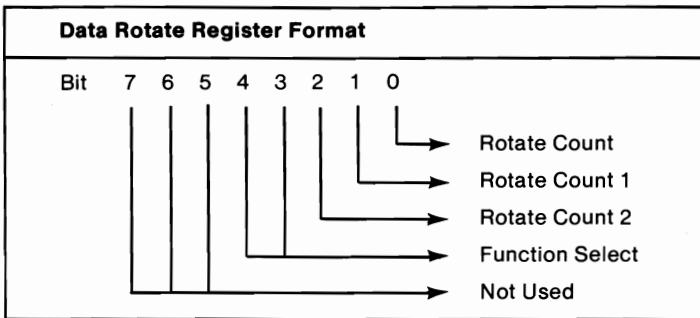
**Bit 0-Bit 3** Color Compare—These bits represent a 4 bit color value to be compared. If the processor sets



read mode 1 on the graphics chips, and does a memory read, the data returned from the memory cycle will be a 1 in each bit position where the 4 bit planes equal the color compare register.

## Data Rotate Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 03 before writing can take place. The processor output port address for this register is hex 3CF.



**Bit 0–Bit 2** Rotate Count—These bits represent a binary encoded value of the number of positions to rotate the processor data bus during processor memory writes. This operation is done when the write mode is 0. To write unrotated data the processor must select a count of 0.

**Bit 3–Bit 4** Function Select—Data written to memory can operate logically with data already in the processor latches. The bit functions are defined in the following table.

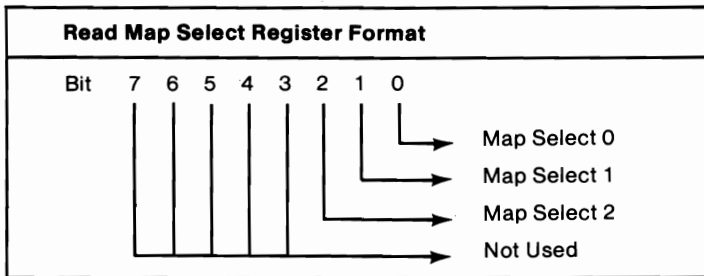
**Bits****4 3**

- 
- 0 0** Data unmodified.
  - 0 1** Data AND'ed with latched data.
  - 1 0** Data OR'ed with latched data.
  - 1 1** Data XOR'ed with latched data.

Data may be any of the choices selected by the Write Mode Register except processor latches. If rotated data is selected, the rotate applies before the logical function.

### Read Map Select Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 04 before writing can take place. The processor output port address for this register is hex 3CF.

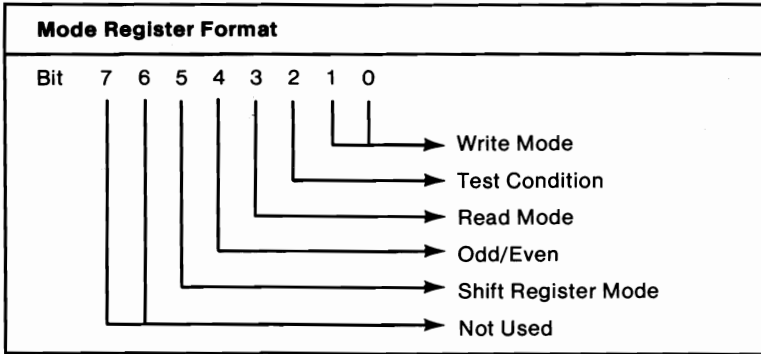


**Bit 0–Bit 2** Map Select—These bits represent a binary encoded value of the memory plane number from which the processor reads data. This register has no effect on the color compare read mode described elsewhere in this section.

### Mode Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 05

before writing can take place. The processor output port address for this register is 3CF.



**Bit 0–Bit 1** Write Mode

**Bits**  
**1 0**

- |            |  |
|------------|--|
| <b>0 0</b> | Each memory plane is written with the processor data rotated by the number of counts in the rotate register, unless Set/Reset is enabled for the plane. Planes for which Set/Reset is enabled are written with 8 bits of the value contained in the Set/Reset register for that plane. |
| <b>0 1</b> | Each memory plane is written with the contents of the processor latches. These latches are loaded by a processor read operation.   |
| <b>1 0</b> | Memory plane <i>n</i> (0 through 3) is filled with 8 bits of the value of data bit <i>n</i> .  |
| <b>1 1</b> | Not Valid  |

The logic function specified by the function select register also applies.

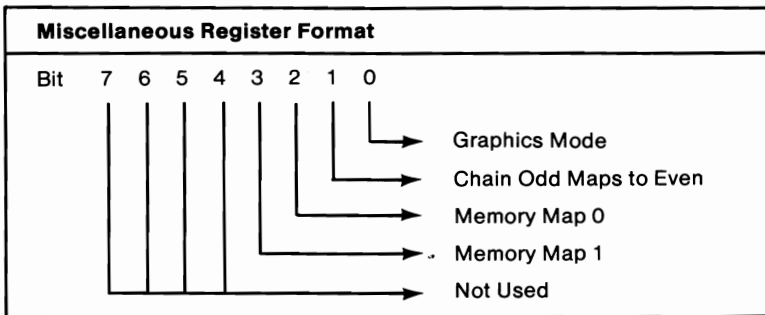
**Bit 2**

**Test Condition**—A logical 1 directs graphics controller outputs to be placed in high impedance state for testing.

- Bit 3** Read Mode—When this bit is a logical 0, the processor reads data from the memory plane selected by the read map select register. When this bit is a logical 1, the processor reads the results of the comparison of the 4 memory planes and the color compare register.
- Bit 4** Odd/Even—A logical 1 selects the odd/even addressing mode, which is useful for emulation of the Color Graphics Monitor Adapter compatible modes. Normally the value here follows the value of the Memory Mode Register bit 3 of the Sequencer.
- Bit 5** Shift Register—A logical 1 directs the shift registers on each graphics chip to format the serial data stream with even numbered bits on the even numbered maps and odd numbered bits on the odd maps.

### Miscellaneous Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 06 before writing can take place. The processor output port for this register is hex 3CF.



- Bit 0** Graphics Mode—This bit controls alpha-mode addressing. A logical 1 selects graphics mode. When set to graphics mode, the character generator address latches are disabled.
- Bit 1** Chain Odd Maps To Even Maps—When set to 1, this bit directs the processor address bit 0 to be replaced by a higher order bit and odd/even maps to be selected with odd/even values of the processor A0 bit, respectively.
- Bit 2-Bit 3** Memory Map—These bits control the mapping of the regenerative buffer into the processor address space.

**Bits**

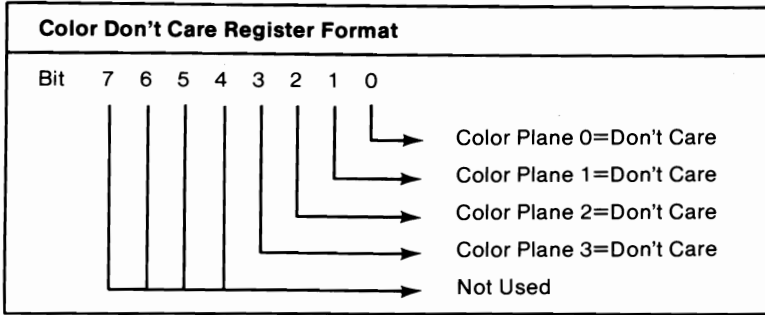
**3 2**

- 
- 0 0** Hex A000 for 128K bytes.
  - 0 1** Hex A000 for 64K bytes.
  - 1 0** Hex B000 for 32K bytes
  - 1 1** Hex B800 for 32K bytes.

If the display adapter is mapped at address hex A000 for 128K bytes, no other adapter can be installed in the system.

### **Color Don't Care Register**

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 07 before writing can take place. The processor output port for this register is hex 3CF.



**Bit 0** Color Don't Care—Color plane 0=don't care when reading color compare when this bit is set to 1.

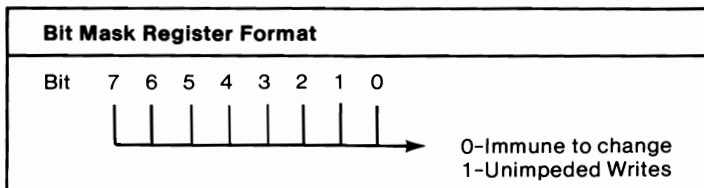
**Bit 1** Color Don't Care—Color plane 1=don't care when reading color compare when this bit is set to 1.

**Bit 2** Color Don't Care—Color plane 2=don't care when reading color compare when this bit is set to 1.

**Bit 3** Color Don't Care—Color plane 3=don't care when reading color compare when this bit is set to 1.

### Bit Mask Register

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 08 before writing can take place. The processor output port for this register is hex 3CF.



**Bit 0–Bit 7**

**Bit Mask**—Any bit programmed to  $n$  causes the corresponding bit  $n$  in each bit plane to be immune to change provided that the location being written was the last location read by the processor. Bits programmed to a 1 allow unimpeded writes to the corresponding bits in the bit planes.

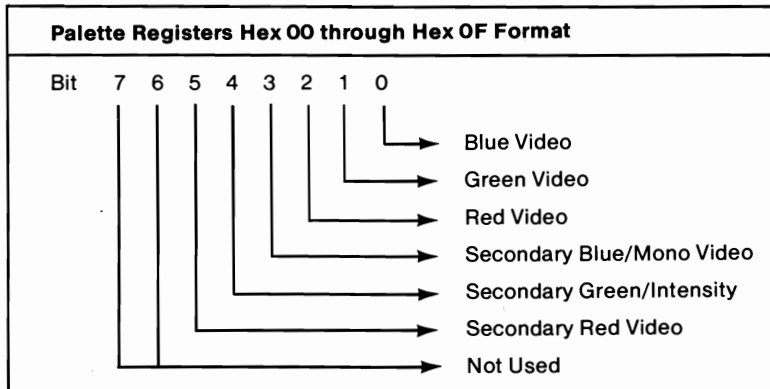
The bit mask applies to any data written by the processor (rotate, AND'ed, OR'ed, XOR'ed, DX, and S/R). To preserve bits using the bit mask, data must be latched internally by reading the location. When data is written to preserve the bits, the most current data in latches is written in those positions. The bit mask applies to all bit planes simultaneously.

## Attribute Controller Registers

Name	Port	Index
Address Register	3C0	-
Palette Registers	3C0	00-0F
Mode Control Register	3C0	10
Overscan Color Register	3C0	11
Color Plane Enable Register	3C0	12
Horizontal Pel Panning Register	3C0	13

### Attribute Address Register

This is a write-only register. The processor output port is hex 3C0.



#### Bit 0–Bit 4

**Attribute Address Bits**—The Address Register is a pointer register located at hex 3C0. This register is loaded with a binary value that points to the attribute data register where data is to be written. The Attribute Controller does not have an address bit input to control selection of the address and data registers. An internal address flip-flop controls selection of either the address or data registers. To initialize the flip-flop, an IOR instruction is issued to the Attribute Controller at address 3BA or 3DA. This clears the flip-flop, and selects the Address Register. After the Address Register has been loaded, the

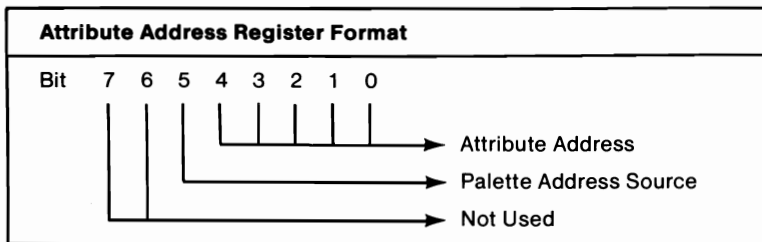


next OUT instruction loads the data register. The flip-flop toggles each time an OUT is issued to the Attribute Controller.

**Bit 5** Palette Address Source—When loading the color palette registers, bit 5 must be cleared to 0. To enable the memory data to access the color palette, bit 5 must be set to 1.

### Palette Register Hex 00 through Hex 0F

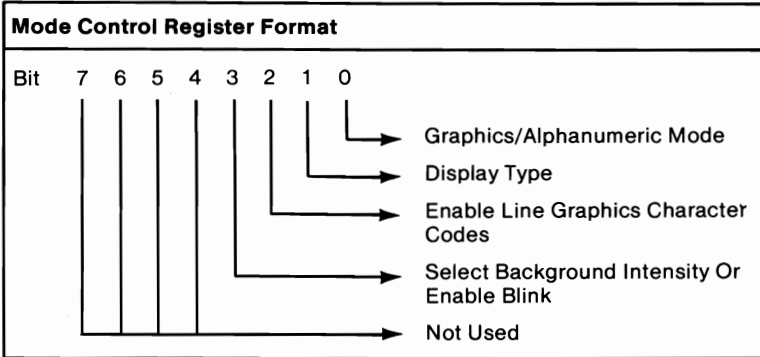
This is a write-only register. The processor output port is hex 3C0.



**Bit 0–Bit 5** Palette—These 6-bit registers allow a dynamic mapping between the text attribute or graphic color input value and the display color in the CRT screen. A logical 1 selects the appropriate color. A logical 0 de-selects. The color palette register should be modified only during the vertical retrace interval to avoid glitches in the displayed image. Note that some color monitors do not have an intensity input and only a maximum of eight colors are available. Monitors with four color inputs display sixteen colors, and monitors with six color inputs display 64 colors.

## Mode Control Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 10 before writing can take place. The processor output port address for this register is hex 3C0.



- Bit 0** Graphics/Alphanumeric Mode—A logical 0 selects alphanumeric mode. A logical 1 selects graphics mode.
- Bit 1** Monochrome Display/Color Display—A logical 0 selects color display attributes. A logical 1 selects IBM Monochrome Display attributes.
- Bit 2** Enable Line Graphics Character Codes—When this bit is set to 0, the ninth dot will be the same as the background. A logical 1 enables the special line graphics character codes for the IBM Monochrome Display adapter. This bit when enabled forces the ninth dot of a line graphic character to be identical to the eighth dot of the character. The line graphics character codes for the Monochrome Display Adapter are Hex C0 through Hex DF.

For character fonts that do not utilize the line graphics character codes in the range of Hex C0

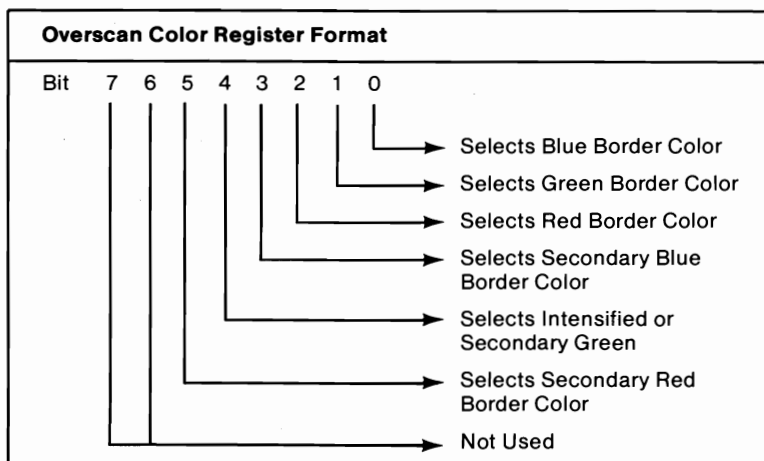
through Hex DF, bit 2 of this register should be a logical 0. Otherwise unwanted video information will be displayed on the CRT screen.

### Bit 3

**Enable Blink/Select Background Intensity**—A logical 0 selects the background intensity of the attribute input. This mode was available on the Monochrome and Color Graphics adapters. A logical 1 enables the blink attribute in alphanumeric modes. This bit must also be set to 1 for blinking graphics modes.

## Overscan Color Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 11 before writing can take place. The processor output port address for this register is hex 3C0.

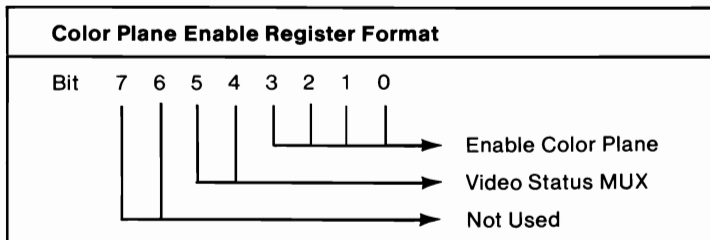


### Bit 0-Bit 5

**Overscan Color**—This 6-bit register determines the overscan (border) color displayed on the CRT screen. For monochrome display this register should be set to a value of 0. A logical 1 selects the appropriate color.

## Color Plane Enable Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 12 before writing can take place. The processor output port address for this register is 3C0.



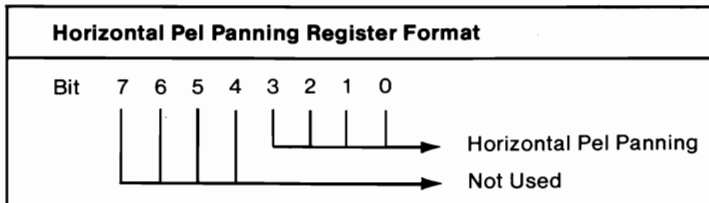
**Bit 0-Bit 3** Enable Color Plane—Writing a logical 1 in any of bits 0 through 3 enables the respective display memory color plane.

**Bit 4-Bit 5** Video Status MUX—Selects two of the six color outputs to be available on the status port. The following table illustrates the combinations available and the color output wiring.

COLOR PLANE ENABLE REGISTER		INPUT STATUS REGISTER ONE	
Bit 5	Bit 4	Bit 5	Bit 4
0	0	Red	Blue
0	1	Secondary Blue	Green
1	0	Secondary Red	Secondary Green
1	1	Not Used	Not Used

## Horizontal Pel Panning Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 12 before writing can take place. The processor output port address for this register is hex 3C0.



**Bit 0–Bit 3**

**Horizontal Pel Panning**—This 4 bit register selects the number of picture elements (pels) to shift the video data horizontally to the left. Pel panning is available in both A/N and APA modes. In Monochrome A/N mode, the image can be shifted a maximum of 9 pels. In all other A/N and APA modes, the image can be shifted a maximum of 8 pels. The sequence for shifting the image is given below:

9 pels/character : 8, 0, 1, 2, 3, 4, 5, 6, 7  
(Monochrome A/N mode only)

8 pels/character : 0, 1, 2, 3, 4, 5, 6, 7 (All other Modes)

# Programming Considerations

## Programming the Registers

Each of the LSI devices has an address register and a number of data registers. The address register serves as a pointer to the other registers on the LSI device. It is a write-only register that is loaded by the processor by executing an 'OUT' instruction to its I/O address with the index of the selected data register.

The data registers on each LSI device are accessed through a common I/O address. They are distinguished by the pointer (index) in the address register. To write to a data register, the address register is loaded with the index of the appropriate data register, then the selected data register is loaded by executing an 'OUT' instruction to the common I/O address.

The external registers that are not part of an LSI device and the Graphics I and II registers are not accessed through an address register; they are written to directly.

The following tables define the values that are loaded into the registers by BIOS to support the different modes of operation supported by this adapter.

Register			Mode of Operation																		
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*	
Miscellaneous	3C2	-	23	23	23	23	23	23	23	A6	23	23	A2	A7	A2	A7	A7	A7	A7	A7	A7
Feature Cntrl	3?A	-	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Input Stat 0	3C2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Input Stat 1	3?2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-

? = B in monochrome modes      ? = D in color modes

\*Values for these modes when the IBM Enhanced Color Display is attached

‡Values for these modes when greater than 64K Graphics Memory is installed

## External Registers

Register			Mode of Operation																		
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*	
Seq Address	3C4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Reset	3C5	00	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03
Clock Mode	3C5	01	0B	0B	01	01	0B	0B	01	00	0B	01	05	05	01	01	0B	0B	01	01	
Map Mask	3C5	02	03	03	03	03	03	03	01	03	0F	0F	0F	0F	0F	0F	03	03	03	03	
Char Gen Sel	3C5	03	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Memory Mode	3C5	04	03	03	03	03	02	02	06	03	06	06	00	00	06	06	03	03	03	03	

\*Values for these modes when the IBM Enhanced Color Display is attached

‡Values for these modes when greater than 64K Graphics Memory is installed

## Sequencer Registers

Register			Mode of Operation																							
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*						
Address Reg	3?4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-						
Horiz Total	3?5	00	37	37	70	70	37	37	70	60	37	70	60	5B	60	5B	2D	2D	5B	5B						
Hz Disp End	3?5	01	27	27	4F	4F	27	27	4F	4F	27	4F	4F	4F	4F	4F	27	27	4F	4F						
Strt Hz Blk	3?5	02	2D	2D	5C	5C	2D	2D	59	56	2D	56	56	53	56	53	2B	2B	53	53						
End Hz Blk	3?5	03	37	37	2F	2F	37	37	2D	3A	37	2D	1A	17	3A	37	2D	2D	37	37						
Strt Hz Retr	3?5	04	31	31	5F	5F	30	30	5E	51	30	5E	50	50	50	52	28	28	51	51						
End Hz Retr	3?5	05	15	15	07	07	14	14	06	60	14	06	E0	BA	60	00	6D	6D	5B	5B						
Vert Total	3?5	06	04	04	04	04	04	04	04	70	04	04	70	6C	70	6C	6C	6C	6C	6C						
Overflow	3?5	07	11	11	11	11	11	11	11	1F	11	11	1F	1F	1F	1F	1F	1F	1F	1F						
Preset Row SC	3?5	08	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00						
Max Scan Line	3?5	09	07	07	07	07	01	01	01	0D	00	00	00	00	00	00	0D	0D	0D	0D						
Cursor Start	3?5	0A	06	06	06	06	00	00	00	0B	00	00	00	00	00	00	0B	0B	0B	0B						
Cursor End	3?5	0B	07	07	07	07	00	00	00	0C	00	00	00	00	00	00	0C	0C	0C	0C						
Strt Addr Hi	3?5	0C	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-						
Strt Addr Lo	3?5	0D	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-						
? = B in monochrome modes      ? = D in color modes																										
*Values for these modes when the IBM Enhanced Color Display is attached																										
:Values for these modes when greater than 64K Graphics Memory is installed																										

## CRT Controller Registers (1 of 2)



Register			Mode of Operation																			
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*		
Cursor LC Hi	375	0E	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		
Cursor LC Low	375	0F	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		
Vrt Retr Strt	375	10	E1	E1	E1	E1	E1	E1	E0	5E	E1	E0	5E	5E	5E	5E	5E	5E	5E	5E		
Light Pen Hi	375	10	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		
Vert Retr End	375	11	24	24	24	24	24	24	23	2E	24	23	2E	2B	2E	2B	2B	2B	2B	2B		
Light Pen Low	375	11	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		
Vrt Disp End	375	12	C7	C7	C7	C7	C7	C7	C7	5D	C7	C7	5D	5D	5D	5D	5D	5D	5D	5D		
Offset	375	13	14	14	28	28	14	14	28	28	14	28	14	14	28	28	14	14	28	28		
Underline Loc	375	14	08	08	08	08	00	00	00	0D	00	00	0D	0F	0D	0F	0F	0F	0F	0F		
Strt Vert Blk	375	15	E0	E0	E0	E0	E0	E0	DF	5E	E0	DF	5E	5F	5E	5F	5E	5E	5E	5E		
End Vert Blk	375	16	F0	F0	F0	F0	F0	F0	EF	6E	F0	EF	6E	0A	6E	0A	0A	0A	0A	0A		
Mode Control	375	17	A3	A3	A3	A3	A2	A2	C2	A3	E3	E3	8B	8B	E3	E3	A3	A3	A3	A3		
Line Compare	375	18	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF		
? = B in monochrome modes      ? = D in color modes																						
*Values for these modes when the IBM Enhanced Color Display is attached																						
:Values for these modes when greater than 64K Graphics Memory is installed																						

## CRT Controller Registers (2 of 2)

Register			Mode of Operation																			
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F2	102	0*	1*	2*	3*		
Grphx I Pos	3CC	-	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Grphx II Pos	3CA	-	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	
Grphx I II AD	3CE	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
Set Reset	3CF	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Enable S/R	3CF	01	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Color Compare	3CF	02	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Data Rotate	3CF	03	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Read Map Sel	3CF	04	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Mode Register	3CF	05	10	10	10	10	30	30	00	10	00	00	10	10	00	00	10	10	10	10		
Miscellaneous	3CF	06	0E	0E	0E	0E	0F	0F	0D	0A	05	05	07	07	05	05	0E	0E	0E	0E		
Color No Care	3CF	07	00	00	00	00	00	00	00	00	0F	0F	0F	0F	0F	0F	00	00	00	00		
Bit Mask	3CF	08	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF		
*Values for these modes when the IBM Enhanced Color Display is attached																						
:Values for these modes when greater than 64K Graphics Memory is installed																						

## Graphics SI Registers

Register			Mode of Operation																		
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*	
Address	3?A	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Palette	3C0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Palette	3C0	01	01	01	01	01	13	13	17	08	01	01	08	01	08	01	01	01	01	01	01
Palette	3C0	02	02	02	02	02	15	15	17	08	02	02	00	00	00	00	02	02	02	02	02
Palette	3C0	03	03	03	03	03	17	17	17	08	03	03	00	00	00	03	03	03	03	03	03
Palette	3C0	04	04	04	04	04	02	02	17	08	04	04	18	04	18	04	04	04	04	04	04
Palette	3C0	05	05	05	05	05	04	04	17	08	05	05	18	07	18	05	05	05	05	05	05
Palette	3C0	06	06	06	06	06	06	06	17	08	06	06	00	00	00	06	14	14	14	14	14
Palette	3C0	07	07	07	07	07	07	07	17	08	07	07	00	00	00	07	07	07	07	07	07
Palette	3C0	08	10	10	10	10	10	10	17	10	10	10	00	00	00	38	38	38	38	38	38
Palette	3C0	09	11	11	11	11	11	11	17	18	11	11	08	01	08	39	39	39	39	39	39
Palette	3C0	0A	12	12	12	12	12	12	17	18	12	12	00	00	00	3A	3A	3A	3A	3A	3A
Palette	3C0	0B	13	13	13	13	13	13	17	18	13	13	00	00	00	3B	3B	3B	3B	3B	3B
? = B in monochrome modes      ? = D in color modes																					
*Values for these modes when the IBM Enhanced Color Display is attached																					
‡Values for these modes when greater than 64K Graphics Memory is installed																					

## Attribute Registers (1 of 2)

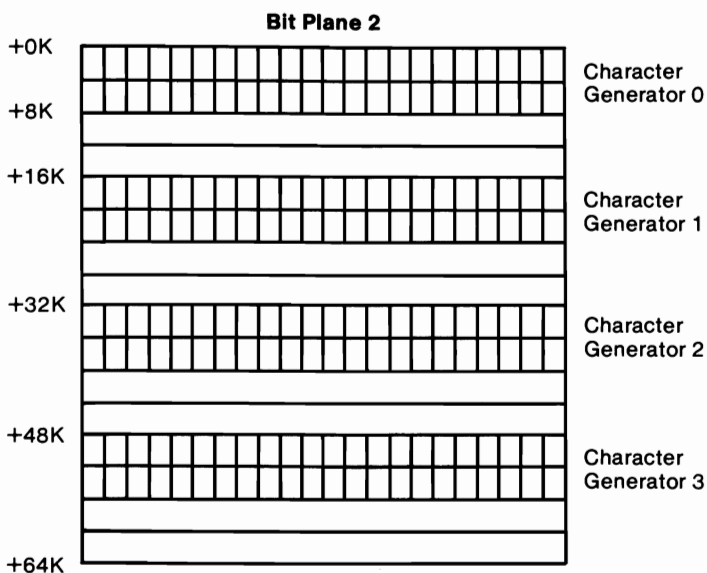
Register			Mode of Operation																							
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*						
Palette	3C0	0C	14	14	14	14	14	14	17	18	14	14	00	04	00	3C	3C	3C	3C	3C						
Palette	3C0	0D	15	15	15	15	15	15	17	18	15	15	18	07	18	3D	3D	3D	3D	3D						
Palette	3C0	0E	16	16	16	16	16	16	17	18	16	16	00	00	00	3E	3E	3E	3E	3E						
Palette	3C0	0F	17	17	17	17	17	17	18	17	17	00	00	00	3F	3F	3F	3F	3F	3F						
Mode Control	3C0	10	08	08	08	08	01	01	01	0E	01	01	0B	0B	0B	01	08	08	08	08						
Overscan	3C0	11	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00						
Color Plane	3C0	12	0F	0F	0F	0F	03	03	01	0F	0F	0F	05	05	05	0F	0F	0F	0F	0F						
Hz Panning	3C0	13	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00						
*Values for these modes when the IBM Enhanced Color Display is attached																										
:Values for these modes when greater than 64K Graphics Memory is installed																										

### Attribute Registers (2 of 2)

## RAM Loadable Character Generator

The character generator on the adapter is RAM loadable and can support characters up to 32 scan lines high. Two character generators are stored within the BIOS and one is automatically loaded into the RAM by the BIOS when an alphanumeric mode is selected. The Character Map Select Register can be programmed to define the function of bit 3 of the attribute byte to be a character generator switch. This allows the user to select between any two character sets residing in bit plane 2. This effectively gives the user access to 512 characters instead of 256. character tables may be loaded off line. The adapter must have 128K bytes of storage to support this function. Up to four tables can be loaded can be loaded with 256K of graphics memory installed.

The structure of the character tables is described in the following figure. The character generator is in bit plane 2 and must be protected using the map mask function.



The following figure illustrates the structure of each character pattern. If the CRT controller is programmed to generate  $n$  row

scans, then  $n$  bytes must be filled in for each character in the character generator. The example assumes eight row scans per character.

Address	Byte Image								Data
$CC * 32 + 0$									18H
1									3EH
2									66H
3									66H
4									7EH
5									66H
6									66H
7									66H

CC = Value of the character code. For example, 41H in the case of an ASCII "A".

## Creating a 512 Character Set

This section describes how to create a 512 character set on the IBM Color Display. Note that only 256 characters can be printed on the printer. This is a special application which the Enhanced Graphics Adapter will support. The 9 by 14 characters will be displayed when attribute bit 3 is a logical 0, and the IBM Color/Graphics Monitor Adapter 8 by 8 characters will be displayed when the attribute bit 3 is a logical 1. This example is for demonstrative purposes only. The assembly language routine for creating 512 characters is given below. Debug 2.0 was used for this example. The starting assembly address is 100 and the character string is stored in location 200. This function requires 128K or more of graphics memory.

```

a100
mov ax,1102 ;load 8x8 character font in character
mov bl,02 ;generator number 2
int 10

( mov ax,1103 ;select 512 character operation
mov bl,08 ;if attribute bit 3=1 use 8x8 font
int 10 ;if attribute bit 3=0 use 9x14 font

mov ax,1000 ;set color plane enable to 7H to disable
mov bx,0712 ;attribute bit 3 in the color palette
int 10 ;lookup table

mov ax,1301
mov bx,000F ;write char. string with attribute bit 3=1
mov cx,003A ;cx = character string length
mov dx,1600 ;write character on line 22 of display
mov bp,0200 ;pointer to character 2 string location
push cs
pop es
int 10

( mov ax,1301
mov bx,0007 ;write char. string with attribute bit 3=0
mov cx,003A ;cx = character string length
mov dx,1700 ;write character on line 23 of display
mov bp,0200 ;pointer to character string location
push cs
pop es
int 10
int 3

a200 db "This character string is used to show 512
characters"

```

## Creating an 80 by 43 Alphanumeric Mode

( The following examples show how to create 80 column by 43 row, both alphanumeric and graphics, images on the IBM Monochrome Display. The BIOS Interface supports an 80 column by *n* row display by using the character generator load routine call. The print screen routine must be revector to

handle the additional character rows on the screen. The assembly language required for both an alphanumeric and a graphics screen is shown below.

```
mov al,7           ;Monochrome alphanumeric mode
int 10            ;video interrupt call
mov ax,1112       ;character generator BIOS routine
mov bl,0          ;load 8 by 8 double dot character font
int 10            ;video interrupt call
mov ax,1200       ;alternate screen routine
move bl,20        ;select alternate print screen routine
int 10            ;video interrupt call
int 3
```

```
mov ax,f           ;Monochrome graphic mode
int 10            ;video interrupt call
mov ax,1123       ;character generator BIOS routine
mov bl,0          ;load 8 by 8 double dot character font
mov dl,2B         ;43 character rows
int 10            ;video interrupt call
mov ax,1200       ;alternate screen routine
mov bl,20         ;alternate print screen routine
int 10            ;video interrupt call
int 3
```

## Vertical Interrupt Feature

The Enhanced Graphics Adapter can be programmed to create an interrupt each time the vertical display refresh time has ended. An interrupt handler routine must be written by the application to take advantage of this feature. The CRT Vertical interrupt is on IRQ2. The CPU can poll the Enhanced Graphics Adapter Input Status Register 0 (bit 7) to determine whether the CRT caused the interrupt to occur.

The Vertical Retrace End Register (11H) in the CRT controller contains two bits which are used to control the interrupt circuitry. The remaining bits must be output as per the value in the mode table.



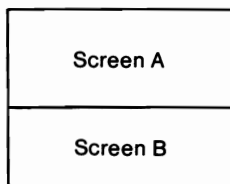
- Bit 5** Enable Vertical Interrupt—A logical 0 will enable vertical interrupt.
- Bit 4** Clear Vertical Interrupt—A logical 0 will clear a vertical interrupt.

The sequence of events which occur in an interrupt handler are outlined below.

1. Clear IRQ latch and enable driver
2. Enable IRQ latch
3. Wait for vertical interrupt
4. Poll Interrupt Status Register 0 to determine if CRTIC has caused the interrupt
5. If CRTIC interrupt, then clear IRQ latch; if not, then branch to next interrupt handler.
6. Enable IRQ latch
7. Update Enhanced Graphics Adapter during vertical blanking interval
8. Wait for next vertical interrupt

## Creating a Split Screen

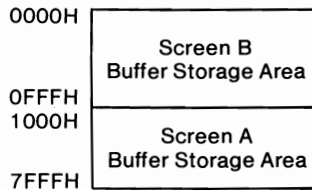
The Enhanced Graphics Adapter hardware supports an alphanumeric mode dual screen display. The top portion of the screen is designated as screen A, and the bottom portion of the screen is designated as screen B as per the following figure.



### Dual Screen Definition

The following figure shows the screen mapping for a system containing a 32K byte alphanumeric storage buffer. Note that the Enhanced Graphics Adapter has a 32K byte storage buffer in alphanumeric mode. Information displayed on screen A is

defined by the start address high and low registers (0CH and 0DH) of the CRTC. Information displayed on screen B always begins at address 0000H.



### Screen Mapping Within the Display Buffer Address Space

The Line Compare Register (18H) of the CRT Controller is utilized to perform the split screen function. The CRTC has an internal horizontal scan counter, and logic which compares the horizontal scan counter value to the Line Compare Register value and clears the memory address generator when a compare occurs. The linear address generator then sequentially addresses the display buffer starting at location zero, and each subsequent row address is determined by the 16 bit addition of the start of line latch and the offset register.

Screen B can be smoothly scrolled onto the CRT screen by updating the Line compare in synchronization with the vertical retrace signal. The information on screen B is immune from scrolling operations which utilize the Start Address High and Low registers to scroll through the Screen A address map.

## Compatibility Issues

The CRT Controller on the IBM Enhanced Graphics Adapter is a custom design, and is different than the 6845 controller used on the IBM Monochrome Monitor Adapter and the IBM Color/Graphics Monitor Adapter. It should be noted that several CRTC register addresses differ between the adapters. The following figure illustrates the registers which do not map directly across the two controllers.

Register	6485 Function	EGA CRTIC Function
02H	Start Horiz. Retrace	Start Horiz. Blanking
03H	End Horiz. Retrace	End Horiz. Blanking
04H	Vertical Total	Start Horiz. Retrace
05H	Vertical Total Adjust	End Horiz. Retrace
06H	Vertical Displayed	Vertical Total
07H	Vertical Sync Position	Overflow
08H	Interlace Mode and Skew	Preset Row Scan

Existing applications which utilize the BIOS interface will generally be compatible with the Enhanced Graphics Adapter.

Horizontal screen centering was required on the IBM Color/Graphics Monitor Adapter in order to center the screen when generating composite video. This was done through the Horizontal Sync Position Register. Since the Enhanced Graphics Adapter does not support a composite video monitor, programs which do screen centering may cause loss of the screen image if centering is attempted.

The Enhanced Graphics Adapter offers a wider variety of displayable monochrome character attributes than the IBM Monochrome Display Adapter. Some attribute values may display differently between the two Adapters. The values listed in the table below, in any combinations with the blink and intensity attributes, will display identically.

Background R G B	Foreground R G B	Function
0 0 0	0 0 0	Non-Display
0 0 0	0 0 1	Underline
0 0 0	1 1 1	White Character/Black Background
1 1 1	0 0 0	Reverse Video

Software which explicitly addresses 3D8 (Mode Select Register) or 3D9 (Color Select Register) on the Color Graphics Monitor Adapter may produce different results on the Enhanced Graphics Adapter. For example, blinking which is disabled by writing to 3D8 on the Color Graphics Adapter will not be disabled on the Enhanced Graphics Adapter.

# Interface

## Feature Connector

The following is a description of the Enhanced Graphics Adapter feature connector. Note that signals coming from the Enhanced Graphics Adapter are labeled "inputs" and the signals coming to the Enhanced Graphics Adapter through the feature connector are labeled "outputs".

<b>Signal</b>	<b>Description</b>
<b>J2</b>	This pin is connected to auxiliary jack 2 on the rear panel of the adapter.
<b>R'OUT</b>	Secondary red output
<b>ATRS/L</b>	Attribute shift load. This signal controls the serialization of the video information. The shift register parallel loads at the dot clock leading edge when this signal is low.
<b>G OUT</b>	Primary green output
<b>R'</b>	Secondary red input
<b>R</b>	Primary red input
<b>FC1</b>	This signal is input from bit 1 (Feature Control Bit 1) of the Feature Control Register.
<b>FC0</b>	This signal is input from bit 0 (Feature Control Bit 0) of the Feature control Register.
<b>FEAT 0</b>	This signal is output to bit 5 (Feature Code 0) of Input Status Register 0.
<b>B'/V</b>	Secondary blue input/Monochrome video
<b>VIN</b>	Vertical retrace input

**Internal** This signal is output to bit 4 (Disable Internal Video Drivers) of the Miscellaneous Output Register.

**V OUT** Vertical retrace output

**J1** This pin is connected to auxiliary jack 1 on the rear panel of the adapter.

**G'OUT** Secondary green output

**B'OUT** Secondary blue output

**B OUT** Blue output

**G** Green input

**B** Blue input

**R OUT** Red output

**BLANK** This is a composite horizontal and vertical blanking signal from the CRTC.

**FEAT 1** This signal is output to bit 6 (Feature Code 1) of Input Status Register 0.

**G'/I** Secondary green/Intensity input

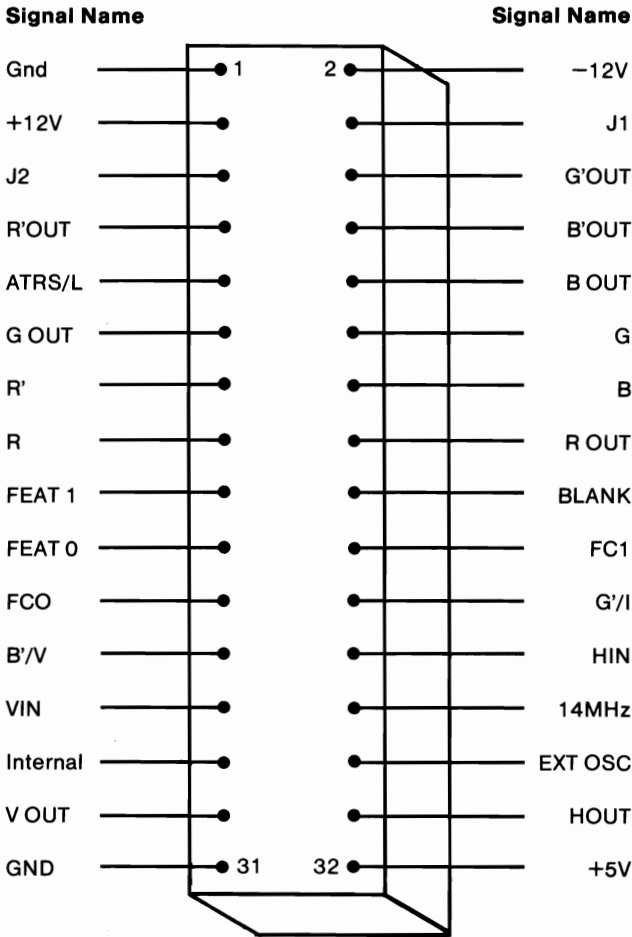
**HIN** Horizontal retrace input from the CRTC

**14MHZ** 14 MHz signal from the system board

**EXT OSC** External dot clock output

**HOUT** Horizontal retrace output

The following figure shows the layout and pin numbering of the feature connector.

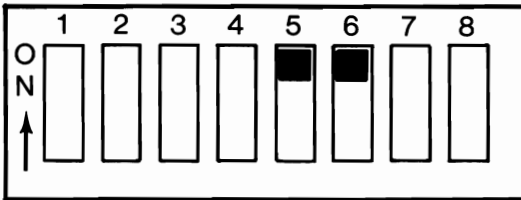


**Feature Connector Diagram**

# Specifications

## System Board Switches

The following figure shows the proper system board DIP switch settings for the IBM Enhanced Graphics Adapter when used with the Personal Computer and the Personal Computer XT. The switch block locations are illustrated in the Technical Reference Manual "System Board Component Diagram". The Personal Computer has two DIP switch blocks; the switch settings shown pertain to DIP Switch Block 1. The Personal Computer XT has one DIP switch block.

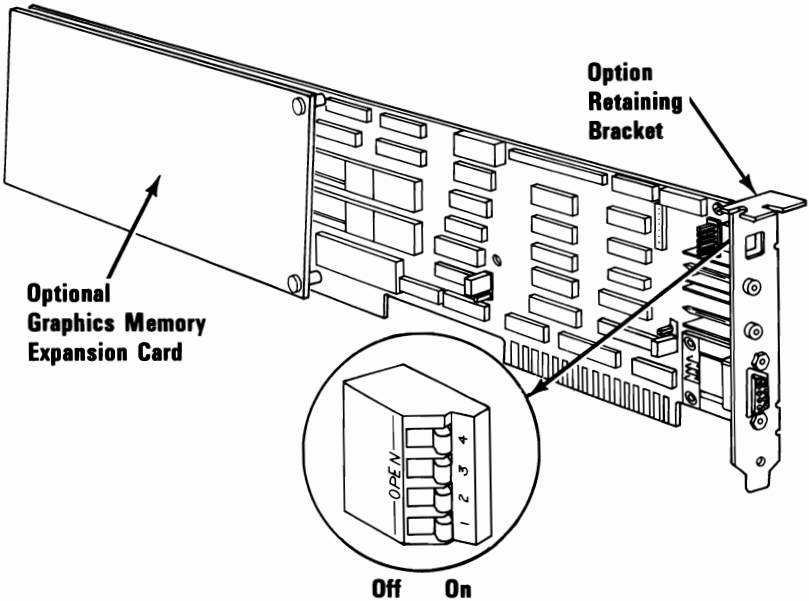


**Switch Block (1)**

**Note:** The DIP switches must be set as shown whenever the IBM Enhanced Graphics Adapter is installed, regardless of display type. This is true even when a second display adapter is installed in the system.

# Configuration Switches

The following diagram shows the location and orientation of the configuration switches on the Enhanced Graphics Adapter.





## Configuration Switch Settings

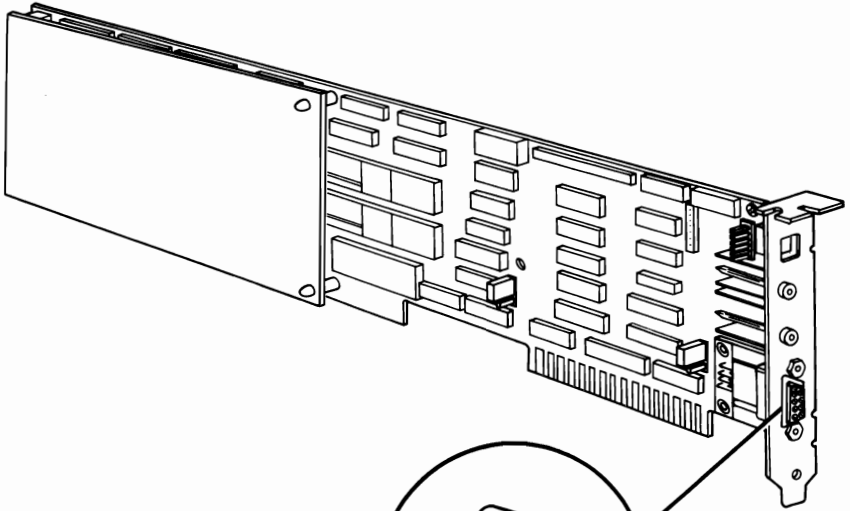
The configuration switches on the Enhanced Graphics Adapter determine the type of display support the adapter provides, as follows:

Switch Settings for Enhanced Graphics Adapter as Primary Display Adapter						
SW1	SW2	SW3	SW4	Configuration		
				Enhanced Adapter	Monochrome Adapter	Color/Graphics Adapter
On	Off	Off	On	Color Display 40x25	Secondary	–
Off	Off	Off	On	Color Display 80x25	Secondary	–
On	On	On	Off	Enhanced Display Emulation Mode	Secondary	–
Off	On	On	Off	Enhanced Display Hi Res Mode	Secondary	–
On	Off	On	Off	Monochrome	–	Secondary 40x25
Off	Off	On	Off	Monochrome	–	Secondary 80x25

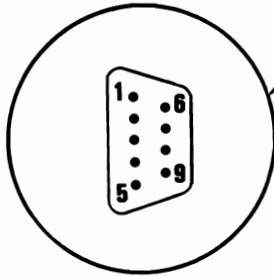
**Switch Settings for Enhanced Graphics Adapter  
as Secondary Display Adapter**

SW1	SW2	SW3	SW4	Configuration		
				Enhanced Adapter	Monochrome Adapter	Color/Graphics Adapter
On	On	On	On	Color Display 40x25	Primary	–
Off	On	On	On	Color Display 80x25	Primary	–
On	Off	On	On	Enhanced Display Emulation Mode	Primary	–
Off	Off	On	On	Enhanced Display Hi Res Mode	Primary	–
On	On	Off	On	Monochrome	–	Primary 40x25
Off	On	Off	On	Monochrome	–	Primary 80x25

# Direct Drive Connector

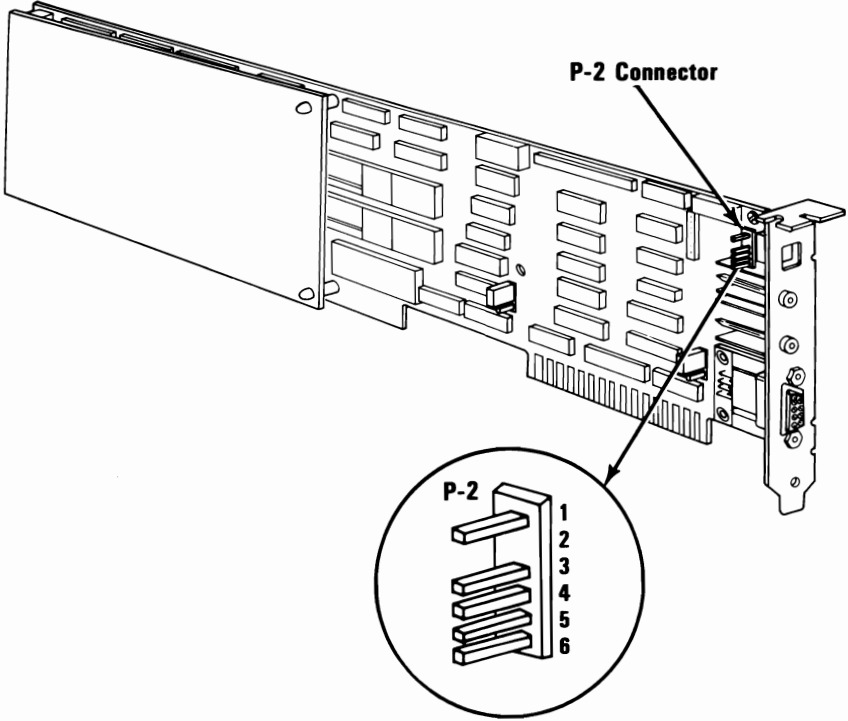


**9-Pin Direct Drive Signal**



	Signal Name - Description	Pin	
<b>Direct Drive Display</b>	Ground	1	<b>Enhanced Graphics Adapter</b>
	Secondary Red	2	
	Primary Red	3	
	Primary Green	4	
	Primary Blue	5	
	Secondary Green/Intensity	6	
	Secondary Blue/Mono Video	7	
	Horizontal Retrace	8	
	Vertical Retrace	9	

# Light Pen Interface



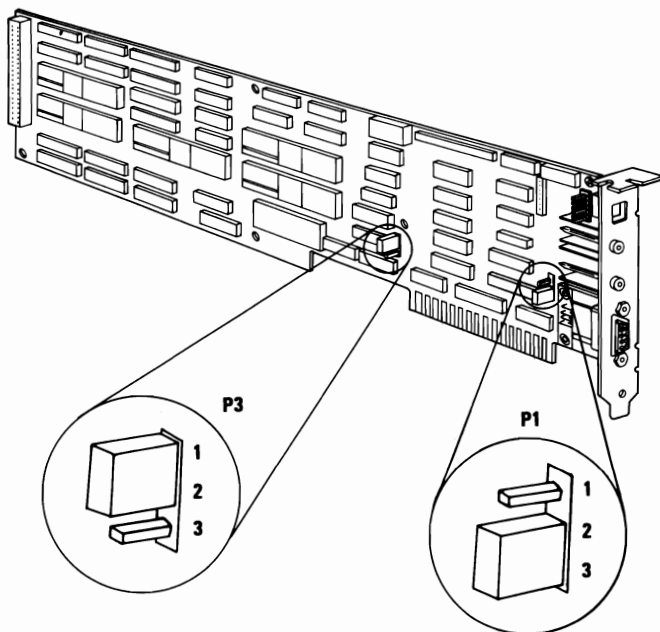
	<b>P-2 Connector</b>	<b>Pin</b>	
<b>Light Pen Attachment</b>	+Light Pen Input	1	<b>Enhanced Graphics Adapter</b>
	Not used	2	
	+Light Pen Switch	3	
	Ground	4	
	+5 Volts	5	
	12 Volts	6	

## Jumper Descriptions

Located on the adapter are two jumpers designated P1 and P3. Jumper P1 changes the function of pin 2 on the direct drive interface. When placed on pins 2 and 3, jumper P1 selects ground as the function of direct drive interface, pin 2. This selection is for displays that support five color outputs, such as the IBM Color Display. When P1 is placed on pins 1 and 2, red prime output is placed on pin 2 of the direct drive interface connector. This supports the IBM Enhanced Color Display, which utilizes six color outputs on the direct drive interface.

Jumper P3 changes the I/O address port of the Enhanced Graphics Adapter within the system. In its normal position, (pins 1 and 2), all Enhanced Graphics Adapter addresses are in the range 3XX. Moving jumper P3 to pins 2 and 3 changes the addresses to 2XX. Operation of the adapter in the 2XX mode is not supported in BIOS.

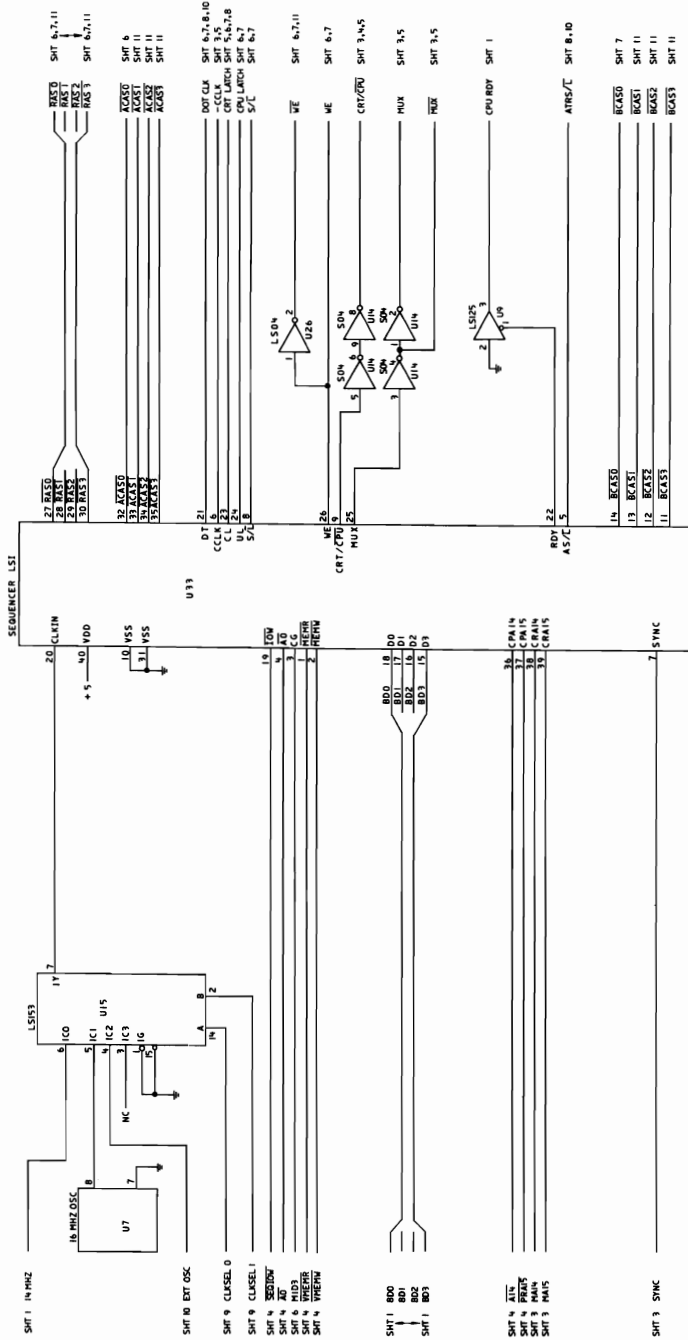
The following figure shows the location of the jumpers and numbering of the connectors.





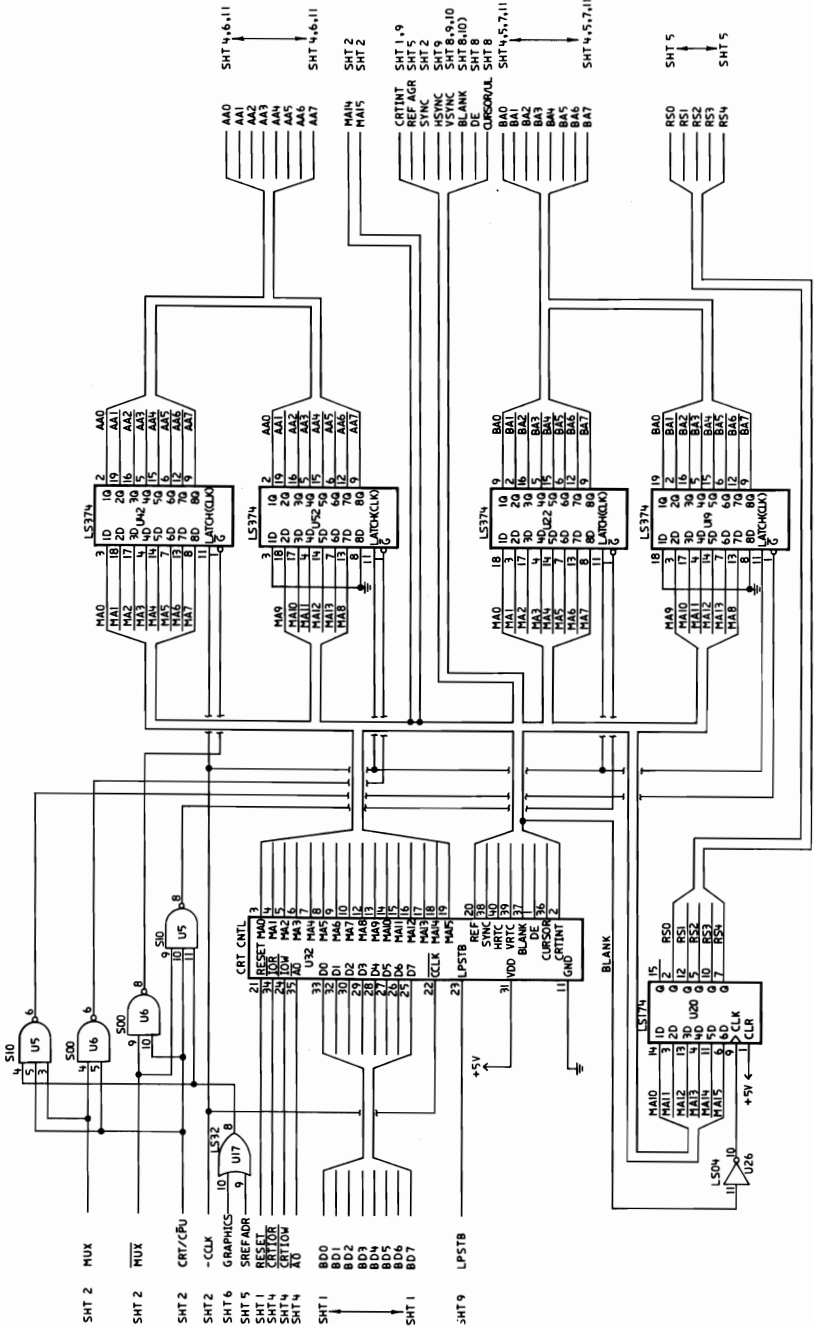


# ENHANCED GRAPHICS ADAPTER

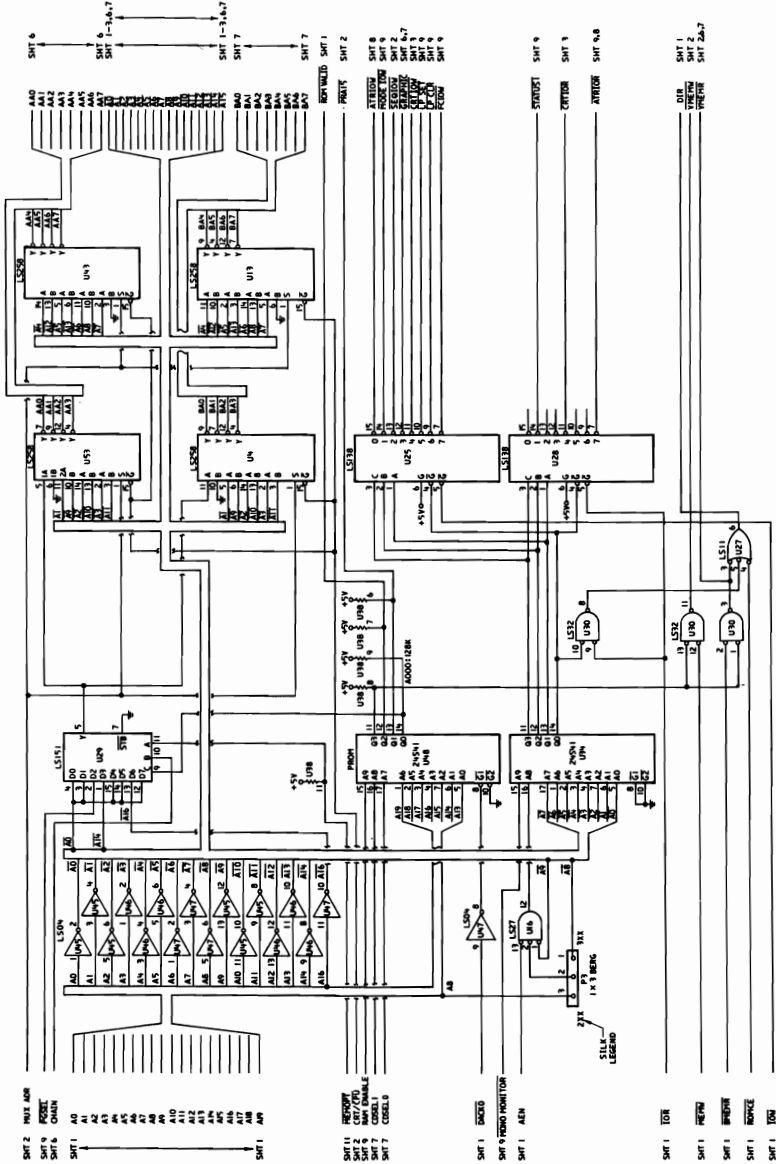




# ENHANCED GRAPHICS ADAPTER



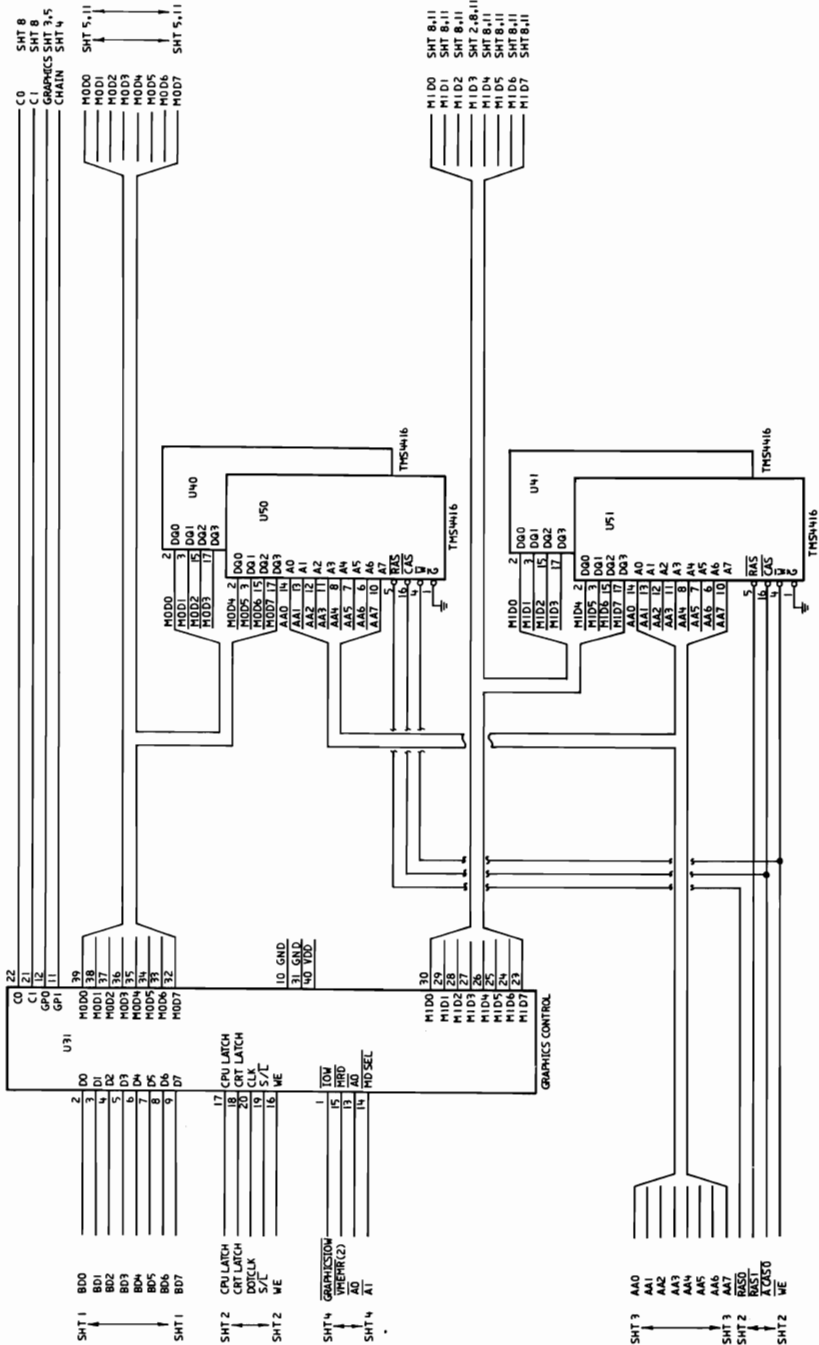
Enhanced Graphics Adapter Sheet 3 of 11



Enhanced Graphics Adapter Sheet 4 of 11

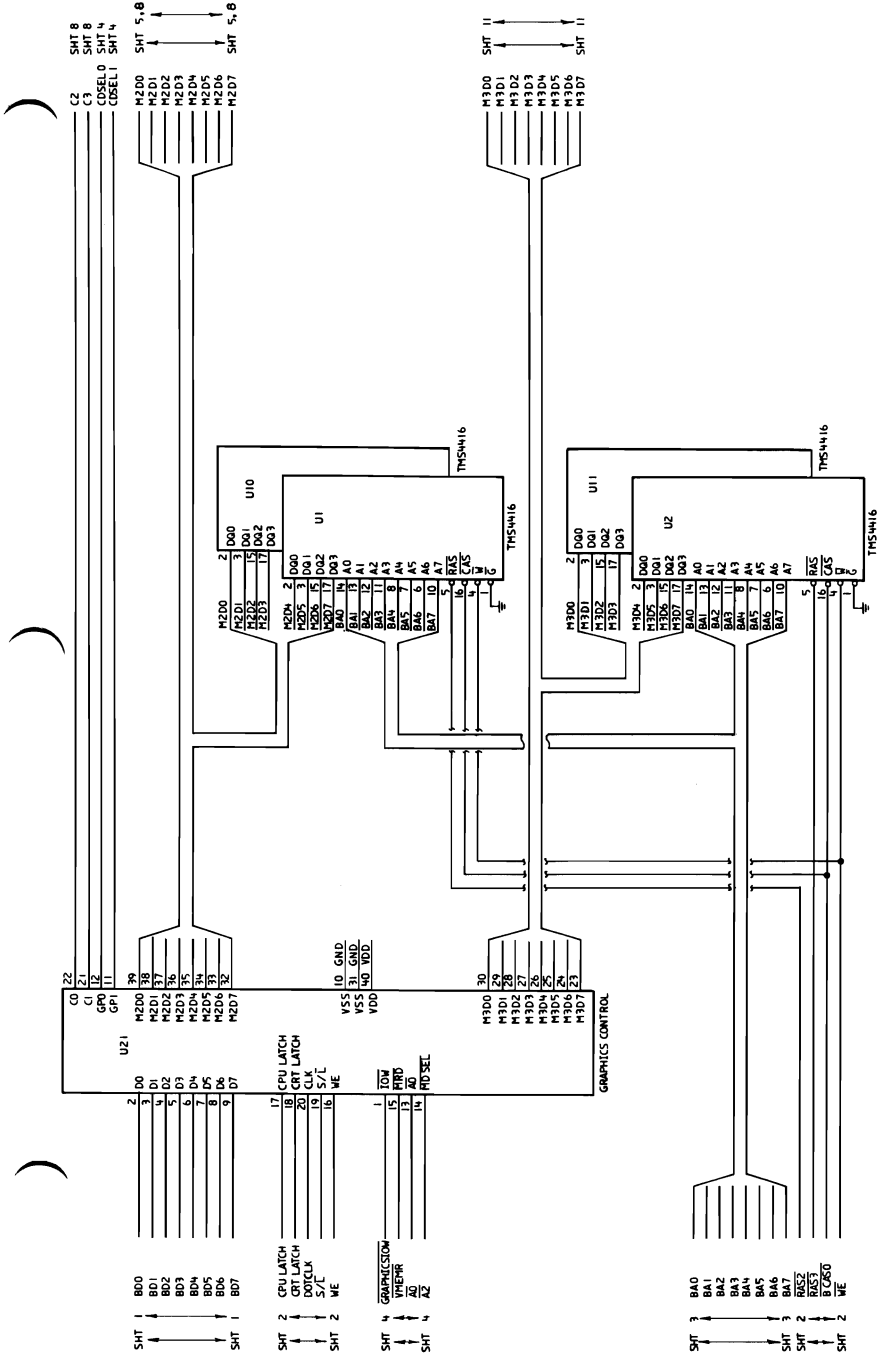


# ENHANCED GRAPHICS ADAPTER



Enhanced Graphics Adapter Sheet 6 of 11

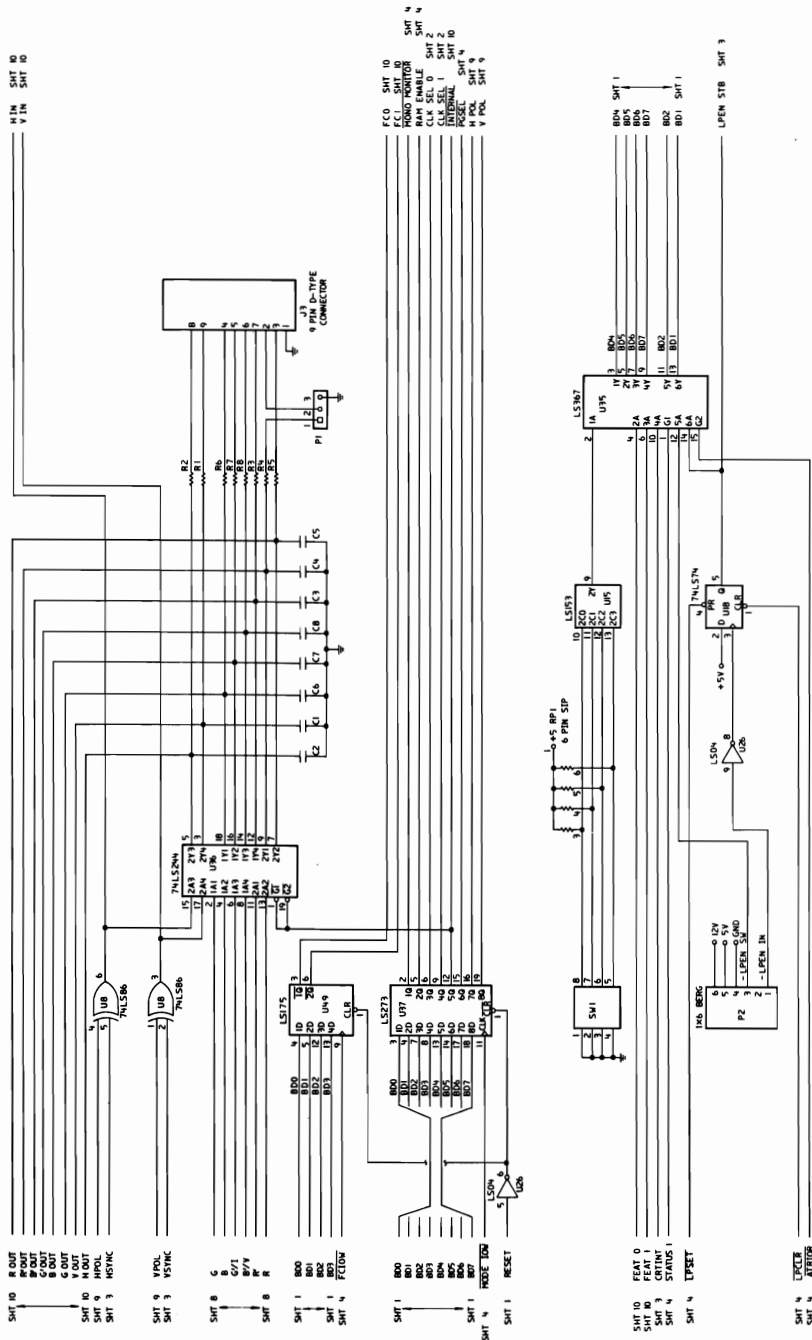
# ENHANCED GRAPHICS ADAPTER



Enhanced Graphics Adapter Sheet 7 of 11

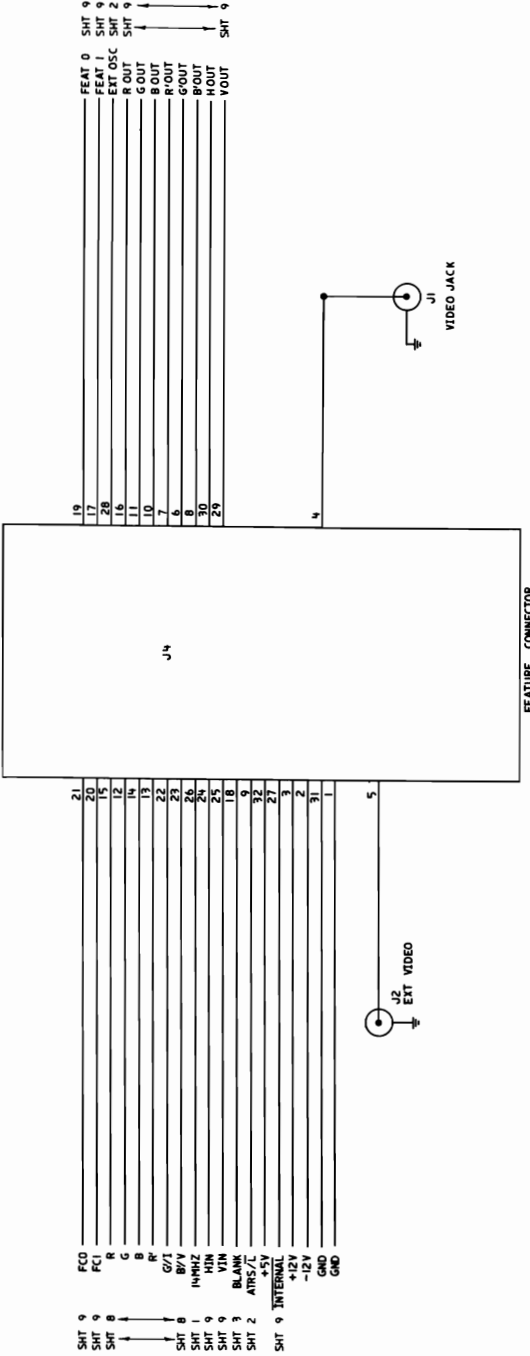


# ENHANCED GRAPHICS ADAPTER



Enhanced Graphics Adapter Sheet 9 of 11

# ENHANCED GRAPHICS ADAPTER

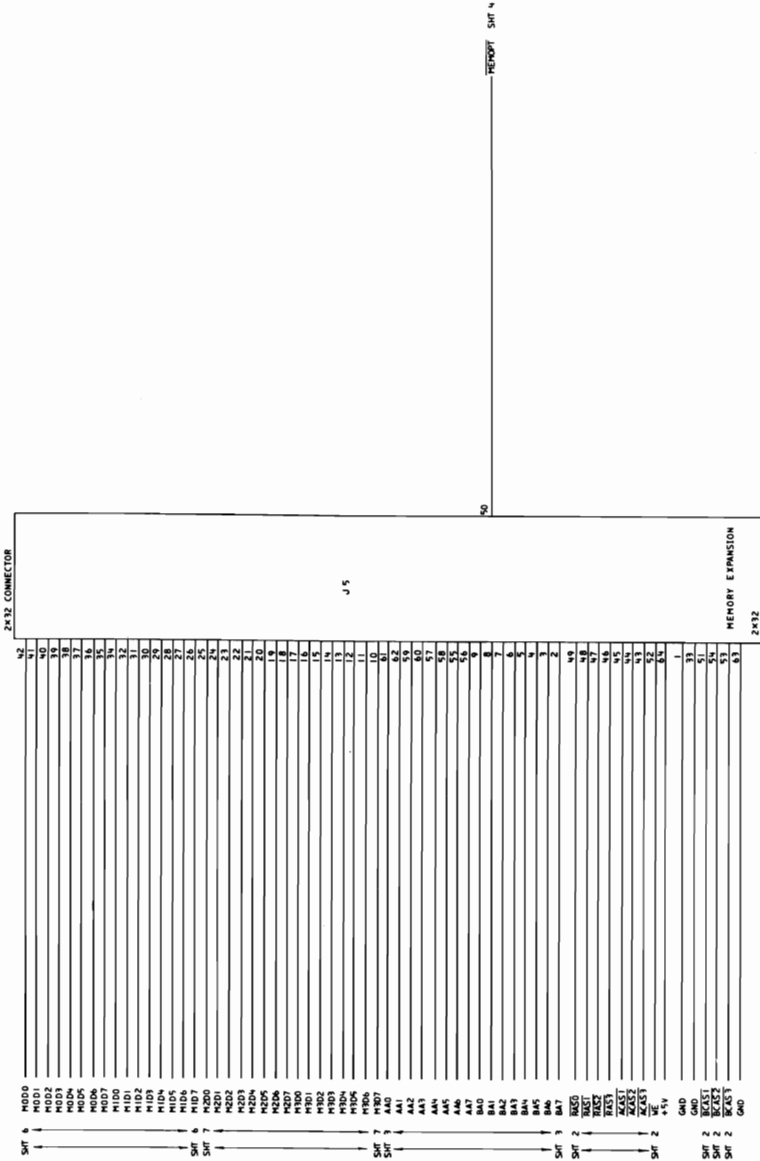


NOTE:  
1 GROUND—ONE AT EACH END OF CONNECTOR.

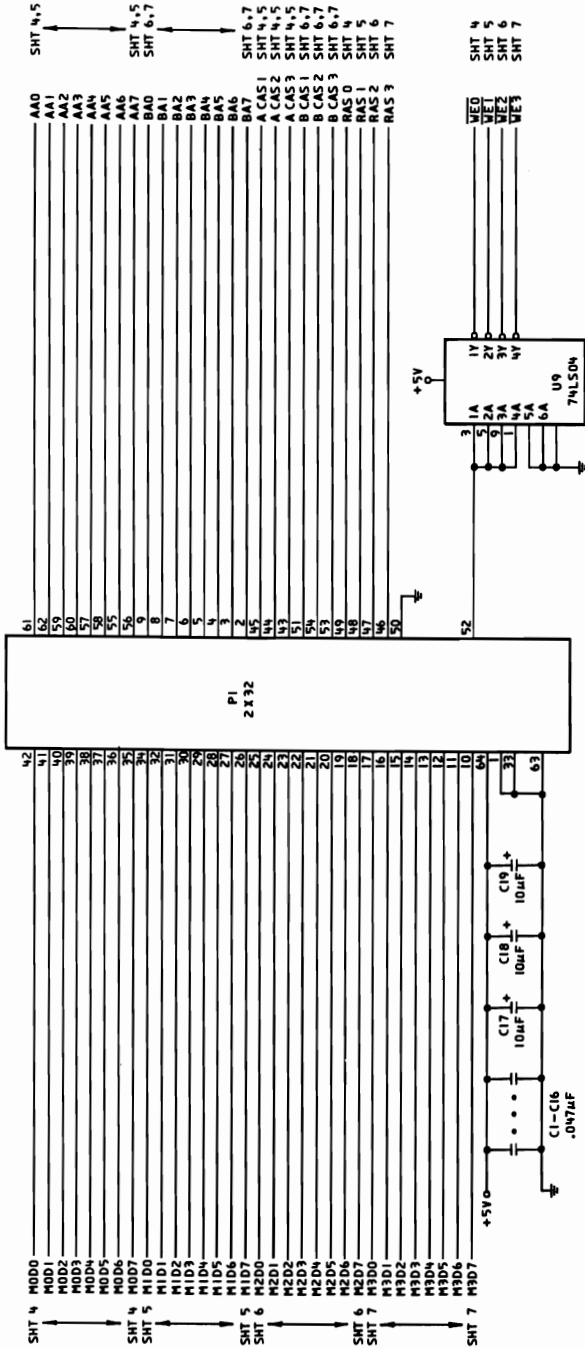
Enhanced Graphics Adapter Sheet 10 of 11



# ENHANCED GRAPHICS ADAPTER

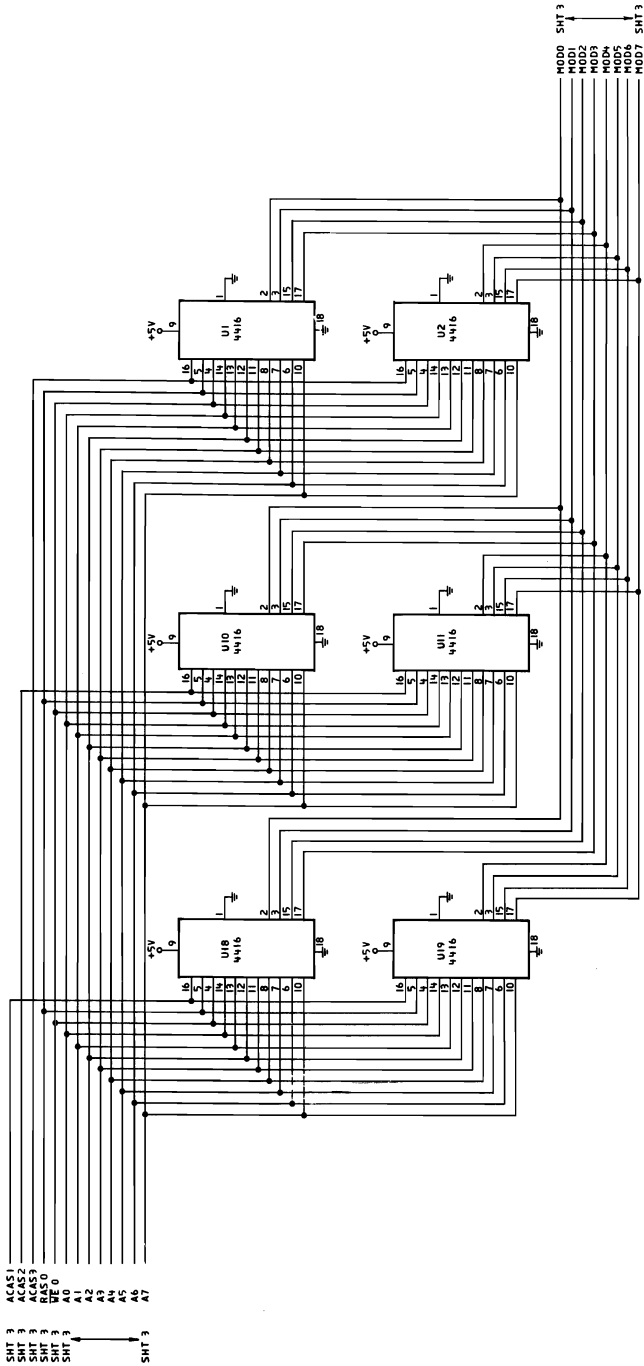


# ENHANCED GRAPHICS ADAPTER



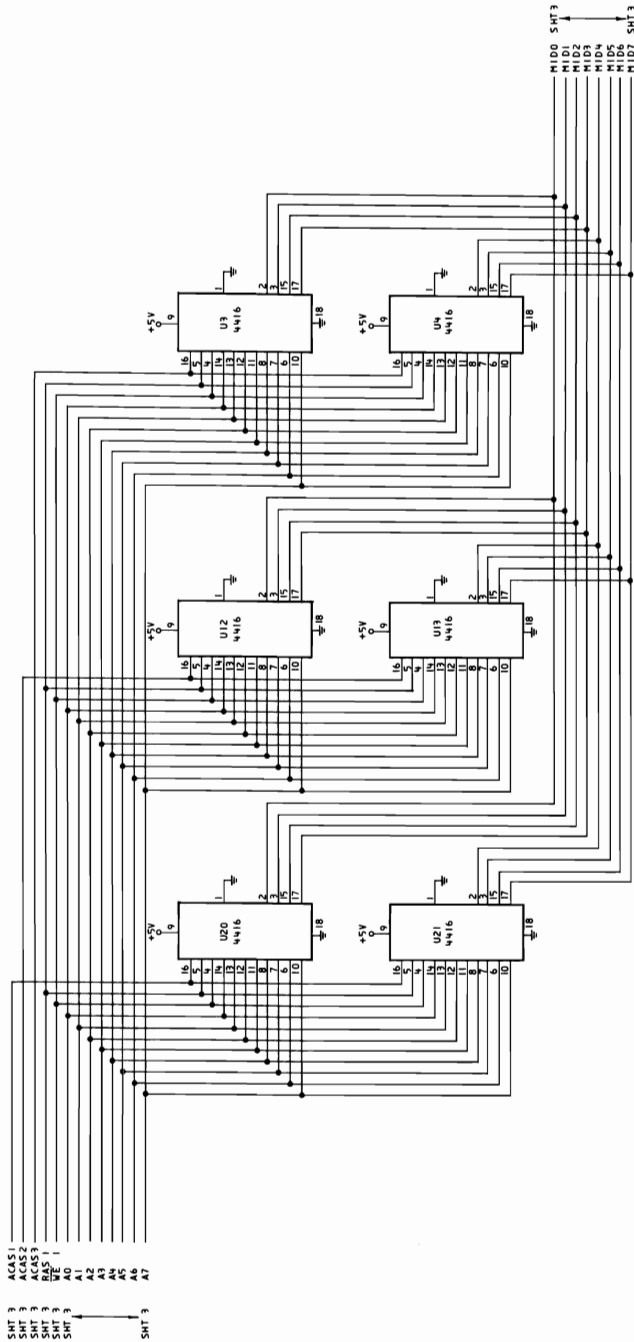
Graphics Memory Expansion Card Sheet 1 of 5

# ENHANCED GRAPHICS ADAPTER



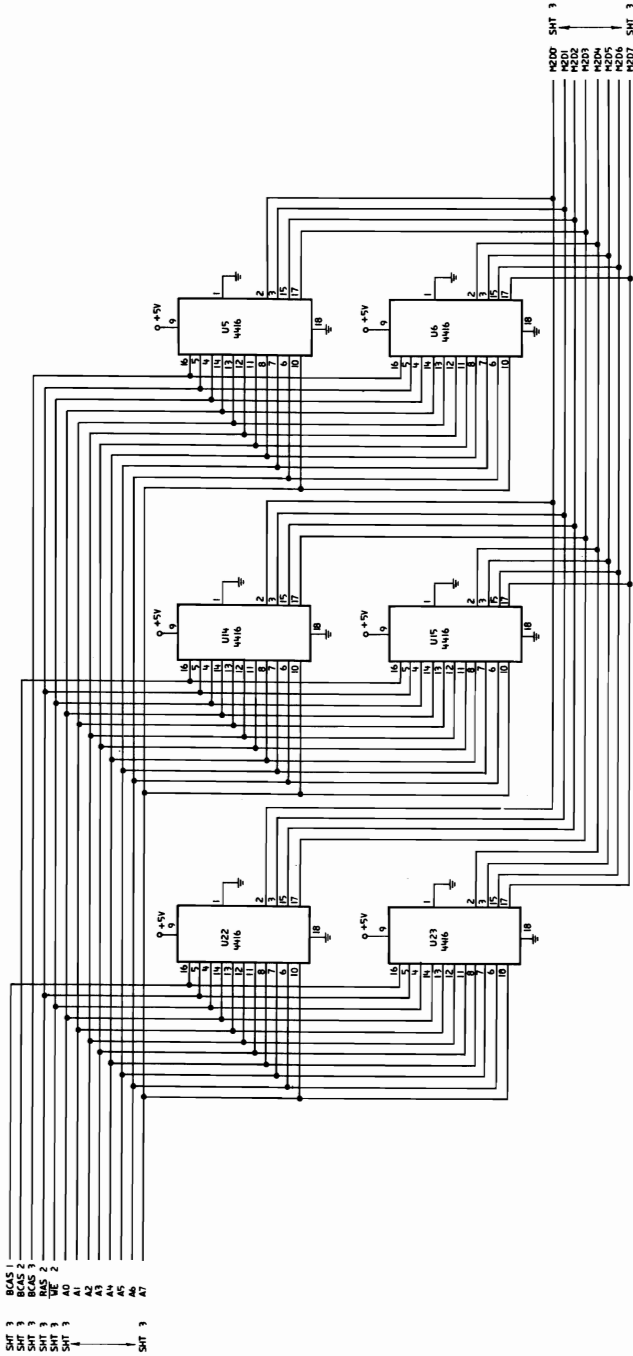
Graphics Memory Expansion Card Sheet 2 of 5

# ENHANCED GRAPHICS ADAPTER



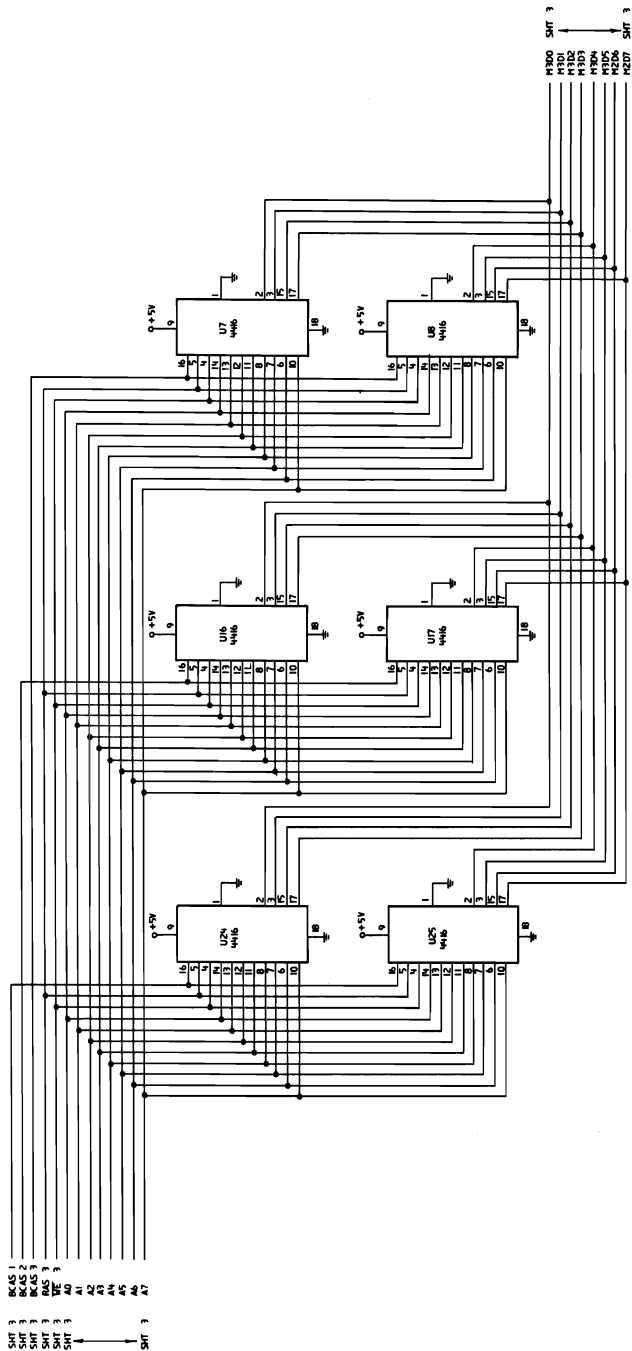
Graphics Memory Expansion Card Sheet 3 of 5

# ENHANCED GRAPHICS ADAPTER



Graphics Memory Expansion Card Sheet 4 of 5

# ENHANCED GRAPHICS ADAPTER



Graphics Memory Expansion Card Sheet 5 of 5

# BIOS Listing

## Vectors with Special Meanings

### Interrupt Hex 42 - Reserved

When an IBM Enhanced Graphics Adapter is installed, the BIOS routines use interrupt 42 to revector the video pointer.

### Interrupt Hex 43 - IBM Enhanced Graphics Video Parameters

When an IBM Enhanced Graphics Adapter is installed, the BIOS routines use this vector to point to a data region containing the parameters required for the initializing of the IBM Enhanced Graphics Adapter. Note that the format of the table must adhere to the BIOS conventions established in the listing. The power-on routines initialize this vector to point to the parameters contained in the IBM Enhanced Graphics Adapter ROM.

### Interrupt Hex 44 - Graphics Character Table

When an IBM Enhanced Graphics Adapter is installed the BIOS routines use this vector to point to a table of dot patterns that will be used when graphics characters are to be displayed. This table will be used for the first 128 code points in video modes 4, 5, and 6. This table will be used for 256 characters in all additional graphics modes. See the appropriate BIOS interface for additional information on setting and using the graphics character table pointer.

-----  
 THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH :  
 SOFTWARE INTERFACES ONLY. ANY ADDRESSES PRESENT IN :  
 THE LISTINGS ARE INCLUDED ONLY FOR COMPLETENESS :  
 NOT FOR REFERENCE. APPLICATIONS WHICH REFERENCE :  
 11 ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENT :  
 12 VIOLATE THE STRUCTURE AND DESIGN OF BIOS.  
 -----

.LIST  
 INCLUDE VFRONT.INC  
 SUBTTL VFRONT.INC  
 PAGE

-----  
 INT 10  
 VIDEO\_IO  
 THESE ROUTINES PROVIDE THE CRT INTERFACE  
 THE FOLLOWING FUNCTIONS ARE PROVIDED:  
 (AH)=0 SET MODE (AL) CONTAINS MODE VALUE  
 -----

AL AD	TYPE	RES	NOTES	DF-DIM	DISPLAY	MAX PGS
* 0	BB	ALPHA	640X200	40X25	COLOR - BW	8
* 1	BB	ALPHA	640X200	40X25	COLOR	8
* 2	BB	ALPHA	640X200	80X25	COLOR - BW	8
	BB	ALPHA	640X200	80X25	COLOR	8
4	BB	GRPHX	320X200	40X25	COLOR	1
5	BB	GRPHX	320X200	40X25	COLOR - BW	1
6	BB	GRPHX	640X200	80X25	COLOR - BW	1
* 7	BO	ALPHA	720X350	80X25	MONOCHROME	8
8		RESERVED				
9		RESERVED				
A		RESERVED				
B		RESERVED - INTERNAL USE				
C		RESERVED - INTERNAL USE				
D	A0	GRPHX	320X200	40X25	COLOR	8
E	A0	GRPHX	640X200	80X25	COLOR	4
F	A0	GRPHX	640X350	80X25	MONOCHROME	2
10	A0	GRPHX	640X350	80X25	H1 RES	2

NOTE : HIGH BIT AL SET PREVENTS REGEN BUFFER CLEAR ON MODES RUNNING ON THE COMBO VIDEO ADAPTER

\*\*\* NOTE BW MODES OPERATE SAME AS COLOR MODES, BUT COLOR BURST IS NOT ENABLED

(AH)=1 SET CURSOR TYPE  
 (CH) = BITS 4-0 = START LINE FOR CURSOR  
 \*\* HARDWARE WILL ALWAYS CAUSE BLINK  
 \*\* SETTING BIT 5 OR 6 WILL CAUSE ERRATIC BLINKING OR NO CURSOR AT ALL  
 (CL) = BITS 4-0 = END LINE FOR CURSOR

(AH)=2 SET CURSOR POSITION  
 (DH,DL) = ROW,COLUMN (0,0) IS UPPER LEFT  
 (BH) = PAGE NUMBER

(AH)=3 READ CURSOR POSITION  
 (BH) = PAGE NUMBER  
 (DH,DL) = ROW,COLUMN OF CURRENT CURSOR  
 (CH,CL) = CURSOR MODE CURRENTLY SET

(AH)=4 READ LIGHT PEN POSITION  
 ON EXIT  
 (AH) = 0 -- LIGHT PEN SWITCH NOT DOWN/NOT TRIGGERED  
 (AH) = 1 -- VALID LIGHT PEN VALUE IN REGISTERS  
 (DH,DL) = ROW,COLUMN OF CHARACTER LP POSN  
 (CH) = RASTER LINE (0-19)  
 (CX) = RASTER LINE (0-MNN) NEW GRAPHICS MODES  
 (BX) = PIXEL COLUMN (0-319,639)

(AH)=5 SELECT ACTIVE DISPLAY PAGE  
 (AL) = NEW PAGE VALUE, SEE AH=0 FOR PAGE INFO

(AH)=6 SCROLL ACTIVE PAGE UP  
 (AL) = NUMBER OF LINES, INPUT LINES BLANKED AT BOTTOM OF WINDOW  
 AL = 0 MEANS BLANK ENTIRE WINDOW  
 (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL  
 (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL  
 (BH) = ATTRIBUTE TO BE USED ON BLANK LINE

(AH)=7 SCROLL ACTIVE PAGE DOWN  
 (AL) = NUMBER OF LINES, INPUT LINES BLANKED AT TOP OF WINDOW  
 AL = 0 MEANS BLANK ENTIRE WINDOW  
 (CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL  
 (DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL  
 (BH) = ATTRIBUTE TO BE USED ON BLANK LINE

CHARACTER HANDLING ROUTINES

(AH) = 8 READ ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION  
 (BH) = DISPLAY PAGE  
 ON EXIT:  
 (AL) = CHAR READ  
 (AH) = ATTRIBUTE OF CHARACTER READ (ALPHA MODES ONLY)

(AH) = 9 WRITE ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION  
 (BH) = DISPLAY PAGE  
 (CX) = COUNT OF CHARACTERS TO WRITE  
 (AL) = CHAR TO WRITE  
 (BL) = ATTRIBUTE OF CHARACTER (ALPHA)/COLOR OF CHAR (GRAPHICS)  
 SEE NOTE ON WRITE DOT FOR BIT 7 OF BL = 1.

(AH) = A WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION  
 (BH) = DISPLAY PAGE  
 (CX) = COUNT OF CHARACTERS TO WRITE  
 (AL) = CHAR TO WRITE

FOR READ/WRITE CHARACTER INTERFACE WHILE IN GRAPHICS MODE, THE CHARACTERS ARE FORMED FROM A CHARACTER GENERATOR IMAGE MAINTAINED IN THE SYSTEM ROM. ONLY THE 1ST 128 CHARS ARE CONTAINED THERE. TO READ/WRITE THE SECOND 128 CHARS, THE USER MUST INITIALIZE THE POINTER AT INTERRUPT\_1FH (LOCATION 0007CH) TO POINT TO THE 1K BYTE TABLE CONTAINING THE CODE POINTS FOR THE SECOND 128 CHARS (128-255).

FOR THE NEW GRAPHICS MODES 256 GRAPHICS CHARS ARE SUPPLIED IN THE SYSTEM ROM.

FOR WRITE CHARACTER INTERFACE IN GRAPHICS MODE, THE REPLICATION FACTOR CONTAINED IN (CX) ON ENTRY WILL PRODUCE VALID RESULTS ONLY FOR CHARACTERS CONTAINED ON THE SAME ROW. CONTINUATION TO SUCCEEDING LINES WILL NOT PRODUCE CORRECTLY.



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GRAPHICS INTERFACE  
 (AH) = B SET COLOR PALETTE  
 FOR USE IN COMPATIBILITY MODES  
 (BH) = PALETTE COLOR ID BEING SET (0-127)  
 (BL) = COLOR VALUE TO BE USED WITH THAT COLOR ID  
 NOTE: FOR THE CURRENT COLOR CARD, THIS ENTRY POINT  
 HAS MEANING ONLY FOR 320X200 GRAPHICS.  
 COLOR ID = 0 SELECTS THE BACKGROUND COLOR (0-15);  
 COLOR ID = 1 SELECTS THE PALETTE TO BE USED:  
 0 = GREEN(1)/RED(2)/BROWN(3)  
 1 = CYAN(1)/MAGENTA(2)/WHITE(3)  
 IN 40X25 OR 80X25 ALPHA MODES, THE VALUE SET  
 FOR PALETTE COLOR 0 INDICATES THE  
 BORDER COLOR TO BE USED (VALUES 0-31,  
 WHERE 16-31 SELECT THE HIGH INTENSITY  
 BACKGROUND SET).

(AH) = C WRITE DOT  
 (BH) = PAGE  
 (DX) = ROW NUMBER  
 (CX) = COLUMN NUMBER  
 (AL) = COLOR VALUE  
 IF BIT 7 OF AL = 1, THEN THE COLOR VALUE IS  
 EXCLUSIVE OR'D WITH THE CURRENT CONTENTS OF  
 THE DOT

(AH) = D READ DOT  
 (BH) = PAGE  
 (DX) = ROW NUMBER  
 (CX) = COLUMN NUMBER  
 (AL) RETURNS THE DOT READ

ASCII TELETYPE ROUTINE FOR OUTPUT

(AH) = E WRITE TELETYPE TO ACTIVE PAGE  
 (AL) = CHAR TO WRITE  
 (BL) = FOREGROUND COLOR IN GRAPHICS MODE  
 NOTE -- SCREEN WIDTH IS CONTROLLED BY PREVIOUS MODE SET

(AH) = F CURRENT VIDEO STATE  
 RETURNS THE CURRENT VIDEO STATE  
 (AL) = MODE CURRENTLY SET (SEE AH=0 FOR EXPLANATION)  
 (AH) = NUMBER OF CHARACTER COLUMNS ON SCREEN  
 (BH) = CURRENT ACTIVE DISPLAY PAGE

(AH) = 10 SET PALETTE REGISTERS  
 (AL) = 0 SET INDIVIDUAL PALETTE REGISTER  
 BL = PALETTE REGISTER TO BE SET  
 BH = VALUE TO SET  
 AL = 1 SET OVERSCAN REGISTER  
 BH = VALUE TO SET  
 AL = 2 SET ALL PALETTE REGISTERS AND OVERSCAN  
 ES:DX POINTS TO A 17 BYTE TABLE  
 BYTES 0 - 15 ARE THE PALETTE VALUES, RESPECTIVELY  
 BYTE 16 IS THE OVERSCAN VALUE  
 AL = 3 TOGGLE INTENSIFY/BLINKING BIT  
 BL - 0 ENABLE INTENSIFY  
 BL - 1 ENABLE BLINKING

(AH) = 11 CHARACTER GENERATOR ROUTINE  
 NOTE : THIS CALL WILL INITIATE A MODE SET, COMPLETELY  
 RESETTING THE VIDEO ENVIRONMENT BUT MAINTAINING  
 THE REGEN BUFFER.

AL = 00 USER ALPHA LOAD  
 ES:BP - POINTER TO USER TABLE  
 CX - COUNT TO STORE  
 DX - CHARACTER OFFSET INTO TABLE  
 BL - BLOCK TO LOAD  
 BH - NUMBER OF BYTES PER CHARACTER

AL = 01 ROM MONOCHROME SET  
 BL - BLOCK TO LOAD

AL = 02 ROM 8X8 DOUBLE DOT  
 BL - BLOCK TO LOAD

AL = 03 BL SET BLOCK SPECIFIER  
 BL - CHAR GEN BLOCK SPECIFIER  
 D3-D2 ATTR BIT 3 ONE, CHAR GEN 0-3  
 D1-D0 ATTR BIT 3 ZERO, CHAR GEN 0-3  
 NOTE : WHEN USING AL = 03 A FUNCTION CALL  
 AX = 1000H  
 BX = 0712H  
 IS RECOMMENDED TO SET THE COLOR PLANES  
 RESULTING IN 512 CHARACTERS AND EIGHT  
 CONSISTENT COLORS.

NOTE : THE FOLLOWING INTERFACE (AL=1X) IS SIMILAR IN FUNCTION  
 TO (AL=0X) EXCEPT THAT :  
 - PAGE ZERO MUST BE ACTIVE  
 - POINTS (BYTES/CHAR) WILL BE RECALCULATED  
 - ROWS WILL BE CALCULATED FROM THE FOLLOWING:  
 INT[(200 OR 350) / POINTS] - 1  
 - CRT\_LEN WILL BE CALCULATED FROM :  
 (ROWS + 1) \* CRT\_COLS \* 2  
 - THE CRTIC WILL BE REPROGRAMMED AS FOLLOWS :  
 RO9H = POINTS - 1 MAX SCAN LINE  
 ROAH = RO9H DONE ONLY IN MODE 7  
 ROAH = POINTS - 2 CURSOR START  
 ROBH = 0 CURSOR END  
 R12H = [(ROWS + 1) \* POINTS] - 1 VERT DISP END  
 R14H = POINTS UNDERLINE LOC

THE ABOVE REGISTER CALCULATIONS MUST BE CLOSE TO THE  
 ORIGINAL TABLE VALUES OR UNDETERMINED RESULTS WILL  
 OCCUR.

NOTE : THE FOLLOWING INTERFACE IS DESIGNED TO BE  
 CALLED ONLY IMMEDIATELY AFTER A MODE SET HAS  
 BEEN ISSUED, FAILURE TO ADHERE TO THIS PRACTICE  
 MAY CAUSE UNDETERMINED RESULTS.

AL = 10 USER ALPHA LOAD  
 ES:BP - POINTER TO USER TABLE  
 CX - COUNT TO STORE  
 DX - CHARACTER OFFSET INTO TABLE  
 BL - BLOCK TO LOAD  
 BH - NUMBER OF BYTES PER CHARACTER

AL = 11 ROM MONOCHROME SET  
 BL - BLOCK TO LOAD

AL = 12 ROM 8X8 DOUBLE DOT  
 BL - BLOCK TO LOAD

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NOTE : THE FOLLOWING INTERFACE IS DESIGNED TO BE CALLED ONLY IMMEDIATELY AFTER A MODE SET HAS BEEN ISSUED. FAILURE TO ADHERE TO THIS PRACTICE MAY CAUSE UNDETERMINED RESULTS.

AL = 20 USER GRAPHICS CHARS INT 01FH (8X8)  
 ES:BP - POINTER TO USER TABLE  
 AL = 21 USER GRAPHICS CHARS  
 ES:BP - POINTER TO USER TABLE  
 CX - POINTS (BYTES PER CHARACTER)  
 BL - ROW SPECIFIER  
 BL = 0 USER DL - ROWS  
 BL = 1 14 (0EH)  
 BL = 2 25 (19H)  
 BL = 3 43 (2BH)  
 AL = 22 ROM 8 X 14 SET  
 BL - ROW SPECIFIER  
 AL = 23 ROM 8 X 8 DOUBLE DOT  
 BL - ROW SPECIFIER  
 AL = 30 INFORMATION  
 CX - POINTS  
 DL - ROWS  
 BH = 0 RETURN CURRENT INT 1FH PTR  
 ES:BP - PTR TO TABLE  
 BH = 1 RETURN CURRENT INT 44H PTR  
 ES:BP - PTR TO TABLE  
 BH = 2 RETURN ROM 8 X 14 PTR  
 ES:BP - PTR TO TABLE  
 BH = 3 RETURN ROM DOUBLE DOT PTR  
 ES:BP - PTR TO TABLE  
 BH = 4 RETURN ROM DOUBLE DOT PTR (TOP)  
 ES:BP - PTR TO TABLE  
 BH = 5 RETURN ROM ALPHA ALTERNATE 9X14  
 ES:BP - PTR TO TABLE

(AH) = 12 ALTERNATE SELECT  
 BL = 10 RETURN EGA INFORMATION  
 BH = 0 - COLOR MODE IN EFFECT <3><D><D>  
 1 - MONOC MODE IN EFFECT <3><B><D>  
 BL = MEMORY VALUE  
 0 0 - 064K 0 1 - 128K  
 1 0 - 192K 1 1 - 256K  
 CH = FEATURE BITS  
 CL = SWITCH SETTING  
 BL = 20 SELECT ALTERNATE PRINT SCREEN ROUTINE

(AH) = 13 WRITE STRING  
 ES:BP - POINTER TO STRING TO BE WRITTEN  
 CX - CHARACTER ONLY COUNT  
 DX - POSITION TO BEGIN STRING, IN CURSOR TERMS  
 BH - PAGE NUMBER

AL = 0 BL - ATTRIBUTE  
 STRING - (CHAR, CHAR, CHAR, ...) CURSOR NOT MOVED  
 AL = 1 BL - ATTRIBUTE  
 STRING - (CHAR, CHAR, CHAR, ...) CURSOR IS MOVED  
 AL = 2 BL - ATTRIBUTE  
 STRING - (CHAR, ATTR, CHAR, ATTR, ...) CURSOR NOT MOVED  
 AL = 3 BL - ATTRIBUTE  
 STRING - (CHAR, ATTR, CHAR, ATTR, ...) CURSOR IS MOVED

NOTE : CHAR RET, LINE FEED, BACKSPACE, AND BELL ARE TREATED AS COMMANDS RATHER THAN PRINTABLE CHARACTERS.

```
SRLOAD MACRO SEGREG,VALUE
        IFNB <VALUE>
        IFIDN <VALUE>,<D>
        SUB    DX,DX
        ELSE
        MOV    DX,VALUE
        ENDF
        MOV    SEGREG,DX
        ENDM
```

;----- LOW MEMORY SEGMENT

```
ABSO SEGMENT AT 0
0000 ORG 005H*4 ; PRINT SCREEN VECTOR
0014 LABEL DWORD
0014 INT5_PTR ORG 010H*4 ; VIDEO I/O VECTOR
0040 LABEL DWORD
0040 VIDEO ORG 01FH*4 ; GRAPHIC CHARS 128-255
007C LABEL DWORD
007C EXT_PTR ORG 01FH*4
0108 ORG 042H*4 ; REVECTORED 10H*4
0108 PLANAR_VIDEO LABEL DWORD
010C ORG 043H*4 ; GRAPHIC CHARS 0-255
010C GRX_SET LABEL DWORD
0410 ORG 0410H LABEL BYTE
0410 EQUIP_LOW LABEL
0410 EQUIP_FLAG DW ?
0449 ORG 449H ?
0449 CRT_MODE DB ?
0449 CRT_COLS DW ?
044C CRT_LEN DW ?
044C CRT_START DW ?
0450 CRT_CURSOR_POSN DW 8 DUP(?)
0460 CRT_CURSOR_MODE DW ?
0462 CRT_ACTIVE_PAGE DB ?
```

```

0463 ???? 379
0465 ?? 380
0466 ?? 381
C ADDR_68H5 DW ?
C CRT_MODE_SET DB ?
C CRT_PALETTE DB ?
C
C 382
C 383 ORG 0472H
C RESET_FLAG DW ?
C 384
C 385 ORG 0484H
C 0484 ?? 386 ; ROWS ON THE SCREEN
C 0485 ???? 387 ; BYTES PER CHARACTER
C 388
C 389
C 390
C 391
C 392 ; INFO
C 393 -D7 - HIGH BIT OF MODE SET. CLEAR/NOT CLEAR REGEN
C 394 D6 - MEMORY D6 D5 = 0 0 - 064K 0 1 - 128K
C 395 D5 - MEMORY 1 0 - 192K 1 1 - 256K
C 396 D4 - RESERVED
C 397 D3 - EGA ACTIVE MONITOR (0), EGA NOT ACTIVE (1)
C 398 D2 - WAIT FOR DISPLAY ENABLE (1)
C 399 D1 - EGA HAS A MONOCHROME ATTACHED (1)
C 400 D0 - SET C_TYPE EMULATE ACTIVE (0)
C 401
C 0488 ?? 402 INFO_3 DB ?
C 403
C ; INFO_3
C 404 07-D4 FEATURE BITS
C 405 03-D0 SWITCHES
C 406
C 04A8 407 ORG 04A8H
C 04A8 408 LABEL DWORD
C 409
C 410
C 411 ----- SAVE_PTR
C 412 SAVE_PTR IS A POINTER TO A TABLE AS DESCRIBED AS FOLLOWS :
C 413
C 414 DWORD_1 VIDEO PARAMETER TABLE POINTER
C 415 DWORD_2 DYNAMIC SAVE AREA POINTER
C 416 DWORD_3 ALPHA MODE AUXILIARY CHAR GEN POINTER
C 417 DWORD_4 GRAPHICS MODE AUXILIARY CHAR GEN POINTER
C 418 DWORD_5 RESERVED
C 419 DWORD_6 RESERVED
C 420 DWORD_7 RESERVED
C 421
C 422 DWORD_1 PARAMETER TABLE POINTER
C 423 INITIALIZED TO BIOS EGA PARAMETER TABLE.
C 424 THIS VALUE MUST EXIST.
C 425
C 426 DWORD_2 PARAMETER SAVE AREA POINTER
C 427 INITIALIZED TO 0000:0000, THIS VALUE IS OPTIONAL.
C 428 WHEN NON-ZERO, THIS POINTER WILL BE USED AS POINTER
C 429 TO A RAM AREA WHERE CERTAIN DYNAMIC VALUES ARE TO
C 430 BE SAVED. WHEN IN EGA OPERATION THIS RAM AREA WILL
C 431 HOLD THE 16 EGA PALETTE REGISTER VALUES PLUS
C 432 THE OVERSCAN VALUE IN BYTES 0-16D RESPECTIVELY.
C 433 AT LEAST 256 BYTES MUST BE ALLOCATED FOR THIS AREA.
C 434
C 435 DWORD_3 ALPHA MODE AUXILIARY POINTER
C 436 INITIALIZED TO 0000:0000, THIS VALUE IS OPTIONAL.
C 437 WHEN NON-ZERO, THIS POINTER IS USED AS A POINTER
C 438 TO A TABLES DESCRIBED AS FOLLOWS :
C 439
C 440 BYTE BYTES/CHARACTER
C 441 BLOCK TO LOAD, SHOULD BE ZERO FOR NORMAL
C 442 OPERATION
C 443 WORD COUNT TO STORE, SHOULD BE 256D FOR NORMAL
C 444 OPERATION
C 445 WORD CHARACTER OFFSET, SHOULD BE ZERO FOR NORMAL
C 446 OPERATION
C 447 DWORD POINTER TO A FONT TABLE
C 448 BYTE DISPLAYABLE ROWS
C 449 IF 'FF' THE MAXIMUM CALCULATED VALUE WILL BE
C 450 USED, ELSE THIS VALUE WILL BE USED
C 451 CONSECUTIVE BYTES OF MODE VALUES FOR WHICH
C 452 THIS FONT DESCRIPTION IS TO BE USED.
C 453 THE END OF THIS STREAM IS INDICATED BY A
C 454 BYTE CODE OF 'FF'
C 455
C 456 NOTE : USE OF THIS POINTER MAY CAUSE UNEXPECTED
C 457 CURSOR TYPE OPERATION. FOR AN EXPLANATION
C 458 OF CURSOR TYPE SEE AH = 01 IN THE INTERFACE
C 459 SECTION.
C 460
C 461 DWORD_4 GRAPHICS MODE AUXILIARY POINTER
C 462 INITIALIZED TO 0000:0000, THIS VALUE IS OPTIONAL.
C 463 WHEN NON-ZERO, THIS POINTER IS USED AS A POINTER
C 464 TO A TABLES DESCRIBED AS FOLLOWS :
C 465
C 466 BYTE DISPLAYABLE ROWS
C 467 WORD BYTES PER CHARACTER
C 468 DWORD POINTER TO A FONT TABLE
C 469 BYTE CONSECUTIVE BYTES OF MODE VALUES FOR WHICH
C 470 THIS FONT DESCRIPTION IS TO BE USED.
C 471 THE END OF THIS STREAM IS INDICATED BY A
C 472 BYTE CODE OF 'FF'
C 473
C 474 DWORD_5 THRU DWORD_7 RESERVED AND SET TO 0000:0000.
C 475
C 476
C 477
C 0500 ?? 478
C 0500 ?? 479
C 0501 480
C 481
C 482 C PORT_B EQU 61H ; 8255 PORT B ADDR
C 483 C TIMER EQU 40H
C 484
C 485 ;----- EQUATES FOR CARD PORT ADDRESSES
C 486
C 487 SEQ_ADDR EQU 0C4H
C 488 SEQ_DATA EQU 0C5H
C 489 CRTC_ADDR EQU 0D4H
C 490 CRTC_ADDR_B EQU 0B4H
C 491 CRTC_DATA EQU 0D5H ; OR 0B5H
C 492 GRAPH_1_POS EQU 0CCH
C 493 GRAPH_2_POS EQU 0CAH
C 494 GRAPH_ADDR EQU 0CEH
C 495 GRAPH_DATA EQU 0CFH
C 496 MISC_OUTPUT EQU 0C2H
C 497 IN_STAT_0 EQU 0C2H
C 498 INPUT_STATUS_B EQU 0BAH
C 499 INPUT_STATUS EQU 0BAH
C 500 ATTR_READ EQU 0BAH
C 501 ATTR_WRITE EQU 0C0H
C 502
C 503 ;----- EQUATES FOR ADDRESS REGISTER VALUES
C 504
C 505

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= 0000      505      C      S_RESET      EQU      00H
= 0001      506      C      S_CLOCK      EQU      01H
= 0002      507      C      S_MAP      EQU      02H
= 0003      508      C      S_CGEN      EQU      03H
= 0004      509      C      S_MEM      EQU      04H
= 0000      510      C
= 0001      511      C      C_HRZ_TOT      EQU      00H
= 0002      512      C      C_HRZ_DSP      EQU      01H
= 0003      513      C      C_STRT_HRZ_BLK      EQU      02H
= 0004      514      C      C_END_HRZ_BLK      EQU      03H
= 0005      515      C      C_STRT_HRZ_SYN      EQU      04H
= 0006      516      C      C_END_HRZ_SYN      EQU      05H
= 0007      517      C      C_VRT_TOT      EQU      06H
= 0008      518      C      C_COVERFLOW      EQU      07H
= 0009      519      C      C_PRE_ROW      EQU      08H
= 000A      520      C      C_MAX_SCAN_LN      EQU      09H
= 000B      521      C      C_CRSR_START      EQU      0AH
= 000C      522      C      C_CRSR_END      EQU      0BH
= 000D      523      C      C_STRT_HGH      EQU      0CH
= 000E      524      C      C_STRT_LOW      EQU      0DH
= 000F      525      C      C_CRSR_LOC_HGH      EQU      0EH
= 0010      526      C      C_CRSR_LOC_LOW      EQU      0FH
= 0011      527      C      C_VRT_SYN_STRT      EQU      10H      ; WRITE ONLY
= 0012      528      C      C_LGHT_PEN_HGH      EQU      10H      ; READ ONLY
= 0013      529      C      C_VRT_SYN_END      EQU      11H      ; WRITE ONLY
= 0014      530      C      C_LGHT_PEN_LOW      EQU      11H      ; READ ONLY
= 0015      531      C      C_VRT_DSP_END      EQU      12H
= 0016      532      C      C_OFFSET      EQU      13H
= 0017      533      C      C_UNDERLN_LOC      EQU      14H
= 0018      534      C      C_STRT_VRT_BLK      EQU      15H
= 0019      535      C      C_END_VRT_BLK      EQU      16H
= 001A      536      C      C_MODE_CNTL      EQU      17H
= 001B      537      C      C_LN_COMP      EQU      18H
= 001C      538      C
= 001D      539      C      G_SET_RESET      EQU      00H
= 001E      540      C      G_ENBL_SET      EQU      01H
= 001F      541      C      G_CLR_COMP      EQU      02H
= 0020      542      C      G_DATA_ROT      EQU      03H
= 0021      543      C      G_READ_MAP      EQU      04H
= 0022      544      C      G_MODE      EQU      05H
= 0023      545      C      G_MISC      EQU      06H
= 0024      546      C      G_COLOR      EQU      07H
= 0025      547      C      G_BIT_MASK      EQU      08H
= 0026      548      C
= 0027      549      C      P_MODE      EQU      10H
= 0028      550      C      P_OVERSC      EQU      11H
= 0029      551      C      P_CPLANE      EQU      12H
= 0030      552      C      P_HPFL      EQU      13H
= 0031      553      C
= 0032      554      C      SUBTTL
= 0033      555      C
= 0034      556      C      ;----- CODE SEGMENT
= 0035      557      C
= 0036      558      C      CODE      SEGMENT      PUBLIC
= 0037      559      C
= 0038      560      C      INCLUDE      VPOST.INC
= 0039      561      C      SUBTTL      VPOST.INC
= 0040      562      C      PAGE
= 0041      563      C
= 0042      564      C      ;----- POST
= 0043      565      C
= 0044      566      C      ASSUME      CS:CODE,DS:ABS0
= 0045      567      C      ORG      0H
= 0046      568      C      DB      055H      ; SIGNATURE
= 0047      569      C      DB      0A0H      ; BYTES
= 0048      570      C      DB      020H      ; LENGTH INDICATOR
= 0049      571      C
= 0050      572      C      ;----- NOTE : DO NOT USE THE SIGNATURE BYTES AS A PRESENCE TEST
= 0051      573      C
= 0052      574      C      ;
= 0053      575      C      PLANAR VIDEO SWITCH SETTINGS
= 0054      576      C      ;
= 0055      577      C      ; 0 0 - UNUSED
= 0056      578      C      ; 0 1 - 40 X 25 COLOR
= 0057      579      C      ; 1 0 - 80 X 25 COLOR
= 0058      580      C      ; 1 1 - 80 X 25 MONOCHROME
= 0059      581      C      ; NOTE : 0 0 MUST BE SET WHEN THIS ADAPTER IS INSTALLED.
= 0060      582      C      ;
= 0061      583      C      VIDEO ADAPTER SWITCH SETTINGS
= 0062      584      C      ;
= 0063      585      C      ; 0 0 0 - MONOC PRIMARY, EGA COLOR, 40X25
= 0064      586      C      ; 0 0 1 - MONOC PRIMARY, EGA COLOR, 80X25
= 0065      587      C      ; 0 0 1 0 - MONOC PRIMARY, EGA HI RES EMULATE (SAME AS 0001)
= 0066      588      C      ; 0 0 1 1 - MONOC PRIMARY, EGA HI RES ENHANCED
= 0067      589      C      ; 0 1 0 0 - COLOR 40 PRIMARY, EGA MONOCHROME
= 0068      590      C      ; 0 1 0 1 - COLOR 80 PRIMARY, EGA MONOCHROME
= 0069      591      C      ;
= 0070      592      C      ; 0 1 1 0 - MONOC SECONDARY, EGA COLOR, 40X25
= 0071      593      C      ; 0 1 1 1 - MONOC SECONDARY, EGA COLOR, 80X25
= 0072      594      C      ; 1 0 0 0 - MONOC SECONDARY, EGA HI RES EMULATE (SAME AS 0111)
= 0073      595      C      ; 1 1 0 0 - MONOC SECONDARY, EGA HI RES ENHANCED
= 0074      596      C      ; 1 0 1 0 - COLOR 40 SECONDARY, EGA MONOCHROME
= 0075      597      C      ; 1 0 1 1 - COLOR 80 SECONDARY, EGA MONOCHROME
= 0076      598      C      ;
= 0077      599      C      ; 1 1 0 0 - RESERVED
= 0078      600      C      ; 1 1 0 1 - RESERVED
= 0079      601      C      ; 1 1 1 0 - RESERVED
= 0080      602      C      ; 1 1 1 1 - RESERVED
= 0081      603      C
= 0082      604      C      ;----- SETUP ROUTINE FOR THIS MODULE
= 0083      605      C      VIDEO_SETUP      PROC      FAR
= 0084      606      C      JMP      SHORT      L1
= 0085      607      C      DB      '2400'
= 0086      608      C      DB      '6277356 (C)COPYRIGHT IBM 1984'
= 0087      609      C
= 0088      610      C
= 0089      611      C
= 0090      612      C      DB      '9/13/84'
= 0091      613      C
= 0092      614      C
= 0093      615      C
= 0094      616      C      ;----- SET UP VIDEO VECTORS
= 0095      617      C
= 0096      618      C      L1:
= 0097      619      C      MOV      DH,3
= 0098      620      C      MOV      DL,INPUT_STATUS
= 0099      621      C      IN      AL,DX
= 0100      622      C      MOV      DL,INPUT_STATUS_B
= 0101      623      C      IN      AL,DX
= 0102      624      C      MOV      DL,ATTR_WRITE
= 0103      625      C      MOV      AL,0
= 0104      626      C      OUT     DX,AL
= 0105      627      C
= 0106      628      C      SRLOAD   DS,0
= 0107      629      C      SUB      DX,DX
= 0108      630      C      MOV      DS,DX

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003E FA 631 C CLI
003F C7 06 0040 R OCD7 R 632 C MOV WORD PTR VIDEO_OFFSET COMBO_VIDEO
0045 8C 0E 0042 R 633 C MOV WORD PTR VIDEO+2, CS
0049 C7 06 0108 R F065 634 C MOV WORD PTR PLANAR_VIDEO_0F065H
004F C7 06 010A R F000 635 C MOV WORD PTR PLANAR_VIDEO+2_0F000H
0055 C7 06 00A8 R 010C R 636 C MOV WORD PTR SAVE_PTR_OFFSET SAVE_TBL
005B 8C 0E 00AA R 637 C MOV WORD PTR SAVE_PTR+2, CS
005F C7 06 007C R 0000 E 638 C MOV WORD PTR EXT_PTR_OFFSET INT_1F_1
0065 8C 0E 007E R 639 C MOV WORD PTR EXT_PTR+2, CS
0069 C7 06 010C R 0000 E 640 C MOV WORD PTR GRX_SET_OFFSET CDDOT
006F 8C 0E 010E R 641 C MOV WORD PTR GRX_SET+2, CS
0073 FB 642 C STI
643 C
644 C ;---- POST FOR COMBO VIDEO CARD
645 C
0074 C6 06 0487 R 04 646 C MOV INFO,00000100B
0079 E8 009B R 647 C RD_SWS
007C 8B 1E 0488 R 648 C MOV INFO_3,BL
0080 E8 00CE R 649 C CALL F_BTS
0083 0B 06 0488 R 650 C OR INFO_3,AL
0087 8A 1E 0488 R 651 C MOV BL,INFO_3
008B E8 00F3 R 652 C CALL MK_ENV
008E E9 0244 R 653 C JMP POST
0091 C SKIP:
0091 CB 654 C RET
0092 655 C VIDEO_SETUP ENDP
656 C
0092 658 C
0092 EE 659 C POR_1 PROC NEAR
0093 50 660 C OUT DX,AL
0094 58 661 C PUSH AX
0095 EC 662 C POP AX
0096 24 10 663 C IN AL,DX
0098 D0 E8 664 C AND AL,010H
009A C3 665 C SHR AL,1
009B 666 C RET
667 C POR_1 ENDP
668 C
669 C ;---- READ THE SWITCH SETTINGS ON THE CARD
670 C
009B 671 C RD_SWS PROC NEAR
009B 86 03 672 C DS:ABS0
009D B2 C2 673 C MOV DH,3
009F B0 01 674 C MOV DL,MISC_OUTPUT
00A1 EE 675 C MOV AL,1
676 C OUT DX,AL
677 C
678 C ;---- COULD BE 0,4,8,C
679 C
00A2 80 0D 680 C MOV AL,0DH
00A4 E8 0092 R 681 C CALL POR_1
00A7 D0 E8 682 C SHR AL,1
00A9 D0 E8 683 C SHR AL,1
00AB D0 E8 684 C SHR AL,1
00AD 8A D8 685 C MOV BL,AL
686 C
00AF 80 09 687 C MOV AL,9
00B1 E8 0092 R 688 C CALL POR_1
00B4 D0 E8 689 C SHR AL,1
00B6 D0 E8 690 C SHR AL,1
00B8 0A D8 691 C OR BL,AL
692 C
00BA 80 05 693 C MOV AL,5
00BC E8 0092 R 694 C CALL POR_1
00BF D0 E8 695 C SHR AL,1
00C1 0A D8 696 C OR BL,AL
697 C
00C3 80 01 698 C MOV AL,1
00C5 E8 0092 R 699 C CALL POR_1
00C8 0A D8 700 C OR BL,AL
701 C
00CA 80 E3 0F 702 C AND BL,0FH
00CD C3 703 C RET
00CE 704 C RD_SWS ENDP
705 C
706 C ;---- OBTAIN THE FEATURE BITS FROM DAUGHTER CARD
707 C
00CE 707 C F_BTS PROC NEAR
00CE 86 03 708 C MOV DH,3
00D0 B2 BA 709 C MOV DL,0BAH
00D2 B0 01 710 C MOV AL,1
00D4 EE 711 C OUT DX,AL
00D5 B2 DA 712 C MOV DL,0DAH
00D7 EE 713 C OUT DX,AL
00D8 B2 C2 714 C MOV DL,IN_STAT_0
00DA EC 715 C IN AL,DX ; READ FEATURE BITS
00DB 24 60 716 C AND AL,060H
00DD D0 E8 717 C SHR AL,1
00DF 8A D8 718 C MOV BL,AL
00E1 B2 BA 719 C MOV DL,0BAH
00E3 80 02 720 C MOV AL,2
00E5 EE 721 C OUT DX,AL
00E7 B2 DA 722 C MOV DL,0DAH
00E8 EE 723 C OUT DX,AL
00E9 B2 C2 724 C MOV DL,IN_STAT_0 ; READ FEATURE BITS
00EB EC 725 C IN AL,DX
00EC 24 60 726 C AND AL,060H
00EE D0 E0 727 C SHL AL,1
00F0 0A C3 728 C OR AL,BL
00F2 C3 729 C RET
00F3 730 C F_BTS ENDP
731 C
732 C ;---- ESTABLISH THE VIDEO ENVIRONMENT, KEYED OFF OF THE SWITCHES
733 C
00F3 734 C MK_ENV PROC NEAR
00F3 2A FF 735 C SUB BH,BH
00F5 80 E3 0F 736 C AND BL,0FH
00F8 D1 E3 737 C SAL BX,1
00FA 52 738 C PUSH DX
00FB B6 03 739 C MOV DH,3
00FD 8A E6 740 C MOV AH,DH
00FF 5A 741 C POP DX
0100 80 E4 01 742 C AND AH,1
0103 FE C4 743 C INC AH
0105 F6 D4 744 C NOT AH
0107 2E FF A7 0128 R 745 C JMP WORD PTR CS:[BX + OFFSET T5]
746 C
747 C
748 C
010C 749 C SAVE_TBL LABEL DWORD
010C 0717 R 750 C DW OFFSET VIDEO_PARMS ; PARMS
010E C000 751 C DW 0CDD00H ; PARMS
0110 0000 752 C DW 0 ; PAL SAVE AREA
0112 0000 753 C DW 0 ; PAL SAVE AREA
0114 0000 754 C DW 0 ; ALPHA TABLES
0116 0000 755 C DW 0 ; ALPHA TABLES
0118 0000 756 C DW 0 ; GRAPHICS TABLES

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011A 0000      757 C      DW      0      ; GRAPHICS TABLES
011C 0000      758 C
011E 0000      759 C      DW      0
0120 0000      760 C      DW      0
0122 0000      761 C      DW      0
0124 0000      762 C      DW      0
0126 0000      763 C      DW      0
0128 0000      764 C      DW      0
0128 0173 R    765 C
012A 017E R    766 C      T5 LABEL WORD
012C 017E R    767 C      DW      OFFSET PST_0
012E 0189 R    768 C      DW      OFFSET PST_1
0130 0194 R    769 C      DW      OFFSET PST_2
0132 01A8 R    770 C      DW      OFFSET PST_3
0134 01BC R    771 C      DW      OFFSET PST_4
0136 01C7 R    772 C      DW      OFFSET PST_5
0138 01C7 R    773 C      DW      OFFSET PST_6
013A 01D2 R    774 C      DW      OFFSET PST_7
013C 01DD R    775 C
013E 01F1 R    776 C      DW      OFFSET PST_8
0140 0204 R    777 C      DW      OFFSET PST_9
0142 0204 R    778 C      DW      OFFSET PST_A
0144 0204 R    779 C      DW      OFFSET PST_B
0146 0204 R    780 C      DW      OFFSET PST_OUT
0148 0204 R    781 C      DW      OFFSET PST_OUT
014A 0204 R    782 C      DW      OFFSET PST_OUT
014C 0204 R    783 C      DW      OFFSET PST_OUT
014E 0204 R    784 C
0148 80 26 0410 R CF 785 C      ENV_X PROC NEAR ; SET 40X25 COLOR ALPHA
014A 80 26 0410 R CF 786 C      AND EQUI_P_LOW,OCFH
014C 80 0E 0410 R 10 787 C      OR EQUI_P_LOW,010H
0152 B8 0001 788 C      MOV AX,1H
0154 CD 10 789 C      INT 10H
0157 C3 790 C      RET
0158 03 791 C      ENV_X ENDP
0158 792 C
0158 80 26 0410 R CF 793 C      ENV_0 PROC NEAR ; SET 80X25 COLOR ALPHA
015A 80 0E 0410 R 20 794 C      AND EQUI_P_LOW,OCFH
0162 B8 0003 795 C      OR EQUI_P_LOW,020H
0164 CD 10 796 C      MOV AX,03H
0166 C3 797 C      INT 10H
0167 03 798 C      ENV_0 ENDP
0168 8000 799 C
0168 80 0E 0410 R 30 800 C      ENV_3 PROC NEAR ; SET MONOCHROME ALPHA
016D B8 0007 801 C      OR EQUI_P_LOW,030H
0170 CD 10 802 C      MOV AX,07H
0172 C3 803 C      INT 10H
0173 03 804 C      ENV_3 ENDP
0173 805 C
0173 806 C
0173 807 C
0173 808 C
0173 809 C      PST_0:
0173 20 26 0487 R 810 C      AND INFO,AH
0177 EB 0168 R 811 C      CALL ENV_X
017A EB 0168 R 812 C      CALL ENV_3
017D C3 813 C      RET
017E 03 814 C      PST_1:
017E 815 C      PST_2:
017E 20 26 0487 R 816 C      AND INFO,AH
0182 EB 0158 R 817 C      CALL ENV_0
0185 EB 0168 R 818 C      CALL ENV_3
0188 C3 819 C      RET
0189 820 C      PST_3:
0189 20 26 0487 R 821 C      AND INFO,AH
018D EB 0158 R 822 C      CALL ENV_0
0190 EB 0168 R 823 C      CALL ENV_3
0193 C3 824 C      RET
0194 825 C      PST_4:
0194 B6 03 826 C      MOV DH,3
0196 B2 C2 827 C      MOV DL,MISC_OUTPUT
0198 B0 00 828 C      MOV AL,0
019A EE 829 C      OUT DX,AL
019C F6 04 830 C      NOT AH
019D 08 26 0487 R 831 C      OR INFO,AH
01A1 EB 0168 R 832 C      CALL ENV_3
01A4 EB 0148 R 833 C      CALL ENV_X
01A7 C3 834 C      RET
01A8 835 C      PST_5:
01A8 B6 03 836 C      MOV DH,3
01AA B2 C2 837 C      MOV DL,MISC_OUTPUT
01AC B0 00 838 C      MOV AL,0
01AE EE 839 C      OUT DX,AL
01AF F6 04 840 C      NOT AH
01B1 08 26 0487 R 841 C      OR INFO,AH
01B5 EB 0168 R 842 C      CALL ENV_3
01B8 EB 0158 R 843 C      CALL ENV_0
01BB C3 844 C      RET
01BC 845 C      PST_6:
01BC 20 26 0487 R 846 C      AND INFO,AH
01C0 EB 0168 R 847 C      CALL ENV_3
01C3 EB 0148 R 848 C      CALL ENV_X
01C6 C3 849 C      RET
01C7 850 C      PST_7:
01C7 851 C      PST_8:
01C7 20 26 0487 R 852 C      AND INFO,AH
01CB EB 0168 R 853 C      CALL ENV_3
01CE EB 0158 R 854 C      CALL ENV_0
01D1 C3 855 C      RET
01D2 856 C      PST_9:
01D2 20 26 0487 R 857 C      AND INFO,AH
01D6 EB 0168 R 858 C      CALL ENV_3
01D9 EB 0158 R 859 C      CALL ENV_0
01DC C3 860 C      RET
01DD 861 C      PST_A:
01DD B6 03 862 C      MOV DH,3
01DF B2 C2 863 C      MOV DL,MISC_OUTPUT
01E1 B0 00 864 C      MOV AL,0
01E3 EE 865 C      OUT DX,AL
01E4 F6 04 866 C      NOT AH
01E6 08 26 0487 R 867 C      OR INFO,AH
01EA EB 0148 R 868 C      CALL ENV_X
01ED EB 0168 R 869 C      CALL ENV_3
01F0 C3 870 C      RET
01F1 871 C      PST_B:
01F1 B6 03 872 C      MOV DH,3
01F3 B2 C2 873 C      MOV DL,MISC_OUTPUT
01F5 B0 00 874 C      MOV AL,0
01F7 EE 875 C      OUT DX,AL
01F8 F6 04 876 C      NOT AH
01FA 08 26 0487 R 877 C      OR INFO,AH
01FE EB 0158 R 878 C      CALL ENV_0
0201 EB 0168 R 879 C      CALL ENV_3
0204 880 C      PST_OUT:
0204 C3 881 C      RET
0205 882 C      MK_ENV ENDP

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0294 74 07      1009 C      JE      E10      ; YES - SKIP VIDEO RAM TEST
0296 8E DB      1010 C      MOV     DS,BX   ; POINT DS TO VIDEO RAM STG
          1011 C      ASSUME DS:NOTHING,ES:NOTHING
0298 E8 02DF R  1012 C      CALL   STGTST_CNT ; GO TEST VIDEO R/W STC
029B 75 2E      1013 C      LINE  E17      ; R/W STC FAILURE - BEEP SPK
          1014 C      -----
          1015 C      SETUP VIDEO DATA ON SCREEN FOR VIDEO LINE TEST.
          1016 C      DESCRIPTION
          1017 C      ENABLE VIDEO SIGNAL AND SET MODE.
          1018 C      DISPLAY A HORIZONTAL BAR ON SCREEN.
          1019 C      -----
029D           1020 C      E10:
029D 58          1021 C      POP     AX      ; GET VIDEO SENSE SWS (AH)
029E 50          1022 C      PUSH   AX      ; SAVE IT
029F B8 7020    1023 C      MOV     AX,7020H ; WRT BLANKS IN REVERSE VIDEO
02A2 2B FF      1024 C      SUB     DI,DI    ; SETUP STARTING LOC
02A4 B9 0028    1025 C      MOV     CX,0     ; NO. OF BLANKS TO DISPLAY
02A7 F3/ AB     1026 C      REP     STOSB   ; WRITE VIDEO STORAGE
          1027 C      -----
          1028 C      CRT INTERFACE LINES TEST
          1029 C      DESCRIPTION
          1030 C      SENSE ON/OFF TRANSITION OF THE VIDEO ENABLE
          1031 C      AND HORIZONTAL SYNC LINES.
          1032 C      -----
02A9 58          1033 C      POP     AX      ; GET VIDEO SENSE SW INFO
02AA 50          1034 C      PUSH   AX      ; SAVE IT
02AB 80 FC 30   1035 C      CMP     AH,30H  ; R/W CARD ATTACHED?
02AE BA 03BA    1036 C      MOV     DX,03BAH ; SETUP ADDR OF BW STATUS PORT
02B1 74 02      1037 C      JZ     E11      ; YES - GO TEST LINES
02B3 82 DA      1038 C      MOV     DL,0DAH ; COLOR CARD IS ATTACHED
02B5           1039 C      E11:
02B5 84 08      1040 C      MOV     AH,8    ; LINE_TST:
02B7           1041 C      E12:
02B7 2B C9      1042 C      SUB     CX,CX   ; OFLOOP_CNT:
02B9 EC          1043 C      E13:
02B9 EC          1044 C      IN     AL,DX   ; READ CRT STATUS PORT
02BA 22 C4      1045 C      AND    AL,AH   ; CHECK V/HORZ LINE
02BC 75 04      1046 C      JNZ    E14     ; ITS ON - CHECK IF IT GOES OFF
02BE E2 F9      1047 C      LOOP  E13     ; LOOP TILL OK OR TIMEOUT
02C0 EB 09      1048 C      JMP     SHORT E17 ; GO PRINT ERROR MSG
02C2           1049 C      E14:
02C2 2B C9      1050 C      SUB     CX,CX   ; OFLOOP_CNT:
02C4           1051 C      E15:
02C4 EC          1052 C      IN     AL,DX   ; READ CRT STATUS PORT
02C5 22 C4      1053 C      AND    AL,AH   ; CHECK V/HORZ LINE
02C7 74 0A      1054 C      JZ     E16     ; ITS ON - CHECK NEXT LINE
02C9 E2 F9      1055 C      LOOP  E15     ; LOOP IF OFF TILL IT GOES ON
02CB           1056 C      E17:
02CB BA 0102    1057 C      MOV     DX,102H ; CRT_ERR
02CE E8 06CB R  1058 C      CALL   ERR_BEEP ; GO BEEP SPEAKER
02D1 EB 06      1059 C      JMP     SHORT E18 ;
02D3 01 03      1060 C      MOV     CL,3    ; NXT_LINE
02D5 D2 EC      1061 C      SHR    AH,CL   ; GET NEXT BIT TO CHECK
02D7 75 DE      1062 C      JNZ    E18     ;
02D9           1063 C      E18:
02D9 58          1064 C      GO CHECK HORIZONTAL LINE
02DA EB 38      1065 C      JMP     SHORT POD14 ; DISPLAY CURSOR:
          1066 C      ; GET VIDEO SENSE SWS (AH)
          1067 C      -----
02E6           1068 C      THIS SUBROUTINE PERFORMS A READ/WRITE STORAGE TEST ON
          1069 C      A 16K BLOCK OF STORAGE.
          1070 C      -----
          1071 C      ENTRY
          1072 C      ES = ADDRESS OF STORAGE SEGMENT BEING TESTED
          1073 C      DS = ADDRESS OF STORAGE SEGMENT BEING TESTED
          1074 C      WHEN ENTERING AT STGTST_CNT, CX MUST BE LOADED WITH
          1075 C      THE BYTE COUNT.
          1076 C      EXIT PARAMETERS:
          1077 C      ZERO FLAG = 0 IF STORAGE ERROR (DATA COMPARE OR PARITY CHECK.
          1078 C      AL = 0 DENOTES A PARITY CHECK, ELSE AL=XOR'ED BIT
          1079 C      PATTERN OF THE EXPECTED DATA PATTERN VS THE
          1080 C      ACTUAL DATA READ.
          1081 C      AX,BX,CX,DX,DI, AND SI ARE ALL DESTROYED.
          1082 C      -----
02DC           1083 C      STGTST PROC NEAR
02DC B9 4000    1084 C      MOV     CX,4000H ; SETUP CNT TO TEST A 16K BLK
02DF           1085 C      STGTST_CNT:
02DF FC          1086 C      CLD
02E0 8B D9      1087 C      MOV     BX,CX  ; SET DIR FLAG TO INCREMENT
02E2 B8 AAAA    1088 C      MOV     AX,AAAAH ; SAVE CNT (4K FOR VIDEO OR 16K)
02E5 BA FF55    1089 C      MOV     DX,OFF55H ; GET DATA PATTERN TO WRITE
02E8 2B FF      1090 C      SUB     DI,DI  ; SETUP OTHER DATA PATTERNS TO USE
02EA F3/ AA     1091 C      REP     STOSB ; D1 = OFFSET 0 RELATIVE TO ES REG
          1092 C      ; WRITE STORAGE LOCATIONS
          1093 C      ; STG0:
02EC 4F          1094 C      DEC     DI    ; POINT TO LAST BYTE JUST WRITTEN
02ED FD          1095 C      STD     DI    ; SET DIR FLAG TO GO BACKWARDS
02EE 8B F7      1096 C      MOV     SI,DI  ;
02F0 8B CB      1097 C      MOV     CX,BX ;
          1098 C      C3:
02F2 AC          1099 C      LODSB
02F3 32 C4      1100 C      XOR     AL,AH  ; SETUP BYTE CNT
02F5 75 1E      1101 C      JNZ    C4     ; INNER TEST LOOP
02F7 8A C2      1102 C      MOV     AL,DL  ; READ OLD TEST BYTE [SI]+
02F9 AA          1103 C      STOSB        ; DATA READ AS EXPECTED ?
02FA E2 F6      1104 C      LOOP  C5     ; NO - GO TO ERROR ROUTINE
          1105 C      ; GET NEXT DATA PATTERN TO WRITE
          1106 C      ; WRITE INTO LOCATION JUST READ
          1107 C      ; DECREMENT COUNT AND LOOP CX
          1108 C      ;
          1109 C      ; ENDING 0 PATTERN WRITTEN TO STG?
          1110 C      ; YES - RETURN TO CALLER WITH AL=0
          1111 C      ; SETUP NEW VALUE FOR COMPARE
          1112 C      ; MOVE NEXT DATA PATTERN TO DI
          1113 C      ; READING ZERO PATTERN THIS PASS ?
          1114 C      ; CONTINUE TEST SEQUENCE TILL 0
          1115 C      ; ELSE SET 0 FOR END READ PATTERN
          1116 C      ; AND MAKE FINAL BACKWARDS PASS
          1117 C      ;
          1118 C      ; SET DIR FLAG TO GO FORWARD
          1119 C      ; SET POINTER TO BEG LOCATION
          1120 C      ; READ/WRITE FORWARD IN STG
          1121 C      ; ADJUST POINTER
          1122 C      ; READ/WRITE BACKWARD IN STG
          1123 C      ;
          1124 C      C6X:
          1125 C      MOV     AL,000H ; AL=0 DATA COMPARE OK
          1126 C      CLD
          1127 C      ; SET DIRECTION FLAG BACK TO INC
          1128 C      RET
          1129 C      STGTST ENDP
          1130 C      -----
          1131 C      EGA CRT ATTACHMENT TEST
          1132 C      ;
          1133 C      ; 1. INIT CRT TO 40X25 - BW ***** TO MODE*****
          1134 C      ; 2. CHECK FOR VERTICAL AND VIDEO ENABLES, AND CHECK
          1135 C      ; 3. CHECK VERTICAL INTERRUPT
          1136 C      ; 4. CHECK RED, BLUE, GREEN, AND INTENSIFY DOTS

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1135 C ; 5. INIT TO 40X25 - COLOR/MONO ****SET TO MODE**** :
1136 C ;-----
1137 C ;-----
1138 C ;----- NOMINAL TIME IS B266H FOR 60 HZ.
1139 C ;----- NOMINAL TIME IS A2FEH FOR 50 HZ.
1140 C
1141 C MAX_VERT_COLOR EQU 0A0ACH ; MAX TIME FOR VERT/VERT
1142 C ; (NOMINAL * 10X)
1143 C MIN_VERT_COLOR EQU 0C460H ; MIN TIME FOR VERT/VERT
1144 C ; (NOMINAL - 10X)
1145 C CENAB_PER_FRAME EQU 200 ; NUM OF ENABLES PER FRAME
1146 C MAX_VERT_MONO EQU 0B899H ; MAX TIME FOR VERT/VERT
1147 C ; (NOMINAL * 10X)
1148 C MIN_VERT_MONO EQU 0B862H ; MIN TIME FOR VERT/VERT
1149 C ; (NOMINAL - 10X)
1150 C EENAB_PER_FRAME EQU 350 ; ENHANCED ENABLES PER FRAME
1151 C MENAB_PER_FRAME EQU 350 ; NUM OF ENABLES PER FRAME
1152 C
1153 C TIM_CTL EQU 043H ; 8253 TIMER CONTROL PORT
1154 C TIMERO EQU 040H ; 8253 TIMER/CNTER 0 PORT
1155 C
1156 C
1157 C POD14 PROC NEAR
1158 C SUB SP,0AH ; RESERVE 5 WORDS ON STACK
1159 C MOV BP,SP ; INIT SCRATCH PAD POINTER
1160 C
1161 C ASSUME DS:ABSO,ES:ABSO
1162 C CALL DDS ; SET TIMER 0 TO MODE 0
1163 C MOV AL,00110000B
1164 C
1165 C OUT TIM_CTL,AL
1166 C ; SEND FIRST BYTE TO TIMER
1167 C TEST INFO,2
1168 C JZ COLOR_EGA_V
1169 C ENV CALL
1170 C MOV WORD PTR[BP][2],MENAB_PER_FRAME ; SET UP IN MONOCHROME
1171 C ; NUM. OF FRAMES FOR MONO
1172 C MOV WORD PTR[BP][4],MAX_VERT_MONO ; MAX TIME FOR VERT/VERT
1173 C ; MIN TIME FOR VERT/VERT
1174 C MOV DL,CRTC_ADDR_B ; MONO CRTC REG
1175 C MOV AH,C_HRZ_DSP ; HORIZ. TOTAL DISPLAY
1176 C MOV AL,27H ; TO 40 COL
1177 C CALL OUT_DX ; 3BA
1178 C JMP SHORT COMMON
1179 C
1180 C COLOR_EGA_V:
1181 C CALL ENV_X ; SET UP IN 40X25 COLOR
1182 C BRST_DET ; ENHANCED MODE
1183 C JNC COLOR_V ; NO,40X25
1184 C MOV DL,CRTC_ADDR ; BRST MODE ONLY!
1185 C MOV AH,1 ; HRZ DSP END
1186 C MOV AL,20 ; MODIFY FOR TEST ONLY
1187 C CALL OUT_DX
1188 C MOV WORD PTR[BP][2],EENAB_PER_FRAME ; NUM. OF FRAMES FOR COLOR
1189 C JMP BRST_COLOR_V
1190 C
1191 C MOV WORD PTR[BP][2],CENAB_PER_FRAME ; NUM. OF FRAMES FOR COLOR
1192 C BRST_COLOR_V:
1193 C MOV WORD PTR[BP][4],MAX_VERT_COLOR ; MAX TIME FOR VERT/VERT
1194 C MOV WORD PTR[BP][6],MIN_VERT_COLOR ; MIN TIME FOR VERT/VERT
1195 C MOV DL,INPUT_STATUS ; SET ADDRESSING TO VIDEO
1196 C ; ATTR STATUS
1197 C
1198 C COMMON:
1199 C MOV AX,0500H ; SET TO VIDEO PAGE 0
1200 C INT 10H
1201 C SUB CX,CX
1202 C
1203 C ;----- LOOK FOR VERTICAL
1204 C
1205 C POD14_1:
1206 C IN AL,DX ; GET STATUS
1207 C TEST AL,00001000B ; VERTICAL THERE YET?
1208 C JNZ PDD14_2 ; CONTINUE IF IT IS
1209 C LOOP PDD14_1 ; KEEP LOOKING TILL COUNT
1210 C MOV BL,00 ; EXHAUSTED
1211 C JMP PDD14_ERR ; NO VERTICAL
1212 C
1213 C ;----- GOT VERTICAL - START TIMER
1214 C
1215 C POD14_2:
1216 C MOV AL,0 ; ENABLE ON YET?
1217 C OUT TIMERO,AL ; GO ON IF IT IS
1218 C ; VERTICAL ON AGAIN?
1219 C SUB BX,BX ; CONTINUE IF IT IS
1220 C ; KEEP LOOKING TILL COUNT
1221 C ; EXHAUSTED
1222 C XOR CX,CX ; VERTICAL STUCK ON
1223 C
1224 C POD14_25:
1225 C IN AL,DX ; GET STATUS
1226 C TEST AL,00001000B ; VERTICAL STILL THERE
1227 C JZ PDD14_3 ; CONTINUE IF IT'S GONE
1228 C LOOP PDD14_25 ; KEEP LOOKING TILL COUNT
1229 C MOV BL,01H ; EXHAUSTED
1230 C JMP PDD14_ERR ; VERTICAL STUCK ON
1231 C
1232 C ;----- NOW START LOOKING FOR ENABLE TRANSITIONS
1233 C
1234 C POD14_3:
1235 C SUB CX,CX
1236 C
1237 C POD14_4:
1238 C IN AL,DX ; GET STATUS
1239 C TEST AL,00000001B ; ENABLE ON YET?
1240 C JE PDD14_5 ; GO ON IF IT IS
1241 C TEST AL,00001000B ; VERTICAL ON AGAIN?
1242 C JNZ PDD14_7S ; CONTINUE IF IT IS
1243 C LOOP PDD14_4 ; KEEP LOOKING IF NOT
1244 C MOV BL,02H ; ENABLE STUCK OFF
1245 C JMP PDD14_ERR
1246 C
1247 C POD14_4A:
1248 C MOV BL,03H ; VERTICAL STUCK ON
1249 C JMP PDD14_ERR
1250 C
1251 C POD14_4B:
1252 C MOV BL,04H ; ENABLE STUCK ON
1253 C JMP PDD14_ERR
1254 C
1255 C ;----- MAKE SURE VERTICAL WENT OFF WITH ENABLE GOING ON
1256 C
1257 C POD14_5:
1258 C TEST AL,00001000B ; VERTICAL OFF?
1259 C JNZ PDD14_4A ; GO ON IF IT IS
1260 C ; (ERROR IF NOT)
1261 C ;----- NOW WAIT FOR ENABLE TO GO OFF
1262 C
1263 C POD14_6:
1264 C IN AL,DX ; GET STATUS
1265 C TEST AL,00000001B ; ENABLE OFF YET?
1266 C LOOPE PDD14_6 ; KEEP LOOKING IF NOT
1267 C JCKZ PDD14_4B ; YES, LOW
1268 C ;----- ENABLE HAS TOGGLED, BUMP COUNTER AND TEST FOR NEXT VERTICAL

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03C5 43 1261 C POD14_7:
03C5 43 1262 C INC BX ; BUMP ENABLE COUNTER
03C6 74 04 1263 C JZ POD14_75 ; IF COUNTER WRAPS,
; SOMETHING IS WRONG
03C8 A8 08 1265 C TEST AL,00001000H ; DID ENABLE GO LOW
; BECAUSE OF VERTICAL
03CA 74 02 1267 C JZ POD14_3 ; IF NOT, LOOK FOR ANOTHER
; ENABLE TOGGLE
; NOW TEST RESULTS
03CC 1270 C ;---- HAVE HAD COMPLETE VERTICAL-VERTICAL CYCLE,
03CC 80 00 1271 C MOV AL,00 ; LATCH TIMER0
03CE E6 43 1272 C OUT TIM_CTL,AL
03D0 3B 5E 02 1273 C CMP BX,WORD PTR[BP][2] ; NUMBER OF ENABLES BETWEEN
; VERTICALS O.K.?
;
03D3 74 04 1275 C JE POD14_8
03D5 83 05 1276 C MOV BL,05H ; BL,05H
03D7 EB 6F 1277 C JMP SHORT POD14_ERR ; SHORT POD14_ERR
;
03D9 E4 40 1278 C IN AL,TIMERO ; GET TIMER VALUE LOW
03DB 8A E0 1279 C MOV AH,AL ; SAVE IT
03DD 90 1281 C NOP
03DE E4 40 1282 C IN AL,TIMERO ; GET TIMER HIGH
03E0 86 E0 1283 C MOV AH,AL
03E2 90 1284 C NOP
03E3 90 1285 C NOP
03E4 3B 46 04 1286 C CMP AX,WORD PTR[BP][4] ; MAXIMUM VERTICAL TIMING
03E7 7D 04 1287 C JGE POD14_9
03E9 83 06 1288 C MOV BL,06H ; BL,06H
03EB 5B 1289 C JMP SHORT POD14_ERR ; SHORT POD14_ERR
;
03ED 3B 46 06 1291 C CMP AX,WORD PTR[BP][6] ; MINIMUM VERTICAL TIMING
03F0 7E 04 1292 C JLE POD14_10
03F2 B3 07 1293 C MOV BL,07H ; BL,07H
03F4 EB 52 1294 C JMP SHORT POD14_ERR ; SHORT POD14_ERR
;
;---- SEE IF RED, GREEN, BLUE AND INTENSIFY DOTS WORK
;
1297 C ;---- FIRST, SET A LINE OF REVERSE VIDEO, INTENSIFIED BLANKS INTO BUFFER
;
03F6 88 09DB 1300 C MOV AX,09DBH ; WRITE CHARS, BLANKS
03F9 BB 000F 1301 C MOV BX,000FH ; PAGE 0, REVERSE VIDEO,
; HIGH INTENSITY
; 80 CHARACTERS
;
03FC 89 0050 1303 C MOV CX,80
03FF CD 10 1304 C INT 10H
0401 EC 1305 C IN AL,DX
0402 52 1306 C PUSH DX ; SAVE INPUT STATUS
0403 B2 C0 1307 C MOV DL,ATTR_WRITE ; ATTRIBUTE ADDRESS
0405 B4 0F 1308 C MOV AH,32H ; PALLETTE REG # F
0407 B0 3F 1309 C MOV AL,03FH ; TEST VALUE
0409 E8 0D15 R 1310 C CALL OUT_DK ; VIDEO STATUS MUX
040C BB 000F 1311 C MOV AX,DX ; START WITH BLUE DOTS
040F 5A 1312 C POP DX
;
0410 50 1313 C
0410 50 1314 C
0411 52 1315 C PUSH AX ; SAVE
; SAVE INPUT STATUS
0412 B2 C0 1316 C MOV DL,ATTR_WRITE ; ATTRIBUTE ADDRESS
0414 B4 32 1317 C MOV AH,32H ; COLOR PLANE ENABLE
0416 EB 0D15 R 1318 C CALL OUT_DK ; VIDEO STATUS MUX
0419 5A 1319 C POP DX ; RECOVER INPUT STATUS
041A 5A 1320 C POP AX
041B 2B C9 1321 C SUB CX,CX
;
041D 1322 C ;---- SEE IF DOT COMES ON
;
041D EC 1323 C
041E A8 30 1324 C IN AL,DX ; GET STATUS
0420 75 09 1325 C JNZ AL,00110000H ; DOT THERE?
0422 F2 19 1326 C LOOP POD14_15 ; LOOK FOR DOT TO TURN OFF
0424 B3 10 1327 C MOV BL,10H ; CONTINUE TEST FOR DOT ON
0426 0A DC 1328 C OR BL,AH ; OR IN DOT BEING TESTED
0428 EB 1E 90 1329 C JMP POD14_ERR ; DOT NOT COMING ON
;
042B 2B C9 1331 C ;---- SEE IF DOT GOES OFF
;
042D EC 1332 C
042D EC 1333 C
042D EC 1334 C
042D EC 1335 C
042E A8 30 1336 C IN AL,DX ; GET STATUS
0430 74 08 1337 C TEST AL,00110000H ; IS DOT STILL ON?
0432 E2 F9 1338 C LOOP POD14_16 ; GO ON IF DOT OFF
; ELSE, KEEP WAITING FOR
; DOT TO GO OFF
;
0434 B3 20 1339 C MOV BL,20H
0436 0A DC 1340 C OR BL,AH ; OR IN DOT BEING TESTED
0438 EB 0E 1341 C JMP SHORT POD14_ERR
;
;---- ADJUST TO POINT TO NEXT DOT
;
043A 1342 C
043A 1343 C
043A 1344 C
043A 1345 C
043C FE C4 1346 C
043C FE C4 1347 C
043C FE C4 1348 C
043F 74 25 1349 C INC AH
0441 80 CC 0F 1350 C CMP AH,030H ; ALL 3 DOTS DONE?
0444 8A C4 1351 C MOV AL,AH ; GO END
0445 EB C8 1352 C JMP POD14_13 ; MAKE OF,1F,2F
; GO LOOK FOR ANOTHER DOT
;
0448 89 0006 1353 C
0448 89 0006 1354 C
0448 89 0006 1355 C
044E EB 06CB R 1356 C MOV CX,6 ; ONE LONG AND THREE SHORT
0451 83 C4 0A 1357 C MOV DX,0103H ; BALANCE STACK
0454 80 36 1358 C CALL ERB_BEEP ; RE-INIT TIMER 0
0456 E6 43 1359 C ADD SP,0AH
0458 2A C0 1360 C MOV AL,00110110B
045A E6 40 1361 C OUT TIM_CTL,AL
045C 90 1362 C SUB AL,AL
045D 90 1363 C OUT TIMERO,AL
045E E6 40 1364 C NOP
0460 BD 0001 R 1365 C OUT TIMERO,AL
0463 E9 0091 R 1366 C MOV BP,0
0466 1367 C JMP SKIP
0466 1368 C ASSUME DS:ABS0
;
0466 EB 0CFE R 1369 C
0469 88 0500 1370 C CALL DDS ; SET TO VIDEO PAGE 0
046C CD 10 1371 C INT 10H
046E B0 36 1372 C MOV AL,00110110B ; RE-INIT TIMER 0
0470 E6 43 1373 C OUT TIM_CTL,AL
0472 2A C0 1374 C SUB AL,AL
0474 E6 40 1375 C OUT TIMERO,AL
0476 90 1376 C NOP
0477 90 1377 C NOP
0478 E6 40 1378 C OUT TIMERO,AL
047A 83 C4 0A 1379 C ADD SP,0AH ; REMOVE SCRATCH PAD
047D BD 0000 1380 C MOV BP,0 ; MAKE BP NON ZERO
0480 1381 C
0480 1382 C
0480 1383 C
;---- TEST STORAGE
;
0480 1E 1384 C
0480 1E 1385 C
0480 1E 1386 C
;
MEM_TEST:
PUSH DS

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0481 E8 0CFE R      1387 C      CALL      DDS
0482 F6 06 0487 R 02 1388 C      ASSUME DS:ABSO
0483 74 12           1389 C      TEST     INFO_2
0484 80 0E 0410 R 30 1390 C      JZ       D_COLOR_M
0485 88 000F       1391 C      OR       EQUIP_LOW,030H
0486 BB 000F       1392 C      MOV     AX,0FH
0487 80 0E 0487 R 60 1393 C      OR       INFO_060H
0488 BB 000F       1394 C      MOV     AX,0FH
0489 EB 0D           1395 C      JMP     SHORT D_OUT_M
0490 EB 0D           1396 C      D_COLOR_M:
0491 80 26 0410 R CF 1397 C      AND     EQUIP_LOW,0CFH
0492 80 0E 0410 R 20 1398 C      OR      EQUIP_LOW,020H
0493 BB 000E       1399 C      MOV     AX,0EH
; INTERNAL COLOR MODE
0494 CD 10         1400 C      D_OUT_M: INT     10H
; TEST IN COLOR
0495 83 EC 06      1401 C      SUB     SP,6
; RESERVE 3 WORDS ON STACK
0496 8B EC         1402 C      MOV     BP,SP
; SET BP
0497 BB A000       1403 C      MOV     AX,0A000H
; PUT BUFFER ADDRESS IN AX
0498 BB A000       1404 C      ASSUME DS:NOTHING,ES:NOTHING
; SET UP SEG REGS TO POINT
; TO BUFFER AREA
0499 8E D8         1405 C      MOV     DS,AX
; INITIALIZE
049A 8E C0         1406 C      MOV     ES,AX
; INITIALIZE
049B C7 46 02 0000 1407 C      MOV     WORD PTR [BP][2],0
; INITIALIZE
049C C7 46 04 0000 1408 C      MOV     WORD PTR [BP][4],0
; INITIALIZE
049D B6 03         1409 C      MOV     DH,3
; INITIALIZE
049E B2 C4         1410 C      DL_SEQ_ADDR
049F B8 0201      1411 C      MOV     AX,0201H
; ADDRESS READ MAP SELECT
0500 EB 0D15 R    1412 C      CALL    OUT_DX
; ADDRESS READ MAP SELECT
0501 B2 CE         1413 C      MOV     DL,GRAPH_ADDR
; ADDRESS READ MAP SELECT
0502 B8 0400      1414 C      MOV     AX,0400H
; ADDRESS READ MAP SELECT
0503 EB 0D15 R    1415 C      CALL    OUT_DX
; ADDRESS READ MAP SELECT
0504 B2 C4         1416 C      PUSH   DX
; ADDRESS READ MAP SELECT
0505 D2 DA         1417 C      MOV     DL,ATTR_READ
; SET UP ATTRIBUTE
0506 EC           1418 C      IN     AL,DX
; ATTRIBUTE WRITE ADDRESS
0507 B2 C0         1419 C      MOV     DL,ATTR_WRITE
; ATTRIBUTE WRITE ADDRESS
0508 B8 3200      1420 C      MOV     AX,3200H
; ATTRIBUTE WRITE ADDRESS
0509 E8 0D15 R    1421 C      CALL    OUT_DX
; ATTRIBUTE WRITE ADDRESS
050A E8 068F R    1422 C      CALL    HOW_BIG
; GO FIND AMOUNT OF MEMORY
050B 80 FC 00      1423 C      CMP     AH,0
; GO FIND AMOUNT OF MEMORY
050C 74 03         1424 C      JZ     AA1
; GO FIND AMOUNT OF MEMORY
050D E9 05CD R    1425 C      JMP     EGA_MEM_ERROR
; GO FIND AMOUNT OF MEMORY
050E E8 05D9 R    1426 C      AA1:   CALL    MEMORY_OK
; GO TEST IT
050F 80 FC 00      1427 C      CMP     AH,0
; GO TEST IT
0510 74 03         1428 C      JZ     AA2
; GO TEST IT
0511 E9 05CD R    1429 C      JMP     EGA_MEM_ERROR
; GO TEST IT
0512 5A           1430 C      AA2:   POP     DX
; GO TEST IT
0513 B2 C4         1431 C      MOV     DL,SEQ_ADDR
; GO TEST IT
0514 B8 0202      1432 C      MOV     AX,0202H
; GO TEST IT
0515 EB 0D15 R    1433 C      CALL    OUT_DX
; GO TEST IT
0516 B2 CE         1434 C      MOV     DL,GRAPH_ADDR
; ADDRESS OF READ MAP
0517 B8 0401      1435 C      MOV     AX,0401H
; ADDRESS OF READ MAP
0518 E8 0D15 R    1436 C      CALL    OUT_DX
; ADDRESS OF READ MAP
0519 52           1437 C      PUSH   DX
; ADDRESS OF READ MAP
0520 B2 DA         1438 C      MOV     DL,ATTR_READ
; SET UP ATTRIBUTE
0521 EC           1439 C      IN     AL,DX
; ATTRIBUTE WRITE ADDRESS
0522 B2 C0         1440 C      MOV     DL,ATTR_WRITE
; ATTRIBUTE WRITE ADDRESS
0523 E8 0D15 R    1441 C      CALL    OUT_DX
; ATTRIBUTE WRITE ADDRESS
0524 C7 46 04 0000 1442 C      MOV     WORD PTR [BP][4],0
; INITIALIZE
0525 E8 068F R    1443 C      CALL    HOW_BIG
; GO FIND AMOUNT OF MEMORY
0526 80 FC 00      1444 C      CMP     AH,0
; GO FIND AMOUNT OF MEMORY
0527 74 03         1445 C      JZ     AA3
; GO FIND AMOUNT OF MEMORY
0528 E9 05CD R    1446 C      JMP     EGA_MEM_ERROR
; GO FIND AMOUNT OF MEMORY
0529 E8 05D9 R    1447 C      AA3:   CALL    MEMORY_OK
; GO TEST IT
052A 80 FC 00      1448 C      CMP     AH,0
; GO TEST IT
052B 74 03         1449 C      JZ     AA4
; GO TEST IT
052C E9 05CD R    1450 C      JMP     EGA_MEM_ERROR
; GO TEST IT
052D 5A           1451 C      AA4:   POP     DX
; GO TEST IT
052E B2 C4         1452 C      MOV     DL,SEQ_ADDR
; GO TEST IT
052F B8 0204      1453 C      MOV     AX,0204H
; GO TEST IT
0530 EB 0D15 R    1454 C      CALL    OUT_DX
; GO TEST IT
0531 52           1455 C      PUSH   DX
; GO TEST IT
0532 B2 CE         1456 C      MOV     DL,GRAPH_ADDR
; ADDRESS OF READ MAP
0533 B8 0402      1457 C      MOV     AX,0402H
; ADDRESS OF READ MAP
0534 E8 0D15 R    1458 C      CALL    OUT_DX
; ADDRESS OF READ MAP
0535 B2 DA         1459 C      MOV     DL,ATTR_READ
; SET UP ATTRIBUTE
0536 EC           1460 C      IN     AL,DX
; ATTRIBUTE WRITE ADDRESS
0537 B2 C0         1461 C      MOV     DL,ATTR_WRITE
; ATTRIBUTE WRITE ADDRESS
0538 E8 0D15 R    1462 C      CALL    OUT_DX
; ATTRIBUTE WRITE ADDRESS
0539 B2 C4         1463 C      MOV     AX,3200H
; ATTRIBUTE WRITE ADDRESS
053A E8 068F R    1464 C      CALL    HOW_BIG
; GO FIND AMOUNT OF MEMORY
053B 80 FC 00      1465 C      CMP     AH,0
; GO FIND AMOUNT OF MEMORY
053C 74 03         1466 C      JZ     AA5
; GO FIND AMOUNT OF MEMORY
053D E9 05CD R    1467 C      JMP     EGA_MEM_ERROR
; GO FIND AMOUNT OF MEMORY
053E E8 05D9 R    1468 C      AA5:   CALL    MEMORY_OK
; GO TEST IT
053F 80 FC 00      1469 C      CMP     AH,0
; GO TEST IT
0540 74 03         1470 C      JZ     AA6
; GO TEST IT
0541 E8 68 90      1471 C      JMP     EGA_MEM_ERROR
; GO TEST IT
0542 5A           1472 C      AA6:   POP     DX
; GO TEST IT
0543 B2 C4         1473 C      MOV     DL,SEQ_ADDR
; GO TEST IT
0544 B8 0204      1474 C      MOV     AX,0204H
; GO TEST IT
0545 EB 0D15 R    1475 C      CALL    OUT_DX
; GO TEST IT
0546 52           1476 C      PUSH   DX
; GO TEST IT
0547 B2 CE         1477 C      MOV     DL,GRAPH_ADDR
; ADDRESS OF READ MAP
0548 B8 0402      1478 C      MOV     AX,0402H
; ADDRESS OF READ MAP
0549 E8 0D15 R    1479 C      CALL    OUT_DX
; ADDRESS OF READ MAP
054A B2 DA         1480 C      MOV     DL,ATTR_READ
; SET UP ATTRIBUTE
054B EC           1481 C      IN     AL,DX
; ATTRIBUTE WRITE ADDRESS
054C B2 C0         1482 C      MOV     DL,ATTR_WRITE
; ATTRIBUTE WRITE ADDRESS
054D E8 0D15 R    1483 C      CALL    OUT_DX
; ATTRIBUTE WRITE ADDRESS
054E C7 46 04 0000 1484 C      MOV     WORD PTR [BP][4],0
; INITIALIZE
054F E8 068F R    1485 C      CALL    HOW_BIG
; GO FIND AMOUNT OF MEMORY
0550 80 FC 00      1486 C      CMP     AH,0
; GO FIND AMOUNT OF MEMORY
0551 74 03         1487 C      JZ     AA5
; GO FIND AMOUNT OF MEMORY
0552 E9 05CD R    1488 C      JMP     EGA_MEM_ERROR
; GO FIND AMOUNT OF MEMORY
0553 E8 05D9 R    1489 C      AA5:   CALL    MEMORY_OK
; GO TEST IT
0554 80 FC 00      1490 C      CMP     AH,0
; GO TEST IT
0555 74 03         1491 C      JZ     AA6
; GO TEST IT
0556 E8 68 90      1492 C      JMP     EGA_MEM_ERROR
; GO TEST IT
0557 5A           1493 C      AA6:   POP     DX
; GO TEST IT
0558 B2 C4         1494 C      MOV     DL,SEQ_ADDR
; GO TEST IT
0559 B8 020B      1495 C      MOV     AX,020BH
; GO TEST IT
055A EB 0D15 R    1496 C      CALL    OUT_DX
; GO TEST IT
055B 52           1497 C      PUSH   DX
; GO TEST IT
055C B2 CE         1498 C      MOV     DL,GRAPH_ADDR
; ADDRESS OF READ MAP
055D B8 0403      1499 C      MOV     AX,0403H
; ADDRESS OF READ MAP
055E E8 0D15 R    1500 C      CALL    OUT_DX
; ADDRESS OF READ MAP
055F 52           1501 C      PUSH   DX
; ADDRESS OF READ MAP
0560 B2 DA         1502 C      MOV     DL,ATTR_READ
; SET UP ATTRIBUTE
0561 EC           1503 C      IN     AL,DX
; ATTRIBUTE WRITE ADDRESS
0562 B2 C0         1504 C      MOV     DL,ATTR_WRITE
; ATTRIBUTE WRITE ADDRESS
0563 E8 0D15 R    1505 C      CALL    OUT_DX
; ATTRIBUTE WRITE ADDRESS
0564 C7 46 04 0000 1506 C      MOV     WORD PTR [BP][4],0
; INITIALIZE
0565 E8 068F R    1507 C      CALL    HOW_BIG
; GO FIND AMOUNT OF MEMORY
0566 80 FC 00      1508 C      CMP     AH,0
; GO FIND AMOUNT OF MEMORY
0567 75 3D         1509 C      JNZ    EGA_MEM_ERROR
; GO TEST IT
0568 E8 05D9 R    1510 C      CALL    MEMORY_OK
; GO TEST IT
0569 80 FC 00      1511 C      CMP     AH,0
; GO TEST IT
056A 75 35         1512 C      JNZ    EGA_MEM_ERROR
; GO TEST IT
056B 55           1513 C      PUSH   BP
; SAVE SCRATCH PAD POINTER
056C B0 0000       1514 C      MOV     BP,0
; RESET BP FOR XT
056D 5C           1515 C      EGEMEM_EXIT: SI
; RESTORE
056E 5E           1516 C      POP     DX
; RESTORE
056F 8B 0CFE R    1517 C      MOV     DI,0CFEH
; SET DATA SEGMENT
0570 5A           1518 C      ASSUME DS:ABSO
; SET DATA SEGMENT
0571 36: BB 5C 02  1519 C      MOV     BX,WORD PTR SS:[SI][2]
; GET EGA MEMORY SIZE
0572 B1 06         1520 C      MOV     CL,06H
; DIVIDE BY 64 TO GET
0573 0B EB        1521 C      SHR     BX,CL
; NUMBER OF 64KB BLOCKS
0574 4B EB        1522 C      DEC     BX
; NUMBER OF 64KB BLOCKS
0575 B1 05         1523 C      MOV     CL,05H
; NUMBER OF 64KB BLOCKS

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05AC D3 E3          1513 C SHL BX,CL
05AE 80 E3 60      1514 C AND BL,01100000B ; ISOLATE BITS 5 AND 6
05B1 80 26 0487 R 9F 1515 C AND INFO,10011111B
05B6 08 1E 0487 R 1517 C OR INFO,BL
05BA 80 0E 0487 R 04 1519 C OR INFO,00000100B ; 04H SET 3XX ACTIVE
05BF 8A 1E 0488 R 1521 C MOV BL,INFO_3
05C3 E8 00F3 R 1522 C CALL MK_ENV
05C6 83 C4 06      1523 C ADD SP,6 ; RESTORE STACK
05C9 1F            1524 C POP DS
05CA E9 0091 R 1525 C JMP SKIP ; GO TO END
05CD 05CD          1527 C EGA_MEM_ERROR: MOV DX,0103H ; ONE LONG AND THREE SHORT
05D0 EB 06C8 R 1528 C CALL ERR_BEEP
05D3 55            1529 C PUSH BP
05D4 BD 0001      1530 C MOV BP,1 ; SAVE SCRATCH PAD POINTER
05D7 EB C3        1531 C JMP EGA_MEM_EXIT ; INDICATE ERROR FOR XT
1532 C
1533 C ;----- THIS ROUTINE FINDS AMOUNT OF MEMORY GOOD
1534 C
05D9 05D9          1535 C MEMORY_OK PROC NEAR
05DB BB A000      1536 C MOV BX,0A000H ; SET PTR. TO BUFFER SEG
05DC 8E DB        1537 C MOV DS,BX ; SET SEG.REG.
05DE 8E C3        1538 C MOV ES,BX
05E0 88 86 04     1539 C MOV AX,WORD PTR[BP][4] ; SET COUNT FOR 32K WORDS
05E3 8A E8        1540 C MOV CH,AL ; SET AMOUNT OF BUFFER
05E5 2A C9        1541 C SUB CL,CL ; TO BE TESTED
05E7 D1 E1        1542 C SHL CX,1 ; MULTIPLY BY TWO
05E9 EB 05FB R 1543 C CALL PODSTG ; TEST FOR ERROR
05EC 80 FC 00     1544 C CMP AH,0 ; IF ERROR GO PRINT IT
05EF 75 09        1545 C JNZ MEMORY_OK_ERR
05F1 05F1          1546 C MEMORY_OK_EX: MOV AX,WORD PTR[BP][4] ; AMOUNT OF MEMORY FOUND
05F4 01 86 02     1547 C MOV WORD PTR[BP][2],AX ; AMOUNT OF MEMORY GOOD
05F7 88 0000      1549 C MOV AX,0
05FA 05FA          1550 C MEMORY_OK_ERR: MOV SI,RET
05FB C3            1551 C RET
1552 C MEMORY_OK ENDP
1553 C
1554 C ;-----
1555 C ; THIS ROUTINE PERFORMS A READ/WRITE TEST ON A BLOCK OF STORAGE :
1556 C ; (MAX. SIZE = 32KW). IF "WARM START", FILL BLOCK WITH 0000 AND :
1557 C ; RETURN.
1558 C ; ON ENTRY:
1559 C ; ES = ADDRESS OF STORAGE TO BE TESTED
1560 C ; DS = ADDRESS OF STORAGE TO BE TESTED
1561 C ; CX = WORD COUNT OF STORAGE BLOCK TO BE TESTED
1562 C ; (MAX. = 8000H (32K WORDS))
1563 C ; ON EXIT:
1564 C ; ZERO FLAG = OFF IF STORAGE ERROR
1565 C ; AX,BX,CX,DX,DI,SI ARE ALL DESTROYED.
1566 C ;-----
05FB 05FB          1567 C PODSTG PROC NEAR
05FB 55            1568 C PUSH BP
05FC FC          1569 C CLD
05FD 2B FF        1570 C SUB DI,DI ; SET DIR TO INCREMENT
05FF 2B C0        1571 C SUB AX,AX ; SET DI=0000 REL TO START
; OF SEGMENT
; INITIAL DATA PATTERN FOR
; 00-FF TEST
0601 E8 0CFE R 1574 C CALL DDS DS:ABS0
0604 8B 1E 0472 R 1575 C ASSUME BX,DS:RESET_FLAG ; WARM START?
0608 81 FB 1234   1576 C MOV CX,1234H
060C 8C C2        1578 C MOV DX,ES
060E 8E DA        1579 C MOV DS,DX
0610 74 62        1581 C JZ PODSTG_5
0612 81 FB 4321   1582 C CMP BX,4321H
0616 74 5C        1583 C JC PODSTG_5
0618 0618          1584 C PODSTG_1: MOV [DI],AL ; WRITE TEST DATA
061A 8A 05        1585 C MOV AL,[DI] ; GET IT BACK
061C 32 C4        1587 C XOR AL,AH ; COMPARE TO EXPECTED
061E 75 40        1588 C JNZ PODSTG_ERROR ; ERROR EXIT IF MISCMPARE
0620 FE C4        1589 C INC AH ; FORM NEW DATA PATTERN
0622 8A C4        1590 C MOV AL,AH
0624 75 F2        1591 C JNZ PODSTG_1
0626 8B E9        1593 C MOV BP,CX
0628 8B AA55      1594 C MOV AX,0AA55H ; SAVE WORD COUNT
062B 8B D8        1595 C MOV BX,AX ; LOAD DATA PATTERN
062D BA 55AA     1596 C MOV DX,055AAH ; LOAD OTHER DATA PATTERN
0630 F3 AB        1597 C REP STOSW ; FILL WORDS FROM LOW TO
; HIGH WITH AAAA
; POINT TO LAST WORD
; WRITTEN
0632 4F          1599 C DEC DI ; SET DIR FLAG TO GO DOWN
0633 4F          1600 C DEC DI ; SET INDEX REGS. EQUAL
0634 FD          1601 C STD ; RECOVER WORD COUNT
0635 8B F7       1602 C MOV SI,DI ; GO FROM HIGH TO LOW
0637 8B CD       1603 C MOV CX,BP ; GET WORD FROM MEMORY
0639 0639          1604 C PODSTG_2: XOR AX,BX ; EQUAL WHAT S/B THERE?
063A 33 C3       1606 C XOR AX,BX ; GO ERROR EXIT IF NOT
063C 75 22       1607 C JNZ PODSTG_ERROR ; GET 55 DATA PATTERN AND
063E 8B C2       1608 C MOV AX,DX ; STORE IN LOC JUST READ
0640 AB          1609 C STOSW ; LOOP TILL ALL BYTES DONE
0641 E2 F6       1610 C LOOP PODSTG_2 ; RECOVER WORD COUNT
0643 8B CD       1611 C MOV CX,BP ; ADJUST PTRS
0645 FC          1612 C CLD
0646 46          1613 C INC SI
0647 46          1614 C INC SI
0648 8B FE       1615 C MOV DI,SI
064A 064A          1616 C PODSTG_3: ; LOW TO HIGH DOING WORDS
064B AD          1617 C LODSW ; GET A WORD
064D 33 C2       1618 C XOR AX,DX ; SHOULD COMPARE TO DX
064F AB          1619 C JNZ PODSTG_ERROR ; GO ERROR IF NOT
0650 E2 F8       1622 C LOOP PODSTG_3 ; WRITE 0000 BACK TO LOC
; JUST READ
; LOOP TILL DONE
0652 FD          1623 C STD ; BACK TO DECREMENT
0653 4E          1625 C DEC SI ; ADJUST POINTER DOWN TO
; LAST WORD WRITTEN
0654 4E          1626 C DEC SI
0655 8B CD       1628 C MOV CX,BP ; GET WORD COUNT
0657 0657          1629 C PODSTG_4: ; GET WORD
0658 AD          1630 C LODSW ; TO 0000
0659 0B C0       1631 C OR AX,AX ; SHOULD COMPARE TO DX
065A 75 04       1632 C JNZ PODSTG_ERROR ; GO ERROR IF NOT
065C E2 F9       1633 C LOOP PODSTG_4 ; LOOP TILL DONE
065E EB 11       1634 C JMP SHORT PODSTG_ERROR
0660 0660          1635 C PODSTG_ERROR: MOV CX,AX ; SAVE BITS IN ERROR
0662 32 E4        1637 C XOR AH,AH
0664 0A ED        1638 C OR CH,CH ; HIGH BYTE ERROR?

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0666 74 02      1639      C      JZ      PODSTG_ERR1
0668 B4 01      1640      C      MOV     AH,1 ; SET HIGH BYTE ERROR
066A          1641      C      C      PODSTG_ERR1:
066A 0A C9      1642      C      OR      CL,CL ; LOW BYTE ERROR?
066C 74 03      1643      C      JZ      PODSTG_ERR2
066E 80 C4 02    1644      C      ADD     AH,2
0671          1645      C      PODSTG_ERR2:
0671 5D          1646      C      POP     BP
0672 FC          1647      C      CLO    RET ; SET DIR FLAG BACK TO INC
0673 C3          1648      C      RET ; RETURN TO CALLER
0674          1649      C      C      PODSTG_5:
0674          1650      C      ; SIMPLE FILL WITH 0000 ON
0674 50          1651      C      ; MARK-START
0675 52          1652      C      ; SAVE
0676 B6 03      1653      C      PUSH   AX ; SAVE VALUE
0678 B2 C4      1654      C      MOV     DL,SEQ_ADDR ; SEQ_ADDR REGISTER
067A BB 020F    1655      C      MOV     AX,020FH
067D EB 0D15 R  1656      C      CLO    OUT_DX
0680 5A          1657      C      POP     DX ; DO IT
0681 58          1658      C      POP     AX ; RESTORE
0682 F3/ AB     1659      C      REP     STOSW ; RESTORE
0684 EB 0CFE R  1660      C      CALL   DDS
0687 89 1E 0472 R 1661      C      ASSUME DS:ABSET ; RESTORE DS
068B BE DA      1662      C      MOV     DS:RESET_FLAG,BX
068D EB E2      1663      C      DS:DX ; AND EXIT
068F          1664      C      JMP     PODSTG_ERR2
068F          1665      C      PODSTG ENDP
068F          1666      C      ;----- DETERMINE SIZE OF BUFFER
068F          1667      C
068F          1668      C
068F 8C DA      1669      C      HOW_BIG PROC NEAR
0691 2B DB      1670      C      MOV     DX,DS ; SET PNTR TO BUFFER LOC
0693          1671      C      SUB     BX,BX ; BASIC COUNT OF 00K
0693 8E C2      1672      C      FILL_LOOP:
0695 2B FF      1673      C      MOV     ES,DX ; SET SEG. REG
0697 B8 A855    1674      C      SUB     DI,DI ; TEST PATTERN
069A BB C8      1675      C      MOV     AX,0A855H ; SEND TO MEMORY
069C 26: 89 05  1676      C      MOV     ES:[DI],AX ; PUT SOMETHING IN AL
069F 80 0F      1677      C      MOV     AL,0FH ; PUT PATTERN FROM MEMORY
06A1 26: 8B 05  1678      C      MOV     AX:[DI] ; COMPARE PATTERNS
06A4 33 C1      1679      C      XOR     AX,CX ; GO END IF NO COMPARE
06A6 75 14      1680      C      JNZ    HOW_BIG_END ; SET COUNT FOR BK WORDS
06A8 B9 2000    1681      C      MOV     CX,2000H ; FILL BK WORDS
06AB F3/ AB     1682      C      REP     STOSW ; POINT TO NEXT 16K BLOCK
06AD B1 C2 0A00 1683      C      ADD     BX,0A00H ; BUMP COUNT BY 16KB
06B1 83 C3 10    1684      C      ADD     BX,16 ; AREA YET ?(B0000H)
06B4 80 FE B0    1685      C      CMP     DH,0B0H
06B7 75 DA      1686      C      JNC    FILL_LOOP
06B9 EB 01 90    1687      C      JNZ    HOW_BIG_END ; AREA YET ?(B0000H)
06BC          1688      C      HOW_BIG_END:
06BC 80 FE A0    1689      C      CMP     DH,0A0H ; 1ST 16KB OK
06BF 74 06      1690      C      JZ      HB_ERROR_EXIT
06C1          1691      C      RESUME:
06C1 01 5E 04    1692      C      ADD     WORD PTR[BP][4],BX ; SAVE BUFFER FOUND
06C4 BB 0000     1693      C      MOV     AX,0
06C7          1694      C      HB_ERROR_EXIT:
06C7 C3          1695      C      RET
06C8          1696      C      HOW_BIG ENDP
06C8          1697      C
06C8          1698      C
06C8          1699      C
06C8          1700      C      ;----- SUBROUTINES FOR POWER ON DIAGNOSTICS -----
06C8          1701      C
06C8          1702      C      ; THIS PROCEDURE WILL ISSUE ONE LONG TONE (3 SEC) AND ONE OR
06C8          1703      C      ; MORE SHORT TONES (1 SEC) TO INDICATE A FAILURE ON THE PLANAR
06C8          1704      C      ; BOARD, A BAD RAM MODULE, OR A PROBLEM WITH THE CRT.
06C8          1705      C      ; ENTRY REQUIREMENTS:
06C8          1706      C      ; DI=NUMBER OF LONG TONES TO BEEP
06C8          1707      C      ; DL=NUMBER OF SHORT TONES TO BEEP.
06C8          1708      C      ;-----
06C8          1709      C      ERR_BEEP PROC NEAR
06C8          1710      C      PUSH   DS ; SAVE FLAGS
06C9 FA          1711      C      CLI ; DISABLE SYSTEM INTS
06CA 1E          1712      C      PUSH   DS
06CB EB 0CFE R    1713      C      CALL   DDS
06CE 0A F6      1714      C      ASSUME DS:ABSO
06D0 74 0B      1715      C      OR      DH,DH ; ANY LONG TONES TO BEEP
06D2 B3 06      1716      C      JZ      G3 ; NO, DO THE SHORT ONES
06D4 E8 0D20 R  1717      C      MOV     BL,6 ; LONG BEEP
06D7          1718      C      CALL   BEEP ; COUNTER FOR BEEPS
06D7          1719      C      G1: ; DO THE BEEP
06D7 E2 FE      1720      C      LOOP   G2
06D9 FE CE      1721      C      DEC     DH ; DELAY BETWEEN BEEPS
06DB 75 F5      1722      C      JNZ    G2 ; ANY MORE TO DO
06DD          1723      C      G2:
06DD B3 01      1724      C      MOV     BL,1 ; DELAY BETWEEN BEEPS
06DF E8 0D20 R  1725      C      CALL   BEEP ; DO IT
06E2          1726      C      G3:
06E2 E2 FE      1727      C      LOOP   G4 ; DELAY BETWEEN BEEPS
06E4 FE CA      1728      C      DEC     DL ; DONE WITH SHORT BEEPS
06E6 75 F5      1729      C      JNZ    G3 ; DO MORE
06E8          1730      C      G4:
06E8 E2 FE      1731      C      LOOP   G5 ; DELAY BEFORE RETURN
06EA          1732      C      G5:
06EA E2 FE      1733      C      LOOP   G6
06EC 1F          1734      C      POP     DS ; RESTORE CONTENTS OF DS
06ED 9D          1735      C      POPF ; RESTORE FLAGS
06EE C3          1736      C      RET ; RESTORE CONTEXTS OF DS
06EF          1737      C      ERR_BEEP ENDP
06EF          1738      C
06EF          1739      C
06EF          1740      C
06EF          1741      C
06EF          1742      C
06EF          1743      C      T2 LABEL WORD
06EF 0EB3 R      1744      C      DW     OFFSET AH0 ; MODE SET
06F1 10EF R     1745      C      DW     OFFSET AH1 ; SET CURSOR TYPE
06F3 1157 R     1746      C      DW     OFFSET AH2 ; SET CURSOR POSITION
06F5 1186 R     1747      C      DW     OFFSET AH3 ; READ CURSOR POSITION
06F7 19D R      1748      C      DW     OFFSET AH4 ; READ LIGHT PEN POSITION
06F9 12A4 R     1749      C      DW     OFFSET AH5 ; ACTIVE DISPLAY PAGE
06FB 150E R     1750      C      DW     OFFSET AH6 ; SCROLL DOWN
06FD 1580 R     1751      C      DW     OFFSET AH7 ; SCROLL UP
06FF 1702 R     1752      C      DW     OFFSET AH8 ; READ CHAR/ATTRIBUTE
0701 1899 R     1753      C      DW     OFFSET AH9 ; WRITE CHAR/ATTRIBUTE
0703 18DD R     1754      C      DW     OFFSET AHA ; WRITE CHARACTER ONLY
0705 1A75 R     1755      C      DW     OFFSET AHB ; SET COLOR PALETTE
0707 18CB R     1756      C      DW     OFFSET AHC ; WRITE DOT
0709 1C9F R     1757      C      DW     OFFSET AHD ; READ DOT
070B 1D01 R     1758      C      DW     OFFSET AHE ; WRITE TTY
070D 1D85 R     1759      C      DW     OFFSET AHF ; CURRENT VIDEO STATE
070F 1D05 R     1760      C      DW     OFFSET AH0 ; SET PALETTE REGISTERS
0711 1F98 R     1761      C      DW     OFFSET AH1 ; CHAR GENERATOR ROUTINE
0713 208F R     1762      C      DW     OFFSET AH2 ; ALTERNATE SELECT
0715 2118 R     1763      C      DW     OFFSET AH3 ; WRITE STRING
= 0028        1764      C      EQU     $-T2

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1765
1766 C INCLUDE VPARMS. INC
1767 C SUBTTL VPARMS. INC
1768 C PAGE
1769 C VIDEO_PARMS LABEL BYTE
1770
1771 C ; STRUCTURE OF THIS TABLE
1772 C ;
1773 C ; COLUMNS, ROWS, PELS PER CHARACTER
1774 C ; PAGE LENGTH
1775 C ; SEQUENCER PARAMETERS
1776 C ; MISCELLANEOUS REGISTER
1777 C ; CRT PARAMETERS
1778 C ; ATTRIBUTE PARAMETERS
1779 C ; GRAPHICS PARAMETERS
1780
1781 C BASE_1 EQU $ - VIDEO_PARMS
1782 C BASE_1_L EQU LABEL BYTE
1783
1784 C ;---- DEFAULT MODES
1785 C ;
1786 C ;--0--
1787 C DB 40D,24D,08D
1788 C DW 00800H
1789
1790 C TFS_LEN EQU $ - BASE_1_L
1791 C
1792 C SEQ_PARMS LABEL BYTE
1793 C DB 008H,003H,000H,003H
1794 C M1 EQU $ - SEQ_PARMS
1795 C
1796 C DB 023H
1797 C
1798 C CRT_PARMS LABEL BYTE
1799 C DB 037H,027H,02DH,037H,031H,015H
1800 C DB 004H,011H,000H,007H,006H,007H
1801 C DB 000H,000H,000H,000H,0E1H,024H
1802 C DB 0C7H,014H,008H,0E0H,0F0H,0A3H
1803 C DB 0FFH
1804 C M4 EQU $-CRT_PARMS
1805 C
1806 C LN_4 EQU $ - BASE_1_L
1807 C
1808 C ATTR_PARMS LABEL BYTE
1809 C DB 000H,001H,002H,003H,004H,005H
1810 C DB 006H,007H,010H,011H,012H,013H
1811 C DB 014H,015H,016H,017H,008H,000H
1812 C DB 00FH,000H
1813 C M5 EQU $-ATTR_PARMS
1814 C
1815 C LN_2 EQU $ - BASE_1_L
1816 C GRAPH_PARMS LABEL BYTE
1817 C DB 000H,000H,000H,000H,000H,010H
1818 C DB 00CH,000H,0FFH
1819 C M6 EQU $-GRAPH_PARMS
1820 C
1821 C H_TBL_LEN EQU $ - BASE_1_L
1822 C ;
1823 C ;--1--
1824 C DB 40D,24D,08D
1825 C DW 00800H
1826 C
1827 C DB 008H,003H,000H,003H
1828 C
1829 C DB 023H
1830 C
1831 C DB 037H,027H,02DH,037H,031H,015H
1832 C DB 004H,011H,000H,007H,006H,007H
1833 C DB 000H,000H,000H,000H,0E1H,024H
1834 C DB 0C7H,014H,008H,0E0H,0F0H,0A3H
1835 C DB 0FFH
1836 C
1837 C DB 000H,001H,002H,003H,004H,005H
1838 C DB 006H,007H,010H,011H,012H,013H
1839 C DB 014H,015H,016H,017H,008H,000H
1840 C DB 00FH,000H
1841 C
1842 C DB 000H,000H,000H,000H,000H,010H
1843 C DB 00EH,000H,0FFH
1844 C
1845 C ;--2--
1846 C DB 80D,24D,08D
1847 C DW 01000H
1848 C
1849 C DB 001H,003H,000H,003H
1850 C
1851 C DB 023H
1852 C
1853 C DB 070H,04FH,05CH,02FH,05FH,007H
1854 C DB 004H,011H,000H,007H,006H,007H
1855 C DB 000H,000H,000H,000H,0E1H,024H
1856 C DB 0C7H,028H,008H,0E0H,0F0H,0A3H
1857 C DB 0FFH
1858 C
1859 C DB 000H,001H,002H,003H,004H,005H
1860 C DB 006H,007H,010H,011H,012H,013H
1861 C DB 014H,015H,016H,017H,008H,000H
1862 C DB 00FH,000H
1863 C
1864 C DB 000H,000H,000H,000H,000H,010H
1865 C DB 00EH,000H,0FFH
1866 C
1867 C ;--3--
1868 C DB 80D,24D,08D
1869 C DW 01000H
1870 C
1871 C DB 001H,003H,000H,003H
1872 C
1873 C DB 023H
1874 C
1875 C DB 070H,04FH,05CH,02FH,05FH,007H
1876 C DB 004H,011H,000H,007H,006H,007H
1877 C DB 000H,000H,000H,000H,0E1H,024H
1878 C DB 0C7H,028H,008H,0E0H,0F0H,0A3H
1879 C DB 0FFH
1880 C
1881 C DB 000H,001H,002H,003H,004H,005H
1882 C DB 006H,007H,010H,011H,012H,013H
1883 C DB 014H,015H,016H,017H,008H,000H
1884 C DB 00FH,000H
1885 C
1886 C DB 000H,000H,000H,000H,000H,010H
1887 C DB 00EH,000H,0FFH
1888 C
1889 C ;--4--
1890 C DB 40D,24D,08D

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081A	4000	1891	C	DW	04000H
081C	08 03 00 02	1892	C	DB	00BH, 003H, 000H, 002H
0820	23	1893	C	DB	023H
0821	37 27 20 37 30 14	1894	C	DB	037H, 027H, 02DH, 037H, 030H, 014H
0827	04 11 00 01 00 00	1895	C	DB	004H, 011H, 000H, 001H, 000H, 000H
082D	00 00 00 00 E1 24	1896	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0833	C7 14 00 E0 F0 A2	1897	C	DB	0C7H, 014H, 000H, 0E0H, 0F0H, 0A2H
0839	FF	1898	C	DB	0FFH
083A	00 13 15 17 02 04	1902	C	DB	000H, 013H, 015H, 017H, 002H, 004H
0840	06 07 10 11 12 13	1903	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0846	14 15 16 17 01 00	1904	C	DB	014H, 015H, 016H, 017H, 001H, 000H
084C	03 00	1905	C	DB	003H, 000H
084E	00 00 00 00 00 30	1906	C	DB	000H, 000H, 000H, 000H, 000H, 030H
0854	0F 00 FF	1907	C	DB	00FH, 000H, 0FFH
0857	28 18 08	1908	C	DB	40D, 24D, 08D
085A	4000	1909	C	DW	04000H
085C	08 03 00 02	1910	C	DB	00BH, 003H, 000H, 002H
0860	23	1911	C	DB	023H
0861	37 27 20 37 30 14	1912	C	DB	037H, 027H, 02DH, 037H, 030H, 014H
0867	04 11 00 01 00 00	1913	C	DB	004H, 011H, 000H, 001H, 000H, 000H
086D	00 00 00 00 E1 24	1914	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0873	C7 14 00 E0 F0 A2	1915	C	DB	0C7H, 014H, 000H, 0E0H, 0F0H, 0A2H
0879	FF	1916	C	DB	0FFH
087A	00 13 15 17 02 04	1917	C	DB	000H, 013H, 015H, 017H, 002H, 004H
0880	06 07 10 11 12 13	1918	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0886	14 15 16 17 01 00	1919	C	DB	014H, 015H, 016H, 017H, 001H, 000H
088C	03 00	1920	C	DB	003H, 000H
088E	00 00 00 00 00 30	1921	C	DB	000H, 000H, 000H, 000H, 000H, 030H
0894	0F 00 FF	1922	C	DB	00FH, 000H, 0FFH
0897	50 18 08	1923	C	DB	80D, 24D, 08D
089A	4000	1924	C	DW	04000H
089C	01 01 00 06	1925	C	DB	001H, 001H, 000H, 006H
08A0	23	1926	C	DB	023H
08A1	70 4F 59 2D 5E 06	1927	C	DB	070H, 04FH, 059H, 02DH, 05EH, 006H
08A7	04 11 00 01 00 00	1928	C	DB	004H, 011H, 000H, 001H, 000H, 000H
08AD	00 00 00 00 E0 23	1929	C	DB	000H, 000H, 000H, 000H, 0E0H, 023H
08B3	C7 28 00 DF EF 02	1930	C	DB	0C7H, 028H, 000H, 0DFH, 0EFH, 0C2H
08B9	FF	1931	C	DB	0FFH
08BA	00 17 17 17 17 17	1932	C	DB	000H, 017H, 017H, 017H, 017H, 017H
08C0	17 17 17 17 17 17	1933	C	DB	017H, 017H, 017H, 017H, 017H, 017H
08C6	17 17 17 01 00	1934	C	DB	017H, 017H, 017H, 017H, 001H, 000H
08CC	01 00	1935	C	DB	001H, 000H
08CE	00 00 00 00 00 00	1936	C	DB	000H, 000H, 000H, 000H, 000H, 000H
08D4	0D 00 FF	1937	C	DB	00DH, 000H, 0FFH
08D7	50 18 0E	1938	C	DB	80D, 24D, 14D
08DA	1000	1939	C	DW	01000H
08DC	00 03 00 03	1940	C	DB	000H, 003H, 000H, 003H
08E0	A6	1941	C	DB	0A6H
08E1	60 4F 56 3A 51 60	1942	C	DB	060H, 04FH, 056H, 03AH, 051H, 060H
08E7	70 1F 00 0D 0B 0C	1943	C	DB	070H, 01FH, 000H, 00DH, 00BH, 00CH
08ED	00 00 00 00 5E 2E	1944	C	DB	000H, 000H, 000H, 000H, 05EH, 02EH
08F3	50 28 0D 5E 6E A3	1945	C	DB	05DH, 028H, 000H, 05EH, 06EH, 0A3H
08F9	FF	1946	C	DB	0FFH
08FA	00 08 08 08 08 08	1947	C	DB	000H, 008H, 008H, 008H, 008H, 008H
0900	08 08 10 18 18 18	1948	C	DB	008H, 008H, 010H, 018H, 018H, 018H
0906	18 18 18 18 0E 00	1949	C	DB	018H, 018H, 018H, 018H, 00EH, 000H
090C	0F 08	1950	C	DB	00FH, 008H
090E	00 00 00 00 00 10	1951	C	DB	000H, 000H, 000H, 000H, 000H, 010H
0914	0A 00 FF	1952	C	DB	00AH, 000H, 0FFH
0917	28 18 08	1953	C	DB	40D, 24D, 08D
091A	4000	1954	C	DW	04000H
091C	00 00 00 03	1955	C	DB	000H, 000H, 000H, 003H
0920	23	1956	C	DB	023H
0921	37 27 20 37 31 15	1957	C	DB	037H, 027H, 02DH, 037H, 031H, 015H
0927	04 11 00 07 06 07	1958	C	DB	004H, 011H, 000H, 007H, 006H, 007H
092D	00 00 00 00 E1 24	1959	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0933	C7 14 08 E0 F0 A3	1960	C	DB	0C7H, 014H, 008H, 0E0H, 0F0H, 0A3H
0939	FF	1961	C	DB	0FFH
093A	00 01 02 03 04 05	1962	C	DB	000H, 001H, 002H, 003H, 004H, 005H
0940	06 07 10 11 12 13	1963	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0946	14 15 16 17 01 00	1964	C	DB	014H, 015H, 016H, 017H, 008H, 000H
094C	0F 00	1965	C	DB	00FH, 000H
094E	00 00 00 00 00 10	1966	C	DB	000H, 000H, 000H, 000H, 000H, 010H
0954	0E 00 FF	1967	C	DB	00EH, 000H, 0FFH
0957	28 18 08	1968	C	DB	40D, 24D, 08D
095A	4000	1969	C	DW	04000H
095C	00 00 00 03	1970	C	DB	000H, 000H, 000H, 003H
0960	23	1971	C	DB	023H
0961	37 27 20 37 31 15	1972	C	DB	037H, 027H, 02DH, 037H, 031H, 015H
0967	04 11 00 07 06 07	1973	C	DB	004H, 011H, 000H, 007H, 006H, 007H
096D	00 00 00 00 E1 24	1974	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0973	C7 14 08 E0 F0 A3	1975	C	DB	0C7H, 014H, 008H, 0E0H, 0F0H, 0A3H
0979	FF	1976	C	DB	0FFH
097A	00 01 02 03 04 05	1977	C	DB	000H, 001H, 002H, 003H, 004H, 005H
0980	06 07 10 11 12 13	1978	C	DB	006H, 007H, 010H, 011H, 012H, 013H
0986	14 15 16 17 08 00	1979	C	DB	014H, 015H, 016H, 017H, 008H, 000H
098C	0F 00	1980	C	DB	00FH, 000H

098E	00	00	00	00	00	10	2017	C		
099A	0E	00	FF				2018	C	DB	000H,000H,000H,000H,000H,010H
							2019	C	DB	00EH,000H,0FFH
							2020	C		
							2021	C	;	--A--
0997	28	18	08				2022	C	DB	40D,24D,08D
099A	4000						2023	C	DW	04000H
							2024	C		
099C	00	00	00	03			2025	C	DB	000H,000H,000H,003H
							2026	C		
09A0	23						2027	C	DB	023H
							2028	C		
09A1	37	27	2D	37	31	15	2029	C	DB	037H,027H,02DH,037H,031H,015H
09A7	04	11	00	07	06	07	2030	C	DB	004H,011H,000H,007H,006H,007H
09AD	00	00	00	00	E1	24	2031	C	DB	000H,000H,000H,000H,0E1H,024H
09B3	C7	14	08	E0	F0	A3	2032	C	DB	0C7H,014H,008H,0E0H,0F0H,0A3H
09B9	FF						2033	C	DB	0FFH
							2034	C		
09BA	00	01	02	03	04	05	2035	C	DB	000H,001H,002H,003H,004H,005H
09C0	06	07	10	11	12	13	2036	C	DB	006H,007H,010H,011H,012H,013H
09C6	14	15	16	17	08	00	2037	C	DB	014H,015H,016H,017H,008H,000H
09CC	0F	00					2038	C	DB	00FH,000H
							2039	C		
09CE	00	00	00	00	00	10	2040	C	DB	000H,000H,000H,000H,000H,010H
09D4	0E	00	FF				2041	C	DB	00EH,000H,0FFH
							2042	C		
							2043	C	;	--B--
09D7	50	18	08				2044	C	DB	80D,24D,08D
09DA	1000						2045	C	DW	01000H
							2046	C		
09DC	01	04	00	07			2047	C	DB	001H,004H,000H,007H
							2048	C		
09E0	23						2049	C	DB	023H
							2050	C		
09E1	70	4F	5C	2F	5F	07	2051	C	DB	070H,04FH,05CH,02FH,05FH,007H
09E7	04	11	00	07	06	07	2052	C	DB	004H,011H,000H,007H,006H,007H
09E9	00	00	00	00	E1	24	2053	C	DB	000H,000H,000H,000H,0E1H,024H
09F3	C7	28	08	E0	F0	A3	2054	C	DB	0C7H,028H,008H,0E0H,0F0H,0A3H
09F9	FF						2055	C	DB	0FFH
							2056	C		
09FA	00	00	00	00	00	00	2057	C	DB	000H,000H,000H,000H,000H,000H
0A00	00	00	00	00	00	00	2058	C	DB	000H,000H,000H,000H,000H,000H
0A06	00	00	00	00	00	00	2059	C	DB	000H,000H,000H,000H,000H,000H
0A0C	0F	00					2060	C	DB	00FH,000H
							2061	C		
0A0E	00	00	00	00	00	00	2062	C	DB	000H,000H,000H,000H,000H,000H
0A14	04	00	FF				2063	C	DB	004H,000H,0FFH
							2064	C	;	--C--
0A17	50	18	0E				2065	C	DB	80D,24D,14D
0A1A	1000						2066	C	DW	01000H
							2067	C		
0A1C	00	04	00	07			2068	C	DB	000H,004H,000H,007H
							2069	C		
0A20	A6						2070	C	DB	0A6H
							2071	C		
0A21	60	4F	56	3A	51	60	2072	C	DB	060H,04FH,056H,03AH,051H,060H
0A27	70	1F	00	00	08	0C	2073	C	DB	070H,01FH,000H,000H,008H,00CH
0A2D	00	20	00	00	5E	2E	2074	C	DB	000H,000H,000H,000H,05EH,02EH
0A33	5D	28	0D	5E	6E	A3	2075	C	DB	05DH,028H,00DH,05EH,06EH,0A3H
0A39	FF						2076	C	DB	0FFH
							2077	C		
0A3A	00	00	00	00	00	00	2078	C	DB	000H,000H,000H,000H,000H,000H
0A40	00	00	00	00	00	00	2079	C	DB	000H,000H,000H,000H,000H,000H
0A46	00	00	00	00	0E	00	2080	C	DB	000H,000H,000H,000H,00EH,000H
0A4C	0F	08					2081	C	DB	00FH,008H
							2082	C		
0A4E	00	00	00	00	00	00	2083	C	DB	000H,000H,000H,000H,000H,000H
0A54	04	00	FF				2084	C	DB	004H,000H,0FFH
							2085	C	;	--D--
0A57	28	18	08				2086	C	DB	40D,24D,08D
0A5A	2000						2087	C	DW	02000H
							2088	C		
0A5C	08	0F	00	06			2089	C	DB	008H,00FH,000H,006H
							2090	C		
0A60	23						2091	C	DB	023H
							2092	C		
0A61	37	27	2D	37	30	14	2093	C	DB	037H,027H,02DH,037H,030H,014H
0A67	04	11	00	00	00	00	2094	C	DB	004H,011H,000H,000H,000H,000H
0A6D	00	00	00	00	E1	24	2095	C	DB	000H,000H,000H,000H,0E1H,024H
0A73	C7	14	08	E0	F0	E3	2096	C	DB	0C7H,014H,008H,0E0H,0F0H,0E3H
0A79	FF						2097	C	DB	0FFH
							2098	C		
0A7A	00	01	02	03	04	05	2099	C	DB	000H,001H,002H,003H,004H,005H
0A80	06	07	10	11	12	13	2100	C	DB	006H,007H,010H,011H,012H,013H
0A86	14	15	16	17	01	00	2101	C	DB	014H,015H,016H,017H,001H,000H
0A8C	0F	00					2102	C	DB	00FH,000H
							2103	C		
0A8E	00	00	00	00	00	00	2104	C	DB	000H,000H,000H,000H,000H,000H
0A94	05	0F	FF				2105	C	DB	005H,00FH,0FFH
							2106	C	;	--E--
0A97	50	18	08				2107	C	DB	80D,24D,08D
0A9A	4000						2108	C	DW	04000H
							2109	C		
0A9C	01	0F	00	06			2110	C	DB	001H,00FH,000H,006H
							2111	C		
0AA0	23						2112	C	DB	023H
							2113	C		
0AA1	70	4F	59	2D	5E	06	2114	C	DB	070H,04FH,059H,02DH,05EH,006H
0AA7	04	11	00	00	00	00	2115	C	DB	004H,011H,000H,000H,000H,000H
0AAO	00	00	00	00	E0	23	2116	C	DB	000H,000H,000H,000H,0E0H,023H
0AB3	C7	28	0D	DF	E3		2117	C	DB	0C7H,028H,00DH,00FH,0EFH,0E3H
0AB9	FF						2118	C	DB	0FFH
							2119	C		
0ABA	00	01	02	03	04	05	2120	C	DB	000H,001H,002H,003H,004H,005H
0AC6	06	07	10	11	12	13	2121	C	DB	006H,007H,010H,011H,012H,013H
0AC6	14	15	16	17	01	00	2122	C	DB	014H,015H,016H,017H,001H,000H
0ACC	0F	00					2123	C	DB	00FH,000H
							2124	C		
0ACE	00	00	00	00	00	00	2125	C	DB	000H,000H,000H,000H,000H,000H
0AD4	05	0F	FF				2126	C	DB	005H,00FH,0FFH
							2127	C	;	--F--
0AD7	50	18	0E				2128	C	DB	80D,24D,14D
0ADA	8000						2129	C	DW	08000H
							2130	C		
0ADC	05	0F	00	00			2131	C	DB	005H,00FH,000H,000H
							2132	C		
0AE0	A2						2133	C	DB	0A2H
							2134	C		
0AE1	60	4F	56	1A	50	E0	2135	C	DB	060H,04FH,056H,01AH,050H,0E0H
0AE7	70	1F	00	00	00	00	2136	C	DB	070H,01FH,000H,000H,000H,000H
0AED	00	00	00	00	5E	2E	2137	C	DB	000H,000H,000H,000H,05EH,02EH
0AF3	5D	14	0D	5E	6E	8B	2138	C	DB	05DH,014H,00DH,05EH,06EH,08BH
0AF9	FF						2139	C	DB	0FFH
							2140	C		
0AFA	00	08	00	00	18	18	2141	C	DB	000H,008H,000H,000H,018H,018H
080A	00	00	00	08	00	00	2142	C	DB	000H,000H,000H,008H,000H,000H



0B06	00 18 00 00 08 00	2143	C	DB	000H,018H,000H,000H,00BH,000H
0B0C	05 00	2144	C	DB	005H,000H
0B0E	00 00 00 00 00 10	2145	C	DB	000H,000H,000H,000H,000H,010H
0B14	07 0F FF	2146	C	DB	007H,00FH,0FFH
0B17	50 18 0E	2148	C	;-10--	80D,24D,14D
0B1A	8000	2149	C	DW	08000H
0B1C	05 0F 00 00	2151	C	DB	005H,00FH,000H,000H
0B20	A7	2152	C	DB	0A7H
0B21	5B 4F 53 17 50 8A	2153	C	DB	05BH,04FH,053H,017H,050H,0BAH
0B27	6C 1F 00 00 00 00	2154	C	DB	06CH,01FH,000H,000H,000H,000H
0B2D	00 00 00 00 5E 2B	2155	C	DB	000H,000H,000H,000H,05EH,02BH
0B33	5D 14 0F 5F 0A 8B	2156	C	DB	05DH,014H,00FH,05FH,00AH,08BH
0B39	FF	2157	C	DB	0FFH
0B3A	00 01 00 00 04 07	2158	C	DB	000H,001H,000H,000H,004H,007H
0B40	00 00 00 01 00 00	2159	C	DB	000H,000H,000H,001H,000H,000H
0B46	04 07 00 00 01 00	2160	C	DB	004H,007H,000H,000H,001H,000H
0B4C	05 00	2161	C	DB	005H,000H
0B4E	00 00 00 00 00 10	2162	C	DB	000H,000H,000H,000H,000H,010H
0B54	07 0F FF	2163	C	DB	007H,00FH,0FFH
= 0440		2164	C	BASE_2 EQU	\$ - VIDEO_PARMS
		2165	C	;-----	> 16K MODE VALUES
		2166	C	;-F--	
0B57	50 18 0E	2167	C	DB	80D,24D,14D
0B5A	8000	2168	C	DW	08000H
0B5C	01 0F 00 06	2169	C	DB	001H,00FH,000H,006H
0B60	A2	2170	C	DB	0A2H
0B61	60 4F 56 3A 50 60	2171	C	DB	060H,04FH,056H,03AH,050H,060H
0B67	70 1F 00 00 00 00	2172	C	DB	070H,01FH,000H,000H,000H,000H
0B6D	00 00 00 00 5E 2E	2173	C	DB	000H,000H,000H,000H,05EH,02EH
0B73	50 28 0D 5E 6E E3	2174	C	DB	05DH,028H,00DH,05EH,06EH,0E3H
0B79	FF	2175	C	DB	0FFH
0B7A	00 08 00 00 18 18	2176	C	DB	000H,008H,000H,000H,018H,018H
0B80	00 00 00 08 00 00	2177	C	DB	000H,000H,000H,008H,000H,000H
0B86	00 18 00 00 0B 00	2178	C	DB	000H,018H,000H,000H,00BH,000H
0B8C	05 00	2179	C	DB	005H,000H
0B8E	00 00 00 00 00 00	2180	C	DB	000H,000H,000H,000H,000H,000H
0B94	05 0F FF	2181	C	DB	005H,00FH,0FFH
0B97	50 18 0E	2182	C	;-10--	80D,24D,14D
0B9A	8000	2183	C	DW	08000H
0B9C	01 0F 00 06	2184	C	DB	001H,00FH,000H,006H
0BA0	A7	2185	C	DB	0A7H
0BA1	5B 4F 53 37 52 00	2186	C	DB	05BH,04FH,053H,037H,052H,000H
0BA7	6C 1F 00 00 00 00	2187	C	DB	06CH,01FH,000H,000H,000H,000H
0BA9	00 00 00 00 5E 2E	2188	C	DB	000H,000H,000H,000H,05EH,02EH
0BB3	5D 28 0F 5F 0A E3	2189	C	DB	05DH,028H,00FH,05FH,00AH,0E3H
0BB9	FF	2190	C	DB	0FFH
0BBA	00 01 02 03 04 05	2191	C	DB	000H,001H,002H,003H,004H,005H
0BCC	14 07 38 39 3A 3B	2192	C	DB	014H,007H,038H,039H,03AH,03BH
0BC6	3C 3D 3E 3F 01 00	2193	C	DB	03CH,03DH,03EH,03FH,001H,000H
0BCC	0F 00	2194	C	DB	00FH,000H
0BCE	00 00 00 00 00 00	2195	C	DB	000H,000H,000H,000H,000H,000H
0BD4	05 0F FF	2196	C	DB	005H,00FH,0FFH
= 04C0		2197	C	BASE_3 EQU	\$ - VIDEO_PARMS
		2198	C	;-----	HI RES ALTERNATE VALUES
		2199	C	;-0--	
0BD7	28 18 0E	2200	C	DB	40D,24D,14D
0BDA	0800	2201	C	DW	08000H
0BDC	0B 03 00 03	2202	C	DB	00BH,003H,000H,003H
0BE0	A7	2203	C	DB	0A7H
0BE1	2D 27 2B 2D 2B 6D	2204	C	DB	02DH,027H,02BH,02DH,02BH,06DH
0BE7	6C 1F 00 00 06 07	2205	C	DB	06CH,01FH,000H,000H,006H,007H
0BED	00 00 00 00 5E 2B	2206	C	DB	000H,000H,000H,000H,05EH,02BH
0BF3	5D 14 0F 5E 0A A3	2207	C	DB	05DH,014H,00FH,05EH,00AH,0A3H
0BF9	FF	2208	C	DB	0FFH
0BFA	00 01 02 03 04 05	2209	C	DB	000H,001H,002H,003H,004H,005H
0C00	14 07 38 39 3A 3B	2210	C	DB	014H,007H,038H,039H,03AH,03BH
0C06	3C 3D 3E 3F 08 00	2211	C	DB	03CH,03DH,03EH,03FH,008H,000H
0C0C	0F 00	2212	C	DB	00FH,000H
0C0E	00 00 00 00 00 10	2213	C	DB	000H,000H,000H,000H,000H,010H
0C14	0E 00 FF	2214	C	DB	00EH,000H,0FFH
0C17	28 18 0E	2215	C	;-1--	40D,24D,14D
0C1A	0800	2216	C	DW	08000H
0C1C	0B 03 00 03	2217	C	DB	00BH,003H,000H,003H
0C20	A7	2218	C	DB	0A7H
0C21	2D 27 2B 2D 2B 6D	2219	C	DB	02DH,027H,02BH,02DH,02BH,06DH
0C27	6C 1F 00 00 06 07	2220	C	DB	06CH,01FH,000H,000H,006H,007H
0C2D	00 00 00 00 5E 2B	2221	C	DB	000H,000H,000H,000H,05EH,02BH
0C33	5D 14 0F 5E 0A A3	2222	C	DB	05DH,014H,00FH,05EH,00AH,0A3H
0C39	FF	2223	C	DB	0FFH
0C3A	00 01 02 03 04 05	2224	C	DB	000H,001H,002H,003H,004H,005H
0C40	14 07 38 39 3A 3B	2225	C	DB	014H,007H,038H,039H,03AH,03BH
0C46	3C 3D 3E 3F 08 00	2226	C	DB	03CH,03DH,03EH,03FH,008H,000H
0C4C	0F 00	2227	C	DB	00FH,000H
0C4E	00 00 00 00 00 10	2228	C	DB	000H,000H,000H,000H,000H,010H
0C54	0E 00 FF	2229	C	DB	00EH,000H,0FFH
0C57	50 18 0E	2230	C	;-2--	80D,24D,14D
		2231	C		
		2232	C		
		2233	C		
		2234	C		
		2235	C		
		2236	C		
		2237	C		
		2238	C		
		2239	C		
		2240	C		
		2241	C		
		2242	C		
		2243	C		
		2244	C		
		2245	C		
		2246	C		
		2247	C		
		2248	C		
		2249	C		
		2250	C		
		2251	C		
		2252	C		
		2253	C		
		2254	C		
		2255	C		
		2256	C		
		2257	C		
		2258	C		
		2259	C		
		2260	C		
		2261	C		
		2262	C		
		2263	C		
		2264	C		
		2265	C		
		2266	C		
		2267	C		
		2268	C		

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0C5A 1000          2269   C      DW      01000H
                                2270   C      DB
0C5C 01 03 00 03  2271   C      DB      001H,003H,000H,003H
                                2272   C
0C60 A7           2273   C      DB      0A7H
                                2274   C
0C61 58 4F 53 37 51 5B  2275   C      DB      05BH,04FH,053H,037H,051H,05BH
0C67 6C 1F 00 00 06 07  2276   C      DB      06CH,01FH,000H,000H,03AH,007H
0C6D 00 00 00 00 5E 2B  2277   C      DB      000H,000H,000H,000H,05EH,02BH
0C73 5D 2B 0F 5E 0A A3  2278   C      DB      05DH,02BH,00FH,05EH,00AH,0A3H
0C79 FF           2279   C      DB      0FFH
                                2280   C
0C7A 00 01 02 03 04 05  2281   C      DB      000H,001H,002H,003H,004H,005H
0C80 14 07 3B 19 3A 3B  2282   C      DB      014H,007H,03BH,039H,03AH,03BH
0C86 3C 3D 3E 3F 08 00  2283   C      DB      03CH,03DH,03EH,03FH,008H,000H
0C8C 0F 00         2284   C      DB      00FH,000H
                                2285   C
0C8E 00 00 00 00 00 10  2286   C      DB      000H,000H,000H,000H,000H,010H
0C94 0E 00 FF     2287   C      DB      00EH,000H,0FFH
                                2288   C
                                2289   C      ;---3---
0C97 50 18 0E     2290   C      DB      80D,24D,14D
0C9A 1000         2291   C      DW      01000H
                                2292   C
0C9C 01 03 00 03  2293   C      DB      001H,003H,000H,003H
                                2294   C
0CA0 A7           2295   C      DB      0A7H
                                2296   C
0CA1 58 4F 53 37 51 5B  2297   C      DB      05BH,04FH,053H,037H,051H,05BH
0CA7 6C 1F 00 00 06 07  2298   C      DB      06CH,01FH,000H,000H,006H,007H
0CAD 00 00 00 00 5E 2B  2299   C      DB      000H,000H,000H,000H,05EH,02BH
0CB3 5D 2B 0F 5E 0A A3  2300   C      DB      05DH,02BH,00FH,05EH,00AH,0A3H
0CB9 FF           2301   C      DB      0FFH
                                2302   C
0CBA 00 01 02 03 04 05  2303   C      DB      000H,001H,002H,003H,004H,005H
0CBC 14 07 3B 19 3A 3B  2304   C      DB      014H,007H,03BH,039H,03AH,03BH
0CC6 3C 3D 3E 3F 08 00  2305   C      DB      03CH,03DH,03EH,03FH,008H,000H
0CCC 0F 00         2306   C      DB      00FH,000H
                                2307   C
0CCE 00 00 00 00 00 10  2308   C      DB      000H,000H,000H,000H,000H,010H
0CD4 0E 00 FF     2309   C      DB      00EH,000H,0FFH
                                2310   C
                                2311   C
                                2312   C      SUBTTL
                                2313   C
                                2314   C      ;---- VECTOR INTO <AH> SPECIFIED FUNCTION
                                2315   C
0CD7             2316   C      COMBO_VIDEO   PROC   NEAR
0CD7 FB           2317   C      STI
0CD8 FC           2318   C      CLD ; INTERRUPTS ON
0CD9 55           2319   C      PUSH BP ; SET DIRECTION FORWARD
0CDA 06           2320   C      PUSH ES ; SAVE THE REGISTER SET
0CDB 1E           2321   C      PUSH DS
0CDC 52           2322   C      PUSH DX
0CDD 51           2323   C      PUSH CX
0CDE 53           2324   C      PUSH BX
0CDF 56           2325   C      PUSH SI
0CE0 57           2326   C      PUSH DI
                                2327   C
0CE1 50           2328   C      PUSH AX ; SAVE AX VALUE
0CE2 8A C4       2329   C      MOV AL,AH ; GET INTO LOW BYTE
0CE4 32 E4       2330   C      XOR AH,AH ; ZERO TO HIGH BYTE
0CE6 D1 E0       2331   C      SAL AX,1 ; * 2 FOR TABLE LOOKUP
0CE8 8B F0       2332   C      MOV SI,AX ; PUT INTO SI FOR BRANCH
0CEA 3D 002B     2333   C      CMP AX,T2L ; TEST FOR WITHIN RANGE
0CED 72 06       2334   C      JB M2 ; BRANCH AROUND BRANCH
0CEF 58           2335   C      POP AX ; RECOVER REGISTER
0CF0 CD 42       2336   C      INT 42H ; PASS UNRECOGNIZED CALL
0CF2 E9 219E R    2337   C      JMP V_RET ; RETURN TO CALLER
0CF5             2338   C
M2:             2339   C      ASSUME DS:ABSO
0CF5 E8 0CFE R    2340   C      CALL DDS
0CF8 58           2341   C      POP AX ; RECOVER
0CF9 2E: FF A4 06F R 2342   C      JMP WORD PTR CS:[SI + OFFSET T2] ; JMP TO AH=0 THRU AH=XX
                                2343   C
                                2344   C      ;---- UTILITY ROUTINES
                                2345   C
                                2346   C      ;---- SET DS TO THE DATA SEGMENT
0CFE             2347   C      DDS   PROC   NEAR
0CFE 50           2348   C      PUSH AX ; SAVE REGISTER
0CFF 2B C0       2349   C      SUB AX,AX
0D01 8E D8       2350   C      MOV DS,AX
0D03 58           2351   C      POP AX ; RESTORE REGISTER
0D04 C3           2352   C      RET
0D05             2353   C      DDS   ENDP
                                2354   C
0D05             2355   C      WHAT_BASE   PROC   NEAR
0D05 1E           2356   C      ASSUME DS:ABSO
0D06 E8 0CFE R    2357   C      CALL DDS ; SAVE DATA SEGMENT
0D09 8B 16 0463 R 2358   C      MOV DX,ADDR_6845 ; GET LOW MEMORY SEGMENT
0D0D 80 E2 F0     2359   C      AND DL,0FH ; GET CRTG ADDRESS
0D10 80 CA 0A     2360   C      OR DL,0AH ; STRIP OFF LOW NIBBLE
0D13 1F           2361   C      POP DS ; SET TO STATUS REGISTER
0D14 C3           2362   C      RET
0D15             2363   C      WHAT_BASE   ENDP
0D15             2364   C      OUT_DX     PROC   NEAR
0D15 86 C4       2365   C      XCHG AL,AH ; AH=INDEX,AL=DATA,DX=PORT
0D17 EE           2366   C      OUT DX,AL ; GET INDEX VALUE
0D18 42           2367   C      INC DX ; SET INDEX REG
0D19 86 C4       2368   C      XCHG AL,AH ; SET DX TO DATA REG
0D1B EE           2369   C      OUT DX,AL ; GET DATA VALUE
0D1C 4A           2370   C      DEC DX ; SET DATA REG
0D1D C3           2371   C      RET ; SET DX BACK TO INDEX
0D1E             2372   C      OUT_DX     ENDP
                                2373   C
                                2374   C      ;---- ROUTINE TO SOUND BEEPER
0D1E             2375   C      BP_1     PROC   NEAR
0D1E EE           2376   C      OUT DX,AL
0D1F C3           2377   C      RET
0D20             2378   C      BP_1     ENDP
                                2379   C
0D20             2380   C      BEEP     PROC   NEAR
0D20 52           2381   C      PUSH DX
0D21 BA 0043     2382   C      MOV DX,TIMER+3
0D24 80 86       2383   C      MOV AL,10110110B
0D26 E8 0D1E R    2384   C      CALL BP_1 ; SEL TIM 2,LSB,MSB,BINARY
0D29 8B 0533     2385   C      MOV AX,533H ; WRITE THE TIMER,MODE REG
0D2C 4A           2386   C      DEC DX ; STRIP OFF 1000 HZ
0D2D E8 0D1E R    2387   C      CALL BP_1 ; WRITE TIMER 2 CNT - LSB
0D30 8A C4       2388   C      MOV AL,AH
0D32 E8 0D1E R    2389   C      CALL BP_1 ; WRITE TIMER 2 CNT - MSB
0D35 BA 0061     2390   C      MOV DX,PORT_B

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OD3B EC 2395 IN AL,DX ; GET SETTING OF PORT
OD39 8A E0 2396 MOV AH,AL ; SAVE THAT SETTING
OD3B 0C 03 2397 OR AL,03 ; TURN SPEAKER ON
OD3D E8 0D1E R 2398 ;
OD40 2B C9 2399 SUB CX,CX ; SET CNT TO WAIT 500 MS
OD42 ;
OD42 E2 FE 2401 LOOP G7 ; DELAY BEFORE TURNING OFF
OD44 FE CB 2402 DEC BL ; DELAY CNT EXPIRED?
OD46 75 FA 2403 JNZ C7 ; NO-CONTINUE BECPING SPK
OD48 8A C4 2404 MOV AL,AH ; RECOVER VALUE OF PORT
OD4A E8 0D1E R 2405 CALL BP_1
OD4D 5A 2406 POP DX
OD4E C3 2407 RET ; RETURN TO CALLER
OD4F 2408 ENDP
2409
2410 ;----- FIND THE PARAMETER TABLE VECTOR IN THE SAVE TABLE
2411
2412 SET_BASE PROC NEAR
2413 ASSUME DS:ABS0
2414 CALL DDS
2415 LES BX,SAVE_PTR ; GET PTR TP PTR TABLE
2416 LES BX,DWORD PTR ES:[BX] ; GET PARAMETER PTR
2417 RET
2418
2419 SET_BASE ENDP
2420
2421 ;----- ESTABLISH ADDRESSING TO THE CORRECT MODE TABLE ENTRY
2422
2423 MAKE_BASE PROC NEAR
2424 ASSUME DS:ABS0
2425 PUSH CX
2426 DX
2427 CALL SET_BASE ; GET PARM TBL PTR
2428 MOV AH,CRT_MODE ; TEST FOR BASE CARD
2429 TEST INFO_060H ; MIN MEMORY
2430 B_M_1
2431
2432 ;----- WE HAVE A MEMORY EXPANSION OPTION HERE
2433
2434 CMP AH,0FH
2435 JNE B_M_2
2436 ADD BX,BASE_2 - BASE_1
2437 JMP B_M_OUT
2438
2439 B_M_2:
2440 CMP AH,010H
2441 JNE B_M_1
2442 ADD BX,BASE_2 + M_TBL_LEN - BASE_1
2443 JMP B_M_OUT
2444
2445 B_M_1:
2446 CMP AH,03H ; SKIP ENHANCED PORTION
2447 JA B_M_3
2448
2449 ;----- CHECK THE SWITCH SETTING FOR ENHANCEMENT
2450
2451 MOV AL,INFO_3
2452 AND AL,0FH
2453 CMV AL,03H ; SECONDARY EMULATE SETTING
2454 JE BR3
2455 CMP AL,09H ; PRIMARY EMULATE SETTING
2456 JE BR5
2457 JMP B_M_3
2458
2459 ;----- WE WILL PERFORM ENHANCEMENT
2460
2461 BR5:
2462 ADD BX,BASE_3 - BASE_1 ; VECTOR TO ENHANCEMENT TBL
2463
2464 B_M_3:
2465 MOV CL,CRT_MODE
2466 SUB CH,CH
2467 JCKZ B_M_4
2468
2469 ;----- THIS LOOP WILL MOVE THE PTR TO THE INDIVIDUAL MODE ENTRY
2470
2471 B_M_5:
2472 ADD BX,M_TBL_LEN ; LENGTH OF ONE MODE ENTRY
2473 LOOP B_M_5
2474
2475 B_M_4:
2476 POP DX
2477 POP CX
2478 RET
2479
2480 MAKE_BASE ENDP
2481
2482 ;----- PROGRAM THE EGA REGISTERS FROM THE PARAMETER TABLE
2483
2484 SET_REGS PROC NEAR
2485 ASSUME DS:ABS0,ES:NOTHING
2486
2487 ;----- PROGRAM THE SEQUENCER
2488
2489 CALL MAKE_BASE ; GET TABLE PTR
2490 ADD BX,TS_LEN ; MODE TO SEQUENCER PARMS
2491 MOV DH,3
2492 DL,SEQ_ADDR
2493 MOV AX,0003H ; RESET SEQUENCER
2494 CLI ; DISABLE INTERRUPTS
2495 CALL OUT_DX
2496 MOV AL,ES:[BX] ; GET SEQUENCER VALUE
2497 INC AH ; NEXT INDEX
2498 CALL OUT_DX ; SET IT
2499
2500 DI:
2501 INC AH ; NEXT INDEX REGISTER
2502 INC BX ; NEXT TABLE ENTRY
2503 MOV AL,ES:[BX]
2504 CALL OUT_DX
2505 CMP AH,M1+1
2506 JB D1
2507
2508 MOV AL,ES:[BX]
2509 INC BX
2510 MOV DL,MISC_OUTPUT
2511 OUT DX,AL
2512 MOV DL,SEQ_ADDR
2513 MOV AX,0003H
2514 CALL OUT_DX ; START SEQUENCER
2515 STI ; ENABLE INTERRUPTS
2516
2517 ;----- PROGRAM THE CRT CONTROLLER
2518
2519 MOV DX,ADDR_6845 ; CRTC INDEX REGISTER
2520 SUB AH,AH ; COUNTER
2521
2522 X1:
2523 MOV AL,ES:[BX] ; GET VALUE FROM TABLE
2524 CALL OUT_DX ; SET CRTC REGISTER
2525 INC BX ; NEXT TABLE ENTRY
2526 INC AH ; NEXT INDEX VALUE
2527 CMP AH,M4 ; TEST REGISTER COUNT

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0DF4 72 F2          2521      JB      X1          ; DO THE REST
0DF6 26: 8B 47 F1  2522      MOV     AX,ES:[BX](-OFH) ; GET CURSOR MODE
0DF8 86 E0          2523      XCHG   AH,AL
0DFC A3 0460 R      2524      MOV     AH,AL
2525                      ; SET LOW RAM VALUE
2526
;----- PROGRAM THE ATTRIBUTE CHIP
0DF7 8B F3          2527
0E01 E8 0D05 R     2528      MOV     SI,BX
0E04 EC            2529      CALL  WHAT_BASE
0E05 B2 C0          2530      IN      AL,DX
0E07 2A E4          2531      MOV     DL,ATTR_WRITE
0E09              2532      SUB     AH,AH          ; INDEX COUNTER
0E09 26: 8A 07      2533      D3:     MOV     AL,ES:[BX] ; GET DATA VALUE
0E0C 86 E0          2534      XCHG   AH,AL
0E0E EE            2535      OUT     DX,AL
0E10 86 E0          2536      XCHG   AH,AL
0E11 EE            2537      IN      DX,AL
0E12 43            2538      INC     BX
0E13 FE C4          2539      INC     AH          ; NEXT DATA VALUE
0E15 80 FC 14       2540      INC     AH          ; NEXT INDEX VALUE
0E18 72 EF          2541      CMP     AH,M5      ; TEST REGISTER COUNT
2542      JB      D3          ; DO THE REST
2543
0E1A 80 00          2544      MOV     AL,0
0E1C EE            2545      OUT     DX,AL
2546
;----- CHECK IF PALETTE REGISTER VALUES ARE TO BE SAVED
0E1D 1E            2547      PUSH   DS
0E1E 06            2548      PUSH   ES
0E1F C4 3E 04A8 R   2549      LES     DI,SAVE_PTR ; GET TABLE PTR
0E23 26: C4 7D 04   2550      LES     DI,DWORD PTR ES:[DI][4] ; GET PALETTE PTR
0E27 8C C0          2551      MOV     AX,ES
0E29 0B C7          2552      OR      AX,DI
0E2B 74 09          2553      JZ      SAVE_OUT    ; IF ZERO, NO SAVE OCCURS
2554
;----- STORE AWAY THE PALETTE VALUES IN RAM SAVE AREA
0E2D 1F            2554      POP     DS
0E2E 1E            2555      PUSH   DS
0E2F 89 0010        2556      MOV     CX,16D
0E32 F3/ A4         2557      REP    MOVSB
0E34 46            2558      INC     SI          ; SAVE THE PALETTE REGS
0E35 A4             2559      MOVSB
0E36 07            2560      SAVE_OUT: POP    ES ; SAVE THE OVERSCAN REG
0E37 1F            2561      POP     DS
2562
;----- PROGRAM THE GRAPHICS CHIPS
0E38 B2 CC          2570      MOV     DL,GRAPH_1_POS
0E3A B0 00          2571      MOV     AL,0
0E3C EE            2572      OUT     DX,AL
0E3D B2 CA          2573      MOV     DL,GRAPH_2_POS
0E3F B0 01          2574      MOV     AL,0
0E41 EE            2575      OUT     DX,AL
0E42 B2 CE          2576      MOV     DL,GRAPH_ADDR
0E44 2A E4          2577      MOV     AH,AH
0E46              2578      SUB     AH,AH
0E46 26: 8A 07      2579      D4:     MOV     AL,ES:[BX] ; PARAMETER BYTE
0E49 E8 0D15 R     2580      CALL  OUT_DX      ; SET IT
0E4C 43            2581      INC     BX          ; NEXT BYTE
0E4D FE C4          2582      INC     AH          ; NEXT REGISTER
0E4F B0 FC 09       2583      CMP     AH,M6
0E52 72 F2          2584      JB      D4          ; CONTINUE
0E54 C3            2585      RET
0E55              2586      SET_REGS  ENDP
2587
;----- MODE SET REGEN CLEAR ROUTINE
0E55              2588
0E55              2589
BLANK  PROC  NEAR ; FILL REGEN WITH BLANKS
2590      ASSUME DS:ABS0,ES:NOTHING
0E55 A0 04B7 R     2591      MOV     AL,INFO
0E56 A8 80          2592      TEST   AL,080H
0E5A 75 39          2593      JNZ    OUT_1      ; SEE IF BLANK IS TO OCCUR
0E5C BA B800        2594      MOV     AL,080H ; MODE SET HIGH BIT
0E5F A0 0449 R     2595      MOV     AL,CRT_MODE ; SKIP BLANK FOR REGEN
0E62 3C 06          2596      CMP     AL,6      ; COLOR MODE REGEN ADDRESS
0E64 76 DA          2597      JBE    CGO        ; CURRENT MODE SET
0E66 BA 8000        2598      MOV     AL,6      ; 0-6 ARE COLOR MODES
0E69 3C 07          2599      CMP     AL,7      ; MONOCHROME REGEN ADDRESS
0E6B 74 03          2600      JE     CGO        ; MONOCHROME MODE
0E6D BA A0DD        2601      MOV     AL,7
0E70              2602      CGO:    JE     CGO
2603      MOV     DX,0A000H ; REMAINING MODES
0E70 BB 0720        2604      MOV     BX,0720H
2605      CMP     AL,4      ; ALPHA BLANK VALUE
0E73 3C 04          2606      JB     W1         ; ALPHAMODES 0-3
0E75 72 06          2607      JB     W1
0E77 3C 07          2608      CMP     AL,7
0E79 74 02          2609      JE     W1
0E7B 2B DB          2610      SUB     BX,BX      ; ALPHA MODE
2611      ; GRAPHICS BLANK VALUE
0E7D              2612      W1:    SRLOAD  ES ; SET THE REGEN SEGMENT
0E7D 8E C2          2613      MOV     ES,DX
0E7F 8B 0E 044C R   2614      MOV     CX,CRT_LEN
0E83 E3 10          2615      JCKZ   OUT_1
0E85 B9 8000        2616      MOV     CX,08000H
0E88 80 FE A0       2617      CMP     DH,0A0H
0E8B 74 02          2618      JE     N_BA
0E8D B5 40          2619      MOV     N_BA,CH,040H
0E8F              2620
0E8F 8B C3          2621      N_BA:  MOV     AX,BX
0E91 2B FF          2622      MOV     DI,D1
0E93 F3/ AB         2623      REP    STOSW      ; BLANK VALUE
2624      ; CLEAR POINTER
0E95              2624      OUT_1:  RET          ; CLEAR THE PAGE
2625      ; RETURN TO CALLER
0E96              2626
0E96 86 10B7 R     2627      BLANK  ENDP
2628
0E99 C3            2629      PH_5   PROC  NEAR
2630      CALL PAL_ON
0E9A              2630      RET
2631
0E9A              2631      PH_5   ENDP
2632
;----- SEE IF WE ARE TO SUPPORT 640 X 350 ON A 640 X 200 MODE
0E9A              2633
BRST_DET  PROC  NEAR
2634      ASSUME DS:ABS0
0E9A 50            2635      BRST_DET  PUSH  AX
0E9B 1E            2636      PUSH   DS
0E9C E8 0CFE R     2637      CALL  DDS
2638      DDS
0E9F A0 04B8 R     2639      MOV     AL,INFO_3
0EA2 1F            2640      POP     DS
0EA3 24 0F          2641      AND     AL,0FH
0EA5 3C 03          2642      CMP     AL,03H
0EA7 74 07          2643      JE     YES        ; EMULATE MODE
0EA9 3C 09          2644      CMP     AL,09H
0EAB 74 03          2645      JE     YES        ; EMULATE MODE
2646

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OEAD 58          2647          POP      AX
OEAE  F8          2648          CLC
OEAF  C3          2649          RET
OEBO  58          2651          B_YES:   POP      AX
OE81  F9          2652          STC
OE82  C3          2653          RET
OE83  2654          BRST_DET  ENDP
          2655          ;----- MODE SET
          2657
OE83  2658          AHO:
          2659          ASSUME  DS:ABS0
          2660          CLI
          2661          MOV     WORD PTR GRX_SET, OFFSET CGDD0T
          2662          MOV     WORD PTR GRX_SET+2, CS
          2663          STI
          2664          AND     INFO,11110011B          ; TURN OFF RETRACE BIT
          2665          ; EGA ACTIVE BIT
          2666          PUSH   AX          ; SAVE
          2667          TEST   INFO,2          ; THERE IS NO MONOCHROME
          2668          ST_1          ; THERE IS A MONOCHROME
          2669          MOV     AX,EQUIP_FLAG          ; CHECK THE EQUIPMENT FLAG
          2670          AND     AL,030H          ; FOR MONOCHROME CALL
          2671          CMP     AL,030H          ; IT IS A MONOCHROME CALL
          2672          JE      ST_2
          2673
          ;----- FALL THROUGH => REGULAR COLOR CARD SETUP
          2674
          2675          MOV     ROWS,024D
          2676          MOV     POINTS,8
          2677          POP     AX
          2678          OR     INFO,00001000B          ; RECOVER
          2679          CMP     AL,1          ; EGA NOT ACTIVE
          2680          JBE    ST_7          ; WAIT FOR RETRACE ON
          2681          JBE    CMP     AL,4          ; MODES 2,3 ONLY
          2682          JAE    ST_7          ; DO RETRACE
          2683          OR     INFO,00000100B          ; OTHER ADAPTER MODE CALL
          2684          INT     42H          ; BACK TO CALLER
          2685          JMP     V_RET
          2686
          ;----- AT THIS POINT THERE IS NO MONOCHROME ATTACHED TO THE ADAPTER
          2687
          2688          MOV     AX,EQUIP_FLAG          ; TEST THE EQUIPMENT FLAG
          2689          AND     AL,030H          ; TO SEE IF THIS IS A
          2690          CMP     AL,030H          ; MONOCHROME SETUP CALL
          2691          JNE    ST_3          ; MUST BE COLOR TO CARD
          2692
          ;----- FALL THROUGH => REGULAR MONOCHROME CARD SETUP
          2693
          2694          MOV     ROWS,024D
          2695          MOV     POINTS,014D
          2696          POP     AX          ; RECOVER
          2697          INT     42H          ; OTHER ADAPTER MODE CALL
          2698          MOV     CURSOR_MODE,0BOCH          ; FIX PLANAR VALUE
          2699          OR     INFO,8          ; THE EGA IS NOT ACTIVE
          2700          JMP     V_RET          ; BACK TO CALLER
          2701
          ;----- MONOCHROME SETUP TO THE ADAPTER
          2702
          2703          ST_2:
          2704          POP     AX          ; RECOVER
          2705          PUSH   DH,3          ; SAVE
          2706          AND     AL,080H          ; PICK OFF THE CLEAR BIT
          2707          AND     INFO,07FH          ; MASK OFF THE OTHER BITS
          2708          OR     INFO,AL          ; SAVE REGEN CLEAR BIT
          2709          POP     AX          ; RECOVER TRUE CALL VALUE
          2710          AND     AL,07FH          ; ALREADY DEALT WITH D7
          2711          CMP     AL,0FH          ; A MONOCHROME MODE
          2712          JE      ST_2A          ; DO THIS MODE
          2713          MOV     AL,7          ; REGULAR MONOCHROME
          2714
          2715          ST_2A:
          2716          MOV     CRT_MODE,AL          ; SAVE MODE VALUE
          2717          MOV     DL,CRTC_ADDR_B          ; IT IS 3-B-X
          2718          MOV     ADDR_6845,DX          ; SAVE CRT ADDRESS
          2719          JMP     Q01          ; CONTINUE THE MODE SET
          2720
          ;----- COLOR SETUP TO THE ADAPTER
          2721
          2722          ST_3:
          2723          POP     AX          ; RECOVER PARAMETER VALUE
          2724          PUSH   AX          ; SAVE IT
          2725          MOV     DH,3          ; ISOLATE REGEN CLEAR BIT
          2726          AND     AL,080H          ; PREPARE INFO BYTE
          2727          AND     INFO,07FH          ; SET IT, OR NOT
          2728          OR     INFO,AL          ; RECOVER TRUE MODE CALL
          2729          POP     AX          ; DONE WITH D7
          2730          AND     AL,07FH          ; SAVE THIS MODE
          2731          MOV     DL,CRTC_ADDR          ; 3-D-X
          2732          MOV     ADDR_6845,DX          ; SAVE CRT ADDRESS
          2733          MOV     CRT_START,0          ; SAVE START ADDRESS
          2734          MOV     ACTIVE_PAGE,0          ; RESET PAGE VALUE TO ZERO
          2735          ASSUME  ES:NOTHING
          2736          MOV     CX,8          ; 8 PAGES OF CURSOR VALUES
          2737          MOV     DI,OFFSET CURSOR_POSN          ; OFFSET
          2738          PUSH   DS          ; ESTABLISH
          2739          POP     ES          ; ADDRESSING
          2740          SUB     AX,AX          ; 0 THOSE CURSOR LOCATIONS
          2741          REP     STOSW          ; CLEAR OUT SAVED VALUES
          2742
          2743          CALL  MAKE_BASE
          2744
          2745          MOV     AL,ES:[BX]          ; GET COLUMN COUNT
          2746          SUB     AH,AH          ; ZERO HIGH BYTE
          2747          MOV     CRT_COLS,AX          ; STORE COLUMN VALUE
          2748
          2749          MOV     AL,ES:[BX][1]          ; GET ROW VALUE
          2750          MOV     STORE_ROW_VALUE
          2751
          2752          MOV     AL,ES:[BX][2]          ; GET THE BYTES/CHAR
          2753          SUB     AH,AH          ; ZERO HIGH BYTE
          2754          MOV     POINTS,AX          ; STORE BYTES/CHAR
          2755
          2756          MOV     AX,ES:[BX][3]          ; GET PAGE SIZE
          2757          MOV     CRT_LEN,AX          ; STORE PAGE LENGTH
          2758
          2759          SUB     BX,BX          ; ZERO
          2760          MOV     AL,1          ; MONOCHROME ALPHA CHAR GEN
          2761          MOV     AH,CRT_MODE          ; GET CURRENT MODE
          2762          CMP     AH,7          ; IS IT MONOCHROME
          2763          JE      ENTRY_2          ; 9X14 FONT

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0FA2 80 FC 03      2773      CMP      AH,03H
0FA5 77 35          2774      JA       ENTRY_1
                                2775
0FA7 E8 0E9A R    2776      CALL    BRST_DET
0FAA 72 02          2777      JC      ENTRY_2
                                2778
0FAC 80 02          2779      MOV     AL,2 ; COLOR ALPHA CHAR GEN
0FAE              2780
0FAE E8 1EAE R    2781      CALL    CH_GEN ; LOAD ALPHA CHAR GEN
0FB1 E8 0CFE R    2782      CALL    DDS ;
0FB4 8A 26 0449 R 2783      MOV     AH,CRT_MODE ; GET CURRENT MODE
0FB8 80 FC 07      2784      CMP     AH,7 ; IS IT MONOCHROME
0FB8 74 03         2785      JE      FDG_IT ; 9X14 FONT
0FB8 FD 1D 90     2786      JMP     ENTRY_1
0FC0              2787
0FC0 BD 0000 E     2788      MOV     BP,OFFSET CGMN_FDG ; TABLE POINTER
0FC3 BB 0E00       2789      MOV     BX,0E00H ; 14 BYTES PER CHAR
0FC6              2790
0FC6 0E           2791      FGD:   CS ; GET THE ROM SEGMENT
0FC7 07           2792      POP     ES ; INTO ES
0FC8 26: 8B 56 00 2793      MOV     DX,ES:[BP] ; GET THE CHAR HEX CODE
0FCC 0B 02        2794      OR      DX,DX ; ZERO = NO MORE CHARS
0FCE 74 03         2795      MOV     JZ ENTRY_1 ; NO MORE
0FDD 89 0001      2796      ADD     CX,1 ; DO ONE CHAR AT A TIME
0FD3 45           2797      INC     BP ; MOVE TO FIRST CODE POINT
0FD4 E8 1E6F R    2798      CALL    DO_MAP2 ; STORE THE CODE POINT
0FD7 83 C5 0E     2799      ADD     BP,014H ; ADJUST BP TO NEXT CODE
0FDA EB EA        2800      JMP     FDG ; DO ANOTHER
0FDC              2801
0FDC E8 0DAB R    2802      CALL    SET_REGS ;
0FDF E8 0E55 R    2803      CALL    BLANK ;
0FE2 E8 0E96 R    2804      CALL    PH_5 ; CLEAR OUT THE BUFFER
                                2805
0FE5 E8 0CFE R    2806      ASSUME  DS:ABS0
0FE8 80 3E 0449 R OF 2807      CALL    DDS ;
0FED 72 06        2808      CMP     CRT_MODE,OFH ;
0FEF C7 06 010C R 0000 E 2809      MOV     WORD PTR GRX_SET , OFFSET CGMN
0FF5              2810
0FF5 80 3E 0449 R 07 2811      MS_1:  CMP     CRT_MODE,7
0FFA 77 09        2812      JA      SAVE_GRPH ;
0FFA 74 4B        2813      JE      SAVE_GRPH ;
0FFE 80 3E 0449 R 03 2814      MS_1:  CMP     CRT_MODE,3
1003 76 44        2815      JBE     SAVE_GRPH ;
1005              2816
1005 C4 1E 0448 R 2817      SAVE_GRPH: LES     BX,SAVE_PTR
1009 83 C3 0C      2818      ADD     BX,0CH ;
100C 26: C4 1F    2819      LES     BX,DWORD PTR ES:[BX]
100F 8C C0        2820      MOV     AX,ES ;
1011 0B C3        2821      OR      AX,BX ;
1013 74 32        2822      JZ      J4J ; JMP AHO_DONE
1015 BE 0007      2823      MOV     SI,07H ;
1018              2824
1018 26: 8A 00     2825      SG_1:  MOV     AL,ES:[BX][SI]
101B 3C FF        2826      CMP     AL,OFFH ;
101D 74 7A        2827      JNE     AHO_DONE ;
101F 3A 06 0449 R 2828      CMP     AL,CRT_MODE ;
1023 74 03         2829      JE      SG_2 ;
1025 46           2830      INC     SI ;
1026 EB F0        2831      JMP     SG_1 ;
1028              2832
1028 FA           2833      SG_2:  MOV     AL,BYTE PTR ES:[BX]
1029 26: 8A 07     2834      CLI ;
102C FE C8        2835      DEC     AL ;
102E A2 0484 R    2836      MOV     ROWS,AL ;
1031 26: 8B 47 01 2837      MOV     AX,WORD PTR ES:[BX][1]
1035 A3 0485 R    2838      MOV     POINTS,AX ;
1038 26: 8B 47 03 2839      MOV     AX,WORD PTR ES:[BX][3]
103C A3 010C R    2840      MOV     WORD PTR GRX_SET,AX ;
103F 26: 8B 47 05 2841      MOV     AX,WORD PTR ES:[BX][5]
1043 A3 010E R    2842      MOV     WORD PTR GRX_SET + 2,AX ;
1046 FB           2843      STI ;
1047              2844
1047 EB 50          2845      J4J:   JMP     SHORT AHO_DONE
1049              2846
1049 C4 1E 0448 R 2847      SAVE_GRPH: LES     BX,SAVE_PTR
104D 83 C3 08      2848      ADD     BX,08H ;
1050 26: C4 1F    2849      LES     BX,DWORD PTR ES:[BX]
1053 8C C0        2850      MOV     AX,ES ;
1055 0B C3        2851      OR      AX,BX ;
1057 74 40        2852      JZ      AHO_DONE ;
1059 BE 0008      2853      MOV     SI,08H ;
105C              2854
105C 26: 8A 00     2855      SA_1:  MOV     AL,ES:[BX][SI]
105F 3C FF        2856      CMP     AL,OFFH ;
1061 74 36        2857      JNE     AHO_DONE ;
1063 3A 06 0449 R 2858      CMP     AL,CRT_MODE ;
1067 74 03         2859      JE      SA_2 ;
1069 46           2860      INC     SI ;
106A EB F0        2861      JMP     SA_1 ;
106C              2862
106C 26: 8A 27     2863      SA_2:  MOV     AH,ES:[BX]
106F 26: 8A 47 01 2864      MOV     AL,ES:[BX][1]
1073 26: 8B 4F 02 2865      MOV     CX,ES:[BX][2]
1077 26: 8B 57 04 2866      MOV     DX,ES:[BX][4]
107B 26: 8B 4F 06 2867      MOV     BP,ES:[BX][6]
107F 26: 8E 47 08 2868      MOV     ES,ES:[BX][8]
1083 53           2869      PUSH   BX ;
1084 8B 08        2870      MOV     BX,AX ;
1086 8B 1110      2871      MOV     AX,1110H ;
1089 CD 10        2872      INT     10H ;
108B 5B           2873      POP     BX ;
108C 26: 8A 47 0A 2874      MOV     AL,ES:[BX][0AH]
1090 3C FF        2875      CMP     AL,OFFH ;
1092 74 05        2876      JNE     AHO_DONE ;
1094 FE C8        2877      AL     DEC ;
1096 A2 0484 R    2878      MOV     ROWS,AL ;
1098              2879
1098              2880
1098              2881
1098              2882
1099              2883      ;---- SET THE LOW RAM VALUES FOR COMPATIBILITY (308 AND 309 SAVE BYTES)
1099 E8 0CFE R    2884      AHO_DONE: CALL    DDS ;
109C 80 3E 0449 R 07 2885      CMP     CRT_MODE,7 ;
10A1 77 1E        2886      JNE     DNDCS ;
10A3 BB 10C8 R    2887      MOV     BX,OFFSET COMPAT_MODE ;
10A6 A0 0449 R    2888      MOV     AL,CRT_MODE ;
10A9 2A E4        2889      SUB     AH,AH ;
10AB 03 08        2890      ADD     BX,AX ;
10AD 2E: 8A 07     2891      MOV     AL,CS:[BX] ;
10B0 A2 0465 R    2892      MOV     CRT_MODE_SET,AL ;
10B3 80 30        2893      JA      AHO_DONE ;
10B5 80 3E 0449 R 06 2894      CMP     CRT_MODE,6 ;
10B8 75 02        2895      JNE     DO_PAL ;
10BC 80 3F        2896      MOV     AL,03FH ;
10BE              2897
10BE A2 0466 R    2898      DO_PAL: MOV     CRT_PALETTE,AL

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10C1          2899
10C1 8B 0E 0460 R      2900
10C5  EB 28 90         2901
                               2902
                               2903
10C8          2904
10C8 2C 28 2D 29 2A 2E 2904
10CE 1E 29           2905
                               2906
                               2907
                               2908
                               2909
                               2910
1000          2911
1000 80 FD 00         2912
10D3 75 04         2913
10D5  FE C1         2914
10D7  EB 0A         2915
10D9          2916
10D9  FE C1         2917
10DB 3A 0E 0485 R   2918
10DB 72 02         2919
10E1 2A C9         2920
10E3          2921
10E3 51           2922
10E4 2A CD         2923
10E6 80 F9 10      2924
10E9 59           2925
10EA 75 02         2926
10EC  FE C1         2927
10EE          2928
10EE  C3           2929
10EF          2930
                               2931
                               2932
                               2933
                               2934
                               2935
                               2936
                               2937
                               2938
                               2939
                               2940
= 0004        2941
10EF          2942
                               2943
10EF 8A 04         2944
10F1 89 0E 0460 R   2945
10F5  F6 06 0487 R 0B 2946
10FA 75 33         2947
                               2948
                               2949
10FC 8A C5         2950
10FE 24 60         2951
1100 3C 20         2952
1102 75 05         2953
1104 89 1E00       2954
1107  EB 26         2955
                               2956
                               2957
                               2958
                               2959
1109          2960
1109 F6 06 0487 R 01 2961
110E 75 1F         2962
1110 80 3E 0449 R 03 2963
1115 77 15         2964
1117  EB 0E9A R     2965
111A 73 10         2966
111C 80 FD 04       2967
111F 76 03         2968
1121 80 C5 05       2969
1124          2970
1124 80 F9 04       2971
1127 76 03         2972
1129 80 C1 05       2973
112C          2974
112C  E8 1000 R     2975
112F          2976
112F  E8 1135 R     2977
1132  E9 219E R     2978
                               2979
                               2980
                               2981
1135          2982
1135 8B 16 0463 R   2983
1139 8A C5         2984
113B  E8 0D15 R     2985
113E  FE C4         2986
1140 8A C1         2987
1142  E8 0D15 R     2988
1145  C3           2989
                               2990
                               2991
                               2992
                               2993
                               2994
                               2995
                               2996
                               2997
                               2998
                               2999
1146          3000
1146 53           3001
1147 8B D8         3002
1149 8A C4         3003
114B  F6 26 044A R 3004
114F 32 FF         3005
1151 03 C3         3006
1153  D1 E0         3007
1155 5B           3008
1156  C3           3009
1157          3010
                               3011
                               3012
                               3013
                               3014
                               3015
                               3016
                               3017
                               3018
                               3019
                               3020
                               3021
                               3022
                               3023
1157          3024
1157  E8 115D R     3024

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DNDCS:
MOV CX,CURSOR_MODE
JMP AH1

COMPAT_MODE LABEL BYTE
DB 02CH,02BH,02DH,029H,02AH,02EH
DB 01EH,029H

C
C INCLUDE V1-5.INC
C SUBTTL V1-5.INC
C PAGE

CALC_CURSOR PROC NEAR
ASSUME DS:ABS0
CMP CH,0 ; CHECK FOR FULL HEIGHT
JNE CC,1 ; NORMAL CHECK
INC CL ; ADJUST END VALUE
JMP SHORT CALC_OUT

CC_1:
INC CL ; ADJUST FOR EGA REGISTERS
CMP CL,BYTE PTR POINTS ; WILL IT WRAP
JB CALC_OUT ; NO, ITS OK
SUB CL,CL ; EGA METHOD FOR CURSOR END

CALC_OUT:
PUSH CX ; SAVE CURSOR TYPE VALUE
SUB CL,CH ; END - START
CMP CL,010H ; LOW NIBBLE EQUAL
CX ; RESTORE
JNE COMP_4 ; ADD 1 FOR CORRECT CURSOR
INC CL ; BACK TO CALLER

COMP_4:
RET

CALC_CURSOR ENDP

-----
; SET_CTYPE SET CURSOR TYPE
; THIS ROUTINE SETS THE CURSOR VALUE
; INPUT (CX) HAS CURSOR VALUE CH-START LINE, CL-STOP LINE
; OUTPUT NONE
;-----
CUT_OFF EQU 4

AH1:
ASSUME DS:ABS0
MOV AH,C_CRSR_START ; CRTC REG FOR CURSOR SET
MOV CURSOR_MODE,CX ; SAVE IN DATA AREA
TEST INFO,B ; EGA ACTIVE BIT
JNZ DO_SET ; 0=EGA, 1=OLD CARDS

;----- THIS SECTION WILL EMULATE CURSOR OFF ON THE EGA
MOV AL,CH ; GET START VALUE
AND AL,060H ; TURN OFF CURSOR ?
CMP AL,020H ; TEST THE BITS
JNE AH1_A ; SKIP CURSOR OFF
MOV CX,D1E00H ; EMULATE CURSOR OFF
JMP SHORT DO_SET

;----- THIS SECTION : ADJUST THE CURSOR AND TEST FOR ENHANCED OPERATION
AH1_A:
TEST INFO,1 ; CURSOR EMULATE BIT
JNZ DO_SET ; 0=EMULATE, 1=VALUE AS-IS
CMP CRT_MODE,3 ; POSSIBLE EMULATION
JNE AH1_S ; NO, SET THE CURSOR TYPE
CALL BRST_DET ; SEE IF EMULATE MODE
JNC AH1_S ; NOT EMULATING
CMP CH,CUT_OFF ; TEST START
JBE AH1_B ; SKIP ADJUST
ADD CH,5 ; ADJUST

AH1_B:
CMP CL,CUT_OFF ; TEST END
JBE AH1_S ; SKIP ADJUST

AH1_S:
CALL CALC_CURSOR ; ADJUST END REGISTER
DO_SET:
CALL M16 ; OUTPUT CX REG
JMP V_RET ; RETURN TO CALLER

;----- THIS ROUTINE OUTPUTS THE CX REGISTER TO THE CRTC REGS NAMED IN AH
M16:
MOV DX,ADDR_6845 ; ADDRESS REGISTER
MOV AL,CH ; DATA
CALL OUT_DX ; OUTPUT THE VALUE
INC AH ; NEXT REGISTER
MOV AL,CL ; SECOND DATA VALUE
CALL OUT_DX ; OUTPUT THE VALUE
RET ; ALL DONE

-----
; POSITION PROC NEAR
; THIS SERVICE ROUTINE CALCULATES THE REGEN BUFFER
; ADDRESS OF A CHARACTER IN THE ALPHA MODE
; INPUT AX = ROW, COLUMN POSITION
; OUTPUT AX = OFFSET OF CHAR POSITION IN REGEN BUFFER
;-----
POSITION PROC NEAR
PUSH BX ; SAVE REGISTER
MOV BX,AX
MOV AL,AH
MUL BYTE PTR CRT_COLS ; DETERMINE BYTES TO ROW
KOR BH,BH ; ZERO OUT
ADD AX,BX ; ADD IN COLUMN VALUE
SAL AX,1 ; * 2 FOR ATTRIBUTE BYTES
POP BX ; RESTORE REGISTER
POSITION ENDP

-----
; SET_CPOS SET CURSOR POSITION
; THIS ROUTINE SETS THE CURRENT CURSOR POSITION TO THE
; NEW X-Y VALUES PASSED
; INPUT DX = ROW,COLUMN OF NEW CURSOR
; BH = DISPLAY PAGE OF CURSOR
; OUTPUT CURSOR IS SET AT CRTC IF DISPLAY PAGE IS CURRENT
; DISPLAY
;-----
AH2:
CALL SET_CPOS

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115A E9 219E R      3025 C ----- JMP V_RET
115D                3026 C
115D 8A CF          3027 C SET_CPOS:
115F 32 ED          3028 C     MOV CL,BH
1161 D1 E1          3029 C     XOR CL,CH
1163 8B F1          3030 C     SAL CX,1
1165 89 94 0450 R   3031 C     MOV SI,CX
1169 38 3E 0462 R   3032 C     MOV [SI+OFFSET_CURSOR_POSN],DX
116D 75 05          3033 C     CMP ACTIVE_PAGE,BH
116F 8B C2          3034 C     MOV AH,DX
1171 E8 1175 R      3035 C     CALL M18
1174 C3            3036 C M17:
1175                3037 C     RET
1176                3038 C
1177                3039 C ;---- SET CURSOR POSITION, AX HAS ROW/COLUMN FOR CURSOR
1178                3040 C
1175 E8 1146 R      3041 C M18
1178 8B C8          3042 C     PROC NEAR
117A 03 0E 044E R   3043 C     CALL POSITION
117B                3044 C     CX,AX
117C                3045 C     ADD CX,CRT_START
117E D1 F9          3046 C     ; ADD IN THE START ADDR
1180 B4 0E          3047 C     ; FOR THIS PAGE
1182 E8 1135 R      3048 C     ; / 2 FOR CHAR ONLY COUNT
1185 C3            3049 C     ; REGISTER NUMBER FOR CURSOR
1186                3050 C     ; SET VALUE TO CRTC
1187                3051 C
1188                3052 C M18
1189                3053 C     ENDP
1190                3054 C
1191                3055 C ;-----
1192                3056 C READ_CURSOR
1193                3057 C THIS ROUTINE READS THE CURRENT CURSOR VALUE FROM
1194                3058 C MEMORY AND SENDS IT BACK TO THE CALLER
1195                3059 C
1196                3060 C INPUT
1197                3061 C BH - PAGE OF CURSOR
1198                3062 C
1199                3063 C OUTPUT
1200                3064 C DX - ROW, COLUMN OF THE CURRENT CURSOR POSITION
1201                3065 C CX - CURRENT CURSOR MODE
1202                3066 C ;-----
1203                3067 C AH3:
1204                3068 C     MOV BL,BH
1205                3069 C     XOR BH,BH
1206                3070 C     SAL BL,1
1207                3071 C     MOV DX,[BX + OFFSET_CURSOR_POSN]
1208                3072 C     MOV CX,CURSOR_MODE
1209                3073 C     POP DI
1210                3074 C     POP SI
1211                3075 C     POP BX
1212                3076 C     POP AX
1213                3077 C     POP CX
1214                3078 C     POP DS
1215                3079 C     POP ES
1216                3080 C     POP BP
1217                3081 C     IRET
1218                3082 C
1219                3083 C ;---- READ LIGHT PEN POSITION
1220                3084 C
1221                3085 C AH4:
1222                3086 C     MOV AL,CRT_MODE
1223                3087 C     CMP AL,07H
1224                3088 C     JA READ_LPEN
1225                3089 C
1226                3090 C     TEST INFO,2
1227                3091 C     JZ EGA_IS_COLOR
1228                3092 C
1229                3093 C ;---- MONOCHROME HERE (MONOC BIT 1)
1230                3094 C
1231                3095 C     CMP AL,07H
1232                3096 C     JE READ_LPEN
1233                3097 C     JMP OLD_LP
1234                3098 C
1235                3099 C ;---- EGA IS COLOR HERE (MONOC BIT 0)
1236                3100 C
1237                3101 C EGA_IS_COLOR:
1238                3102 C     CMP AL,06H
1239                3103 C     JBE READ_LPEN
1240                3104 C
1241                3105 C OLD_LP:
1242                3106 C     INT 42H
1243                3107 C     POP DI
1244                3108 C     POP SI
1245                3109 C     ADD SP,6
1246                3110 C     POP DS
1247                3111 C     POP ES
1248                3112 C     POP BP
1249                3113 C     IRET
1250                3114 C
1251                3115 C ;-----
1252                3116 C LIGHT PEN
1253                3117 C THIS ROUTINE TESTS THE LIGHT PEN SWITCH AND THE LIGHT
1254                3118 C PEN TRIGGER. IF BOTH ARE SET, THE LOCATION OF THE LIGHT
1255                3119 C PEN IS DETERMINED. OTHERWISE, A RETURN WITH NO
1256                3120 C INFORMATION IS MADE.
1257                3121 C
1258                3122 C ON EXIT
1259                3123 C (AH) = 0 IF NO LIGHT PEN INFORMATION IS AVAILABLE
1260                3124 C BX,CX,DX ARE DESTROYED
1261                3125 C (AH) = 1 IF LIGHT PEN IS AVAILABLE
1262                3126 C (DH,DL) = ROW,COLUMN OF CURRENT LIGHT PEN
1263                3127 C POSITION
1264                3128 C (CH) = RASTER POSITION (OLD MODES)
1265                3129 C (CX) = RASTER POSITION (NEW MODES)
1266                3130 C (BX) = BEST GUESS AT PIXEL HORIZONTAL POSITION:
1267                3131 C ;-----
1268                3132 C ASSUME CS:CODE,DS:ABS0
1269                3133 C ;----- SUBTRACT TABLE
1270                3134 C V1
1271                3135 C LABEL BYTE
1272                3136 C DB 006H,006H,007H,007H,005H,005H ; 0-5
1273                3137 C DB 004H,005H,000H,000H,000H,000H ; 6-8
1274                3138 C DB 000H,005H,006H,004H,004H,004H ; C-11
1275                3139 C DB 004H,006H,006H,004H,007H,004H ; 12-17
1276                3140 C DB 007H,004H ; 18-19
1277                3141 C
1278                3142 C READ_LPEN PROC NEAR
1279                3143 C ;---- WAIT FOR LIGHT PEN TO BE DEPRESSED
1280                3144 C
1281                3145 C     MOV DX,ADDR_6845
1282                3146 C     ADD DX,6
1283                3147 C     IN AL,DX
1284                3148 C     TEST AL,4
1285                3149 C     MOV AH,0
1286                3150 C     JZ V9
1287                3151 C     JMP V6
1288                3152 C
1289                3153 C ;---- NOW TEST FOR LIGHT PEN TRIGGER
1290                3154 C
1291                3155 C V9:
1292                3156 C     TEST AL,2
1293                3157 C ; TEST LIGHT PEN TRIGGER

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11EE 75 03          3151 C C      JMP      V7A          ; RETURN WITHOUT RESETTING
11F0 E9 129B R     3152 C C C C     JMP      V7          ; TRIGGER
                               3153 C C C C     ; EXIT LIGHT PEN ROUTINE
                               3154 C C C C     ;----- TRIGGER HAS BEEN SET, READ THE VALUE IN
                               3155 C C C C     ;
11F3              3157 C C C C     V7A:      MOV      AH,16      ; LIGHT PEN REGISTERS
11F3 B4 10          3158 C C C C     ;----- INPUT REGS POINTED TO BY AH, AND CONVERT TO ROW COLUMN IN DX
                               3159 C C C C     ;
11F5 8B 16 0463 R  3161 C C C C     ; ADDRESS REGISTER
11F9 84 C4          3162 C C C C     MOV      DX,ADDR_6845 ; REGISTER TO READ
11FB EE            3163 C C C C     MOV      AL,AH        ; SET IT UP
11FC 42            3164 C C C C     OUT     DX,AL        ; DATA REGISTER
11FD 50            3165 C C C C     INC     DX           ;
11FE EC            3166 C C C C     AX      PUSH        ;
11FF 8A E8         3167 C C C C     IN     AL,DX        ; GET THE VALUE
1201 58            3168 C C C C     MOV     CH,AL       ; SAVE IN CX
1202 4A            3169 C C C C     POP     AX          ; ADDRESS REGISTER
1203 FE C4         3170 C C C C     DEC     DX          ;
1205 8A C4         3171 C C C C     INC     AH          ; SECOND DATA REGISTER
1207 EE            3172 C C C C     MOV     AL,AH      ;
1208 42            3173 C C C C     OUT     DX,AL      ;
1209 EC            3174 C C C C     INC     DX          ; POINT TO DATA REGISTER
120A 8A E5         3175 C C C C     MOV     AL,DX      ; GET THE 2ND DATA VALUE
                               3176 C C C C     MOV     AH,CH      ; AX HAS INPUT VALUE
                               3177 C C C C     ;----- AX HAS THE VALUE READ IN FROM THE 6845
                               3178 C C C C     ;
120C 8A 1E 0449 R  3180 C C C C     MOV     BL,CRT_MODE ; MODE VALUE TO BX
1210 2A FF         3181 C C C C     SUB     BH,BH       ; AMOUNT TO SUBTRACT
1212 3E 8A 9F 11C1 R 3182 C C C C     MOV     BL,CS-V1[BX] ;
1217 2B C3         3183 C C C C     SUB     AX,BX       ; TAKE IT AWAY
1219 8B 1E 044E R  3184 C C C C     MOV     BX,CRT_START ; SCREEN ADDRESS
121D D1 E8         3185 C C C C     SHR     BX,1        ; DIVIDE BY 2
121F 2B C3         3186 C C C C     SUB     AX,BX       ; ADJUST TO ZERO START
1221 79 02         3187 C C C C     JNS    V2          ; IF POSITIVE, GET MODE
1223 2B C0         3188 C C C C     SUB     AX,AX       ; <0 PLAYS AS 0
                               3189 C C C C     ;----- DETERMINE MODE OF OPERATION
                               3190 C C C C     ;
1225              3191 C C C C     V2:      MOV     CL,3        ; DETERMINE_MODE
1225 B1 03         3192 C C C C     ; SET *8 SHIFT COUNT
1227 80 3E 0449 R 04 3193 C C C C     CMP     CRT_MODE,4  ; GRAPHICS OR ALPHA
122C 72 4D         3195 C C C C     JB     V4          ; ALPHA_PEN
122E 80 3E 0449 R 07 3196 C C C C     CMP     CRT_MODE,7  ;
1233 74 46         3197 C C C C     JE     V4          ; ALPHA_PEN
                               3198 C C C C     ;
1235 80 3E 0449 R 06 3199 C C C C     CMP     CRT_MODE,06H ;
123A 77 2F         3200 C C C C     J     V8          ;
123C 75 02         3201 C C C C     JNE    V8X        ;
123E D1 E8         3202 C C C C     SHR     AX,1        ;
                               3203 C C C C     ;----- OLD GRAPHICS MODES
                               3204 C C C C     ;
1240              3205 C C C C     V8X:     MOV     DL,40      ; DIVISOR FOR GRAPHICS
1240 B2 28         3206 C C C C     DIV     DL,DL       ; ROW(AL) AND COLUMN(AH)
1242 F6 F2         3207 C C C C     ; AL RANGE 0-99,
                               3208 C C C C     ; AH RANGE 0-39
                               3209 C C C C     ;----- DETERMINE GRAPHIC ROW POSITION
                               3210 C C C C     ;
1244 8A E8         3212 C C C C     MOV     CH,AL      ; SAVE ROW VALUE IN CH
1246 02 ED         3213 C C C C     ADD     CH,CH      ; *2 FOR EVEN/ODD FIELD
1248 8A DC         3214 C C C C     MOV     BL,AH      ; COLUMN VALUE TO BX
124A 2A FF         3215 C C C C     SUB     BH,BH      ; *8 FOR MEDIUM RES
124C 80 3E 0449 R 06 3216 C C C C     CMP     CRT_MODE,6 ; MEDIUM OR HIGH RES
1251 75 04         3217 C C C C     JNE    V3         ; NOT HIGH_RES
1253 B1 04         3218 C C C C     MOV     CL,4       ; SHIFT VALUE FOR HIGH RES
1255 D0 E4         3219 C C C C     SAL     AH,1       ; COLUMN VALUE *2 FOR HIGH RES
1257 D3 E3         3220 C C C C     V3:     SHL     BX,CL    ; NOT HIGH_RES
                               3221 C C C C     ; *16 FOR HIGH_RES
                               3222 C C C C     ;----- DETERMINE ALPHA CHAR POSITION
                               3223 C C C C     ;
1259 8A D4         3225 C C C C     MOV     DL,AH      ; COLUMN VALUE FOR RETURN
125B 8A F0         3226 C C C C     MOV     DH,AL      ; ROW VALUE
125D D0 EE         3227 C C C C     SHR     DH,1       ;
125F D0 EE         3228 C C C C     SHR     DH,1       ; DIVIDE BY 4
1261 EB 2C 90      3230 C C C C     JMP     V5         ; FOR VALUE IN 0-2H RANGE
1264              3231 C C C C     V8:     MOV     V5,RET     ; LIGHT_PEN_RETURN_SET
                               3232 C C C C     ;----- NEW GRAPHICS MODES
                               3233 C C C C     ;
1264 99            3234 C C C C     CWD    CRT_COLS    ; PREPARE TO DIVIDE
1265 F7 36 044A R  3235 C C C C     DIV     CRT_COLS   ; AX = ROW, DX = COLUMN
1269 8B DA         3236 C C C C     MOV     BX,DX      ; SAVE REMAINDER
126B D3 E3         3237 C C C C     SAL     BX,CL      ;
126D 8B C8         3238 C C C C     MOV     CX,AX      ; PEL COLUMN
126F 52            3239 C C C C     PUSH   DX          ; PEL ROW
1270 99            3240 C C C C     CWD    DX          ; SAVE FROM DIVIDE
1271 F7 36 0485 R  3241 C C C C     DIV     CRT_COLS   ; PREPARE TO DIVIDE
1275 5A            3242 C C C C     DIV     DX,DX      ; DIVIDE BY BYTES/CHAR
1276 8A F0         3243 C C C C     POP     DX         ; RECOVER
1278 EB 15 90      3244 C C C C     MOV     DH,AL      ; CHARACTER ROW
                               3245 C C C C     JMP     V5         ;
                               3246 C C C C     ;----- ALPHA MODE ON LIGHT PEN
                               3247 C C C C     ;
127B              3248 C C C C     V4:     MOV     DH,AL      ; ALPHA_PEN
127B F6 36 044A R  3250 C C C C     DIV     CRT_COLS   ; ROW,COLUMN VALUE
127F 8A F0         3251 C C C C     MOV     DH,AL      ; ROWS TO DH
1281 8A D4         3252 C C C C     MOV     DL,AH      ; COLS TO DL
1283 8A DC         3253 C C C C     MOV     BL,AH      ; COLUMN VALUE
1285 32 FF         3254 C C C C     XCHG  BH,BH       ; TO BX
1287 D3 E3         3255 C C C C     SAL     BX,CL      ;
1289 F6 26 0485 R  3256 C C C C     MUL     BYTE PTR POINTS ;
128D 8B C8         3257 C C C C     MOV     CX,AX      ;
128F              3258 C C C C     V5:     MOV     AH,1      ; LIGHT_PEN_RETURN_SET
128F B4 01         3259 C C C C     ; INDICATE EVERYTHING SET
1291 52            3260 C C C C     PUSH   DX          ; LIGHT_PEN_RETURN
1291 52            3261 C C C C     ; SAVE RETURN VALUE
1292 8B 16 0463 R  3262 C C C C     MOV     DX,ADDR_6845 ; (IN CASE)
1296 83 C2 07      3263 C C C C     ADD     DX,7        ; GET BASE ADDRESS
1299 EE            3264 C C C C     OUT     DX,AL      ; POINT TO RESET PARM
129A 5A            3265 C C C C     POP     DX         ; ADDRESS, NOT DATA,
129B              3266 C C C C     ; IS IMPORTANT
1298 5F            3267 C C C C     POP     DX         ; RECOVER VALUE
129C 5E            3268 C C C C     ; RETURN_NO_RESET
129D 83 C4 06      3269 C C C C     V7:     POP     D1         ;
12A0 1F            3270 C C C C     POP     SI         ;
12A1 07            3271 C C C C     ADD     SP,6        ; DISCARD SAVED BX,CX,DX
12A2 5D            3272 C C C C     POP     DS         ;
12A3 CF            3273 C C C C     POP     ES         ;
12A4              3274 C C C C     POP     BP         ;
                               3275 C C C C     ; READ_LPEN      ENDP
                               3276 C C C C     ;

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3277 C
3278 C
3279 C ACT_DISP_PAGE SELECT ACTIVE DISPLAY PAGE
3280 C THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE, ALLOWING
3281 C FOR MULTIPLE PAGES OF DISPLAYED VIDEO.
3282 C
3283 C INPUT AL HAS THE NEW ACTIVE DISPLAY PAGE
3284 C
3285 C OUTPUT THE CRTC IS RESET TO DISPLAY THAT PAGE
3286 C
3287 C
3288 C AH5:
3289 C MOV ACTIVE_PAGE,AL ; SAVE ACTIVE PAGE VALUE
3290 C MOV CX,CRT_LEN ; GET SAVED LENGTH OF
3291 C ; REGEN BUFFER
3292 C CBN ; CONVERT AL TO WORD
3293 C PUSH AX ; SAVE PAGE VALUE
3294 C MUL CX ; DISPLAY PAGE TIMES
3295 C ; REGEN LENGTH
3296 C MOV CRT_START,AX ; SAVE START ADDRESS FOR
3297 C ; LATER REQUIREMENTS
3298 C MOV CX,AX ; START ADDRESS TO CX
3299 C CMP BL,7 ; DO NOT DIVIDE BY TWO
3300 C JA ADP_1
3301 C
3302 C ADP_2: SAR CX,1 ; / 2 FOR CRTC HANDLING
3303 C
3304 C ADP_1: MOV AH,C_STRT_HGH ; REG FOR START ADDRESS
3305 C CALL M16 ; RECOVER PAGE VALUE
3306 C POP BX ; *2 FOR WORD OFFSET
3307 C SAL BX,1 ; GET CURSOR FOR THIS PAGE
3308 C MOV AX,[BX + OFFSET CURSOR_POSN] ; SET THE CURSOR POSITION
3309 C CALL M18
3310 C JMP V_RET
3311 C
3312 C SUBTTL
3313 C
3314 C INCLUDE VSCROLL.INC
3315 C SUBTTL VSCROLL.INC
3316 C PAGE
3317 C
3318 C FLTA PROC NEAR ; CHECK FOR SCROLL COUNT
3319 C PUSH AX
3320 C MOV AH,DH ; LOWER ROW
3321 C SUB AH,CH ; UPPER ROW
3322 C INC AH ; NUMBER TO SCROLL
3323 C CMP AH,POP ; SAME AS REQUESTED
3324 C AX POP
3325 C JNE LTA
3326 C SUB AL,AL ; YES, SET TO 0 FOR BLANK
3327 C
3328 C LTA: RET
3329 C
3330 C FLTA ENDP
3331 C
3332 C CRANK PROC NEAR ; MOVE ROWS OF PELS UP
3333 C PUSH BX
3334 C ASSUME DS:ABSO
3335 C DS DS ; SAVE DATA SEGMENT
3336 C CALL DDS ; SET DATA SEGMENT
3337 C MOV BX,CRT_COLS
3338 C POP DS
3339 C CRANK_A: PUSH CX ; SAVE MOVE COUNT
3340 C MOV CL,DL ; COLUMN COUNT
3341 C SUB CH,CH ; CLEAR HIGH BYTE
3342 C PUSH SI ; SAVE POINTERS
3343 C D1 ;
3344 C REP MOVSB ; MOVE THAT ROW
3345 C POP DI ; RECOVER POINTERS
3346 C POP SI ;
3347 C ADD SI,BX ; NEXT ROW
3348 C ADD DI,BX ; NEXT ROW
3349 C POP CX ; RECOVER ROW COUNT
3350 C LOOP CRANK_A ; DO MORE
3351 C POP BX ;
3352 C RET ; RETURN TO CALLER
3353 C
3354 C CRANK_4 ENDP
3355 C
3356 C CRANK_4 PROC NEAR ; MOVE ROWS OF PELS DOWN
3357 C PUSH BX
3358 C ASSUME DS:ABSO
3359 C DS DS ; SAVE DATA SEGMENT
3360 C MOV BX,CRT_COLS ; SET DATA SEGMENT
3361 C POP DS
3362 C CRANK_B: PUSH CX ; SAVE MOVE COUNT
3363 C MOV CL,DL ; COLUMN COUNT
3364 C SUB CH,CH ; CLEAR HIGH BYTE
3365 C PUSH SI ; SAVE POINTERS
3366 C PUSH DI ;
3367 C REP MOVSB ; MOVE THAT ROW
3368 C POP DI ; RECOVER POINTERS
3369 C POP SI ;
3370 C SUB SI,BX ; NEXT ROW
3371 C SUB DI,BX ; NEXT ROW
3372 C POP CX ; RECOVER ROW COUNT
3373 C LOOP CRANK_B ; DO MORE
3374 C POP BX ;
3375 C RET ; RETURN TO CALLER
3376 C
3377 C CRANK_4 ENDP
3378 C
3379 C PART_1 PROC NEAR ; FILL ROW AFTER SCROLL
3380 C PUSH DX
3381 C MOV DH,3
3382 C MOV DL,SEQ_ADDR
3383 C MOV AX,O20FH ; SEQUENCER
3384 C CALL OUT_DX ; MAP MASK
3385 C POP DX ; ALL MAPS ON
3386 C SUB AX,AX ; ZERO
3387 C MOV CL,DL ; COLUMN COUNT
3388 C SUB CH,CH ;
3389 C PUSH DI ;
3390 C REP STOSB ; SAVE POINTER
3391 C POP DI ; CLEAR ONE ROW OF PELS
3392 C MOV AL,DH ; RECOVER POINTER
3393 C PUSH DX ; GET COLOR VALUE
3394 C MOV DH,3 ;
3395 C MOV DL,SEQ_ADDR ; SEQUENCER
3396 C MOV AH,O2H ; MAP MASK
3397 C CALL OUT_DX ; SET THE COLOR
3398 C POP DX ;
3399 C MOV AL,OFFH ; ALL BITS ON
3400 C MOV CL,DL ; COLUMN COUNT
3401 C PUSH DI ; SAVE POINTER
3402 C REP STOSB ; TURN ON THOSE BITS IN

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1346 5F          3403          C          POP          DI          ; ENABLE PLANES
1347 C3          3404          C          RET          ; RECOVER POINTER
1348            3405          C          PART_1  ENDP      ; RETURN TO CALLER
1348            3406          C
1348            3407          C
1348            3408          C
1348 B6 03        3409          C          PART_2  PROC    NEAR
1348 B2 C4        3410          C          MOV          DH,3
1348 B8 020F     3411          C          MOV          DL,SEQ_ADDR ; SEQUENCER
1348 E8 0015 R   3412          C          CALL         AK,02DFH    ; MAP MASK, ALL MAPS
1352 C3          3413          C          RET          ; ENABLE THE MAPS
1353            3414          C          PART_2  ENDP      ; RETURN TO CALLER
1353            3415          C
1353            3416          C
1353 1E          3417          C          BLNK_3  PROC    NEAR
1353            3418          C          PUSH         DS          ; BLANK FOR SCROLL UP
1353            3419          C          ASSUME      DS:ABSO    ; SAVE DATA SEGMENT
1353            3420          C          CALL         DDS        ; GET LOW MEMORY SEGMENT
1353 8A F7        3421          C          MOV          DH,BH      ; ATTRIBUTE FOR BLANK LINE
1353 2A FF        3422          C          SUB          BH,BH      ; CLEAR HIGH BYTE
1353 50          3423          C          PUSH         AX          ; SAVE
1353 52          3424          C          PUSH         DX          ; SAVE BECAUSE OF MULTIPLY
1353 B8 C3        3425          C          MOV          AX,BX      ; ROW COUNT
1353 F7 26 0485 R 3426          C          MUL          POINTS    ; CHARACTER HEIGHT
1353 8B D8        3427          C          MOV          BX,AX      ; NET VALUE TO BX
1353 5A          3428          C          POP          DX          ; RECOVER
1353 56          3429          C          POP          AX
1353            3430          C
1353 1F          3431          C          POP          DS          ;
1353            3432          C          ASSUME      DS:NOTHING ;
1353            3433          C
1353 E8 131C R    3434          C          S13:      CALL         PART_1     ; BLANK OUT ROW WITH COLOR
1353            3435          C          ASSUME      DS:ABSO    ;
1353            3436          C          PUSH         DS          ;
1353            3437          C          CALL         DDS        ; SAVE SEGMENT
1353            3438          C          ADD          DI,CRT_COLS ; LOW MEMORY SEGMENT
1353            3439          C          POP          DS          ; NEXT ROW
1353            3440          C          DEC          BX          ; RECOVER
1353            3441          C          JNZ         S13        ; NEXT
1353            3442          C          CALL         PART_2     ; DO MORE
1353            3443          C          RET          ; RETURN TO CALLER
1353            3444          C
1353            3445          C
1353 1E          3446          C          BLNK_4  PROC    NEAR
1353            3447          C          PUSH         DS          ; BLANK FOR SCROLL DOWN
1353            3448          C          ASSUME      DS:ABSO    ; SAVE DATA SEGMENT
1353            3449          C          CALL         DDS        ; GET LOW MEMORY SEGMENT
1353 8A F7        3450          C          MOV          DH,BH      ; ATTRIBUTE FOR BLANK LINE
1353 2A FF        3451          C          SUB          BH,BH      ; CLEAR HIGH BYTE
1353 50          3452          C          PUSH         AX          ; SAVE
1353 52          3453          C          PUSH         DX          ; SAVE BECAUSE OF MULTIPLY
1353 B8 C3        3454          C          MOV          AX,BX      ; ROW COUNT
1353 F7 26 0485 R 3455          C          MUL          POINTS    ; CHARACTER HEIGHT
1353 8B D8        3456          C          MOV          BX,AX      ; NET VALUE TO BX
1353 5A          3457          C          POP          DX          ; RECOVER
1353 56          3458          C          POP          AX
1353            3459          C
1353 1F          3460          C          POP          DS          ;
1353            3461          C          ASSUME      DS:NOTHING ;
1353            3462          C
1353 E8 131C R    3463          C          S13_4:   CALL         PART_1     ; BLANK OUT ROW WITH COLOR
1353            3464          C          ASSUME      DS:ABSO    ;
1353            3465          C          PUSH         DS          ;
1353            3466          C          CALL         DDS        ; SAVE SEGMENT
1353            3467          C          SUB          DI,CRT_COLS ; LOW MEMORY SEGMENT
1353            3468          C          POP          DS          ; NEXT ROW
1353            3469          C          DEC          BX          ; RECOVER
1353            3470          C          JNZ         S13_4     ; DO MORE
1353            3471          C          CALL         PART_2     ; RETURN TO CALLER
1353            3472          C          RET          ;
1353            3473          C
1353            3474          C
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1353            3489          C
1353            3490          C
1353            3491          C
1353 8A D8        3492          C          SCROLL_ UP  PROC    NEAR
1353 E8 16EB R    3493          C          MOV          BL,AL      ; SAVE LINE COUNT IN BL
1353 80 FC 04     3494          C          CALL         MK_ES     ;
1353 72 0E        3495          C          CMP          AH,4       ; TEST FOR GRAPHICS MODE
1353 80 FC 07     3496          C          JNB         N1         ; HANDLE SEPERATELY
1353 74 03       3497          C          CMP          AH,7       ; TEST FOR BW CARD
1353 E9 1474 R   3498          C          JMC         GRAPHICS_UP ;
1353            3499          C          JMP         GRAPHICS_UP ;
1353            3500          C
1353 53          3501          C          N1:      PUSH         BX          ; UP CONTINUE
1353 8B C1        3502          C          MOV          AX,CX      ; SAVE FILL ATTR IN BH
1353 E8 13F2 R   3503          C          CALL         SCROLL_POSITION ; UPPER LEFT POSITION
1353 74 31       3504          C          JZ          N7         ; DO SETUP FOR SCROLL
1353 03 F0       3505          C          ADD          SI,AX      ; BLANK_FIELD
1353 8A E6       3506          C          MOV          AH,DH      ; FROM ADDRESS
1353 2A E3       3507          C          SUB          AH,BL      ; # ROWS IN BLOCK
1353            3508          C          ; # ROWS TO BE MOVED
1353            3509          C          ; ROW LOOP
1353            3510          C          ; MOVE ONE ROW
1353            3511          C
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1353            4000          C

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13EE      8A DE      3529      C N7:                                ; BLANK FIELD
13EF      EB DC      3530      C ADD AX,CRT_START                ; GET ROW COUNT
13F0      EB DC      3531      C JMP N3                          ; GO CLEAR THAT AREA
13F2      EB DC      3532      C SCROLL_UP                       ENDP
13F3      EB DC      3533      C
13F4      EB DC      3534      C
13F5      EB DC      3535      C
13F6      EB DC      3536      C ;----- HANDLE COMMON SCROLL SET UP HERE
13F7      EB DC      3537      C
13F8      EB DC      3538      C
13F9      EB DC      3539      C
13FA      EB DC      3540      C
13FB      EB DC      3541      C
13FC      EB DC      3542      C SCROLL_POSITION PROC NEAR
13FD      EB DC      3543      C TEST INFO,4
13FE      EB DC      3544      C JZ N9
13FF      EB DC      3545      C ;----- 80X25 COLOR CARD SCROLL
1400      EB DC      3546      C
1401      EB DC      3547      C
1402      EB DC      3548      C
1403      EB DC      3549      C
1404      EB DC      3550      C
1405      EB DC      3551      C
1406      EB DC      3552      C
1407      EB DC      3553      C
1408      EB DC      3554      C
1409      EB DC      3555      C
140A      EB DC      3556      C N9:                                ; CONVERT TO REGEN POINTER
140B      EB DC      3557      C CALL AX,CRT_START              ; OFFSET OF ACTIVE PAGE
140C      EB DC      3558      C ADD AX,CRT_START              ; TO ADDRESS FOR SCROLL
140D      EB DC      3559      C MOV SI,AX                      ; FROM ADDRESS FOR SCROLL
140E      EB DC      3560      C SUB CX,DX                       ; CX = #ROWS, #COLS
140F      EB DC      3561      C INC DH                          ; INCREMENT FOR 0 ORIGIN
1410      EB DC      3562      C INC DL                          ; ZERO HIGH BYTE OF COUNT
1411      EB DC      3563      C XOR CX,CH                       ; NUM OF COLS IN DISPLAY
1412      EB DC      3564      C MOV BP,CRT_COLS                ; TIMES 2 FOR ATTR BYTE
1413      EB DC      3565      C ADD BP,BP                       ; GET LINE COUNT
1414      EB DC      3566      C MOV AX,BL                       ; OFFSET TO FROM ADDRESS
1415      EB DC      3567      C MUL BYTE PTR CRT_COLS          ; *2 FOR ATTRIBUTE BYTE
1416      EB DC      3568      C ADD AX,AX                       ; ESTABLISH ADDRESSING
1417      EB DC      3569      C PUSH ES                         ; FOR BOTH POINTERS
1418      EB DC      3570      C POP DS                          ; 0 MEANS BLANK FIELD
1419      EB DC      3571      C CMP BL,0                       ; RETURN WITH FLAGS SET
141A      EB DC      3572      C RET
141B      EB DC      3573      C SCROLL_POSITION ENDP
141C      EB DC      3574      C
141D      EB DC      3575      C ;----- MOVE_ROW
141E      EB DC      3576      C
141F      EB DC      3577      C
1420      EB DC      3578      C N10 PROC NEAR
1421      EB DC      3579      C MOV CL,DL                      ; GET # OF COLS TO MOVE
1422      EB DC      3580      C SI PUSH D1                     ; SAVE START ADDRESS
1423      EB DC      3581      C PUSH D1                         ; MOVE THAT LINE ON SCREEN
1424      EB DC      3582      C REP MOVSW                       ; RECOVER ADDRESSES
1425      EB DC      3583      C POP D1
1426      EB DC      3584      C POP SI
1427      EB DC      3585      C RET
1428      EB DC      3586      C N10 ENDP
1429      EB DC      3587      C
142A      EB DC      3588      C ;----- CLEAR_ROW
142B      EB DC      3589      C
142C      EB DC      3590      C
142D      EB DC      3591      C N11 PROC NEAR
142E      EB DC      3592      C MOV CL,DL                      ; GET # COLUMNS TO CLEAR
142F      EB DC      3593      C SI PUSH D1                     ; STORE THE FILL CHARACTER
1430      EB DC      3594      C REP STOSW
1431      EB DC      3595      C POP D1
1432      EB DC      3596      C POP SI
1433      EB DC      3597      C RET
1434      EB DC      3598      C N11 ENDP
1435      EB DC      3599      C
1436      EB DC      3600      C ;----- SCROLL_DOWN
1437      EB DC      3601      C ; THIS ROUTINE MOVES THE CHARACTERS WITHIN A
1438      EB DC      3602      C ; DEFINED BLOCK DOWN ON THE SCREEN, FILLING THE
1439      EB DC      3603      C ; TOP LINES WITH A DEFINED CHARACTER
1440      EB DC      3604      C
1441      EB DC      3605      C INPUT
1442      EB DC      3606      C (AH) = CURRENT CRT MODE
1443      EB DC      3607      C (AL) = NUMBER OF LINES TO SCROLL
1444      EB DC      3608      C (CX) = UPPER LEFT CORNER OF REGION
1445      EB DC      3609      C (DX) = LOWER RIGHT CORNER OF REGION
1446      EB DC      3610      C (BH) = FILL CHARACTER
1447      EB DC      3611      C (DS) = DATA SEGMENT
1448      EB DC      3612      C (ES) = REGEN SEGMENT
1449      EB DC      3613      C OUTPUT
1450      EB DC      3614      C NONE -- SCREEN IS SCROLLED
1451      EB DC      3615      C ;----- SCROLL_DOWN
1452      EB DC      3616      C SCROLL_DOWN PROC NEAR
1453      EB DC      3617      C STD                             ; SCROLL DOWN
1454      EB DC      3618      C MOV BL,AL                       ; LINE COUNT TO BL
1455      EB DC      3619      C CALL MK_ES                       ; SAVE ATTRIBUTE IN BH
1456      EB DC      3620      C MOV AX,DX                       ; LOWER RIGHT CORNER
1457      EB DC      3621      C CALL SCROLL_POSITION            ; GET REGEN LOCATION
1458      EB DC      3622      C JZ N16                          ; SI IS FROM ADDRESS
1459      EB DC      3623      C SUB SI,AX                       ; GET TOTAL # ROWS
1460      EB DC      3624      C MOV AH,BL                       ; COUNT TO MOVE IN SCROLL
1461      EB DC      3625      C N13:
1462      EB DC      3626      C CALL N10                         ; MOVE ONE ROW
1463      EB DC      3627      C SUB SI,BP
1464      EB DC      3628      C SUB D1,BP
1465      EB DC      3629      C DEC AH
1466      EB DC      3630      C JNZ N13
1467      EB DC      3631      C N14:
1468      EB DC      3632      C POP AX
1469      EB DC      3633      C MOV AL,
1470      EB DC      3634      C ; RECOVER ATTRIBUTE IN AH
1471      EB DC      3635      C N15:
1472      EB DC      3636      C CALL N11                         ; CLEAR ONE ROW
1473      EB DC      3637      C SUB D1,BP                       ; GO TO NEXT ROW
1474      EB DC      3638      C DEC BL
1475      EB DC      3639      C JNZ N15
1476      EB DC      3640      C ; SCROLL_END
1477      EB DC      3641      C N16:
1478      EB DC      3642      C MOV BL,DH
1479      EB DC      3643      C JMP N14
1480      EB DC      3644      C SCROLL_DOWN ENDP
1481      EB DC      3645      C
1482      EB DC      3646      C ;----- SCROLL_UP
1483      EB DC      3647      C ; THIS ROUTINE SCROLLS UP THE INFORMATION ON THE CRT
1484      EB DC      3648      C ; ENTRY
1485      EB DC      3649      C CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL
1486      EB DC      3650      C DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL
1487      EB DC      3651      C ; BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
1488      EB DC      3652      C BH = FILL VALUE FOR BLANKED LINES
1489      EB DC      3653      C AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE
1490      EB DC      3654      C ; FIELD)
1491      EB DC      3655      C DS = DATA SEGMENT

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3655 C ; ES = REGEN SEGMENT ;
3656 C ; EXIT ;
3657 C ; NOTHING, THE SCREEN IS SCROLLED ;
3658 C ;-----
3659 C GRAPHICS_UP PROC NEAR ;
3660 C MOV BL,AL ; SAVE LINE COUNT IN BL
3661 C MOV AX,CX ; GET UPPER LEFT POSITION
3662 C ; INTO AX REG
3663 C ;
3664 C ;----- USE CHARACTER SUBROUTINE FOR POSITIONING
3665 C ;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
3666 C ;
3667 C CALL GRAPH_POS ;
3668 C MOV D1,AX ; SAVE RESULT AS
3669 C ; DESTINATION ADDRESS
3670 C ;
3671 C ;----- DETERMINE SIZE OF WINDOW
3672 C SUB DX,CX ;
3673 C ADD DX,101H ; ADJUST VALUES
3674 C SAL DH,1 ; MULTIPLY # ROWS BY 4
3675 C ; SINCE 8 VERT DOTS/CHAR
3676 C SAL DH,1 ; AND EVEN/ODD ROWS
3677 C ;
3678 C ;----- DETERMINE CRT MODE
3679 C ;
3680 C CMP CRT_MODE,6 ; TEST FOR MEDIUM RES
3681 C JNC R7 ; FIND_SOURCE
3682 C ;
3683 C ;----- MEDIUM RES UP
3684 C ;
3685 C SAL DL,1 ; * 2,
3686 C SAL D1,1 ; SINCE 2 BYTES/CHAR
3687 C ;
3688 C ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
3689 C ;
3690 C R7: ; FIND_SOURCE
3691 C PUSH ES ; GET SEGMENTS BOTH
3692 C POP DS ; POINTING TO REGEN
3693 C SUB CH,CH ; 0 TO HIGH OF COUNT REG
3694 C SAL BL,1 ; NUMBER OF LINES *4
3695 C SAL BL,1 ;
3696 C JZ R11 ; IF 0, BLANK ENTIRE FIELD
3697 C MOV AL,BL ; NUMBER OF LINES IN AL
3698 C MOV AH,80 ; 80 BYTES/ROW
3699 C MOV AH,7000H ; OFFSET TO SOURCE
3700 C MOV SI,D1 ; SET UP SOURCE
3701 C ADD SI,AX ; ADD IN OFFSET TO IT
3702 C MOV AH,DH ; NUMBER OF ROWS IN FIELD
3703 C SUB AH,BL ; DETERMINE NUMBER TO MOVE
3704 C ;
3705 C ;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS
3706 C ;
3707 C R8: ; ROW_LOOP
3708 C CALL R17 ; MOVE ONE ROW
3709 C SUB SI,2000H-80 ; MOVE TO NEXT ROW
3710 C SUB D1,2000H-80 ;
3711 C DEC AH ; NUMBER OF ROWS TO MOVE
3712 C JNZ R8 ; CONTINUE TILL ALL MOVED
3713 C ;
3714 C ;----- FILL IN THE VACATED LINE(S)
3715 C ;
3716 C R9: ; CLEAR_ENTRY
3717 C MOV AL,BH ; ATTRIBUTE TO FILL WITH
3718 C ;
3719 C R10: ; CLEAR THAT ROW
3720 C CALL R18 ; POINT TO NEXT LINE
3721 C SUB D1,2000H-80 ; NUMBER OF LINES TO FILL
3722 C DEC BL ; CLEAR_LOOP
3723 C JNZ R10 ;
3724 C R11: ; BLANK_FIELD
3725 C MOV BL,DH ; SET BLANK COUNT TO
3726 C ; EVERYTHING IN FIELD
3727 C MOV BL,0 ; CLEAR THE FIELD
3728 C JMP R9 ;
3729 C GRAPHICS_UP ENDP
3730 C ;
3731 C ;----- ROUTINE TO MOVE ONE ROW OF INFORMATION
3732 C ;
3733 C R17 PROC NEAR
3734 C MOV CL,DL ; NUM OF BYTES IN THE ROW
3735 C PUSH SI ;
3736 C PUSH DI ; SAVE POINTERS
3737 C REP MOVSB ; MOVE THE EVEN FIELD
3738 C POP DI ;
3739 C POP SI ;
3740 C ADD SI,2000H ; POINT TO THE ODD FIELD
3741 C ADD DI,2000H ;
3742 C PUSH SI ;
3743 C PUSH DI ; SAVE THE POINTERS
3744 C MOV CL,DL ; COUNT BACK
3745 C REP MOVSB ; MOVE THE ODD FIELD
3746 C POP DI ;
3747 C POP SI ; POINTERS BACK
3748 C ; RETURN TO CALLER
3749 C R17 ENDP
3750 C ;
3751 C ;----- CLEAR A SINGLE ROW
3752 C ;
3753 C R18 PROC NEAR
3754 C MOV CL,DL ; NUMBER OF BYTES IN FIELD
3755 C PUSH DI ; SAVE POINTER
3756 C REP STOSB ; STORE THE NEW VALUE
3757 C POP DI ; POINTER BACK
3758 C ADD D1,2000H ; POINT TO ODD FIELD
3759 C PUSH DI ;
3760 C MOV CL,DL ; FILL THE ODD FIELD
3761 C REP STOSB ;
3762 C POP DI ; RETURN TO CALLER
3763 C RET
3764 C R18 ENDP
3765 C ;
3766 C MEM_DET PROC NEAR
3767 C ASSUME DS:ABS0
3768 C PUSH AX
3769 C PUSH DS
3770 C CALL DDS
3771 C MOV AH,INFO
3772 C AND AH,060H
3773 C POP AX
3774 C POP AX
3775 C JZ MIN
3776 C STC
3777 C RET
3778 C MIN:
3779 C CLC
3780 C RET

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150B          3781 C MEM_DET ENDP
              3782 C
              3783 C ;---- SCROLL ACTIVE PAGE UP
              3784 C
150B          3785 SC_2:
150B E9 13A3 R 3786 C
              3787 C
              3788 C
150E          3789 C AH6:
150E E8 12D1 R 3790 C ASSUME DS:ABSO
1511 8A 26 0449 R 3791 C CALL FLTA
1515 80 FC 07 3792 C MOV AH,CRT_MODE ; GET CURRENT MODE
1518 76 F1 3793 C JBE SC_2 ; ANY OF THE OLD MODES
151A 80 FC 0D 3794 C CMP AH,ODH
151D 73 17 3795 C JAE GRAPHICS_UP_2 ; NEW GRAPHICS MODES
151F E9 219E R 3796 C JMP V_RET ; NOT A RECOGNIZED MODE
              3797 C
1522          3798 C GR_ST_1 PROC NEAR
1525 BA A000 3799 C DX,A0000H ; REGEN BUFFER
1528 0D 0511 3800 C MOV BP,0511H ; GRAPHICS WRITE MODE
1528 80 FC 0F 3801 C CMP AH,0FH
152B 72 08 3802 C JZ VV1
152D E8 14F7 R 3803 C CALL MEM_DET
1530 73 03 3804 C JNC VV1
1532 BD 0501 3805 C MOV BP,0501H ; GRAPHICS WRITE MODE
1535          3806 C
1535 C3 3807 C
1536          3808 C
              3809 C
              3810 C
1536          3811 C GRAPHICS_UP_2 PROC NEAR
1537 52 3812 C ASSUME DS:ABSO
1537 E8 1522 R 3813 C PUSH DX
              3814 C CALL GR_ST_1 ; SET SEGMENT, WRITE MODE
              3815 C SRLoad ES ; SET REGEN
              3816 C MOV ES,DX
              3817 C POP DX
153A 8E C2 3818 C MOV BL,AL ; NUMBER OF LINES
153D 8A D8 3819 C MOV AX,CX ; UPPER LEFT CORNER
153F 8B C1 3820 C PUSH BX
1541 53 3821 C MOV BH,ACTIVE_PAGE ; ACTIVE PAGE FOR SCROLL
1542 8A 3E 0462 R 3822 C CALL GR_PSN ; ADDRESS IN REGEN
1544 8B F8 3823 C MOV DI,AX ; SET POINTER
154C 2B D1 3824 C SUB DX,CX ; DETERMINE WINDOW
154E 81 C2 0101 3825 C ADD DX,0101H ; ADJUST
1552 2A E4 3826 C SUB AH,AH ; ZERO HIGH BYTE
1554 8A C3 3827 C MOV AL,BL ; LINE COUNT
1556 52 3828 C PUSH DX
1557 F7 26 0485 R 3829 C MUL POINTS ; BYTES PER CHARACTER
1558 F7 26 044A R 3830 C CRT_COLS ; COLUMNS
155F 8B F7 3831 C MOV SI,BI ; SET UP SOURCE INDEX
1561 03 F0 3832 C ADD SI,AX ; ADJUST
              3833 C ASSUME DS:NOTHING
              3834 C PUSH ES
              3835 C POP DS
              3836 C POP DX
1563 06 3837 C OR BL,BL ; LINE COUNT
1564 1F 3838 C JZ AR9
1565 5A 3839 C MOV CL,DH
1566 0A DB 3840 C CL,BL
1568 74 3F 3841 C JZ CH,CH
156A 8A CE 3842 C MOV CH,CH
156C 2A C8 3843 C SUB CH,CH
156E 2A ED 3844 C
              3845 C
              3846 C ASSUME DS:ABSO
1570 1E 3847 C PUSH DS
1571 E8 0CFE R 3848 C CALL DDS ; LOW MEMORY SEGMENT
1574 50 3849 C PUSH AX
1575 52 3850 C PUSH DX
1576 8B C1 3851 C MOV AX,CX
1578 F7 26 0485 R 3852 C MUL POINTS ; BYTES PER CHAR
157C 8B C8 3853 C MOV CX,AX ; SET THE COUNT
157E 5A 3854 C POP DX
157F 58 3855 C POP AX
1580 1F 3856 C ASSUME DS:NOTHING
              3857 C POP DS
              3858 C
              3859 C PUSH DX
1581 52 3860 C MOV AX,BP
1582 8B C5 3861 C MOV DH,3
1584 B6 03 3862 C MOV DL,GRAPH_ADDR ; GRAPHICS
1586 B2 CE 3863 C DL,GRAPH_ADDR
1588 E8 0D15 R 3864 C CALL OUT_DX ; SEQUENCER
158B B2 C4 3865 C MOV DL,SEQ_ADDR ; ENABLE ALL MAPS
158D 8B 020F 3866 C MOV AX,020FH
1590 E8 0D15 R 3867 C CALL OUT_DX
1593 5A 3868 C POP DX
1594 E8 12E0 R 3869 C CALL CRANK ; SCROLL THE SCREEN
              3870 C
              3871 C PUSH DX
1597 52 3872 C DEC BP
1598 4D 3873 C MOV AX,BP
1599 8B C5 3874 C MOV DH,3
159B B6 03 3875 C MOV DL,GRAPH_ADDR ; GRAPHICS
159D B2 CE 3876 C DL,GRAPH_ADDR
159F E8 0D15 R 3877 C CALL OUT_DX
15A2 5A 3878 C POP DX
15A3 5A 3879 C
15A3 E8 1353 R 3880 C
15A6 E9 219E R 3881 C
              3882 C CALL BLNK_3
15A9 3883 C
              3884 C JMP V_RET
15AB 8A DE 3885 C
15AB EB F6 3886 C
              3887 C
              3888 C
15AD          3889 C AH7:
15AD          3890 C ASSUME DS:ABSO
1580 E8 12D1 R 3891 C CALL FLTA
1583 8A 26 0449 R 3892 C MOV AH,CRT_MODE
1587 80 FC 03 3893 C CMP AH,03H ; OLD COLOR ALPHA
158A 76 F1 3894 C JBE SC_3 ; OLD GRAPHICS MODES
158C 80 FC 07 3895 C CMP AH,07H ; MONOCHROME ALPHA
158F 74 EC 3896 C JE SC_3
              3897 C
              3898 C CMP AH,ODH
15C1 80 FC 0D 3899 C JAE GRAPHICS_DN_2 ; NEW GRAPHICS MODES
15C6 80 FC 06 3900 C CMP AH,06H ; OLD GRAPHICS MODES
15C9 77 04 3901 C JA M_0
15CB 84 07 3902 C MOV AH,07H
15CD CD 42 3903 C INT 42H
15CF          3904 C M_0:
15CF E9 219E R 3905 C JMP V_RET
15D2          3906 C GRAPHICS_DN_2 PROC NEAR
15D2 FD 3907 C ; DIRECTION TO DECREMENT

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1503 8A D8      3907 C      MOV BL,AL          ; LINE COUNT
1505 52         3908 C      PUSH DX           ; SAVE LOWER RIGHT
1506 E8 1522 R  3909 C      CALL GR_ST_1     ; SET REGEN SEGMENT
                3910 C      SRRLOAD ES,DX
1509 8E C2     3911 C+     MOV DX           ; MOV CHAR ROW UP BY ONE
150B 5A       3912 C      POP DX
150C BB C2     3913 C      MOV AX,DX
150E FE C4     3914 C      INC AH
150F 53       3915 C      PUSH BX
1511 8A 3E 0462 R 3916 C      MOV BH,ACTIVE_PAGE
1515 E8 16C6 R  3917 C      CALL GRK_PSN    ; ADDRESS IN REGEN
1518 5B       3918 C      POP BX
1519 2B 06 044A R 3919 C      SUB AX,CRT_COLS ; ONE SCAN OVERSHOOT
1520 BB F8     3920 C      MOV D1,AX
152F 2B D1     3921 C      SUB DX,CX
1531 81 C2 0101 3922 C      ADD DX,0101H   ; CALCULATE WINDOW
1535 2A E8     3923 C      SUB AH,AH      ; ADJUST COUNT
1537 8A C3     3924 C      MOV AL,BL
1539 52       3925 C      PUSH DX
153A F7 26 0485 R 3926 C      MOV POINTS
153E F7 26 044A R 3927 C      MUL CRT_COLS  ; BYTES PER CHAR
1602 8B F7     3928 C      MOV SI,D1
1604 2B F0     3929 C      SUB SI,AX      ; BYTES PER ROW
                3930 C      ASSUME DS:NOTHING
1606 06       3931 C      PUSH ES
1607 1F       3932 C      POP DS
1608 5A       3933 C      POP DX
1609 0A DB     3934 C      OR DL,BL
160B 74 40     3935 C      JZ DKR9       ; SCROLL COUNT
160D 8A CE     3936 C      MOV CL,DH    ; BLANK ENTIRE WINDOW
160F 2A CB     3937 C      SUB CL,BL
1611 2A ED     3938 C      SUB CH,CH
                3939 C
                3940 C      ASSUME DS:ABSO
1613 1E       3941 C      PUSH DS
1614 E8 0CFE R  3942 C      CALL DDS
1617 50       3943 C      PUSH AX
1618 52       3944 C      PUSH DX
1619 BB C1     3945 C      MOV AX,CX
161B F7 26 0485 R 3946 C      MUL POINTS    ; BYTES PER CHAR
161F BB C8     3947 C      MOV CX,AX
1621 5A       3948 C      POP DX
1622 58       3949 C      POP AX
1623 1F       3950 C      ASSUME DS:NOTHING
                3951 C      POP DS
                3952 C
1624 52       3953 C      PUSH DX
1625 BB C5     3954 C      MOV AX,BP
1627 B6 03     3955 C      MOV DH,3
1629 B2 CE     3956 C      MOV DL,GRAPH_ADDR ; GRAPHICS
162B E8 0D15 R  3957 C      CALL OUT_DX
162E B2 C4     3958 C      MOV DL,REQ_ADDR ; SEQUENCER
1630 BB 020F   3959 C      MOV AX,020FH  ; ENABLE ALL MAPS
1633 E8 0D15 R  3960 C      CALL OUT_DX
1636 5A       3961 C      POP DX
1637 E8 12FE R  3962 C      CALL CRANK_4   ; SCROLL THE SCREEN
                3963 C
163A 52       3964 C      PUSH DX
163B 4D       3965 C      DEC BP
163C BB C5     3966 C      MOV AX,BP
163E B6 03     3967 C      MOV DH,3
1640 B2 CE     3968 C      MOV DL,GRAPH_ADDR
1642 E8 0D15 R  3969 C      CALL OUT_DX
1645 5A       3970 C      POP DX
1646         3971 C      DXR10:
1646 E8 137B R  3972 C      CALL BLNK_4
1649 FC       3973 C      CLD
164A E9 219E R  3974 C      JMP V_RET
164D         3975 C      DXR9:
164D 8A DE     3976 C      MOV BL,DH
164F EB F5     3977 C      JMP DXR10     ; BLANK ENTIRE WINDOW
1651         3978 C      GRAPHICS_ON_2
                3979 C      ENDP
                3980 C      SUBTTL
                3981 C
                3982 C      INCLUDE VGRW.INC
                3983 C      SUBTTL VGRW.INC
                3984 C      PAGE
                3985 C
                3986 C      ASSUME DS:ABSO
1651 8A CF     3987 C      FIND_POSITION PROC NEAR
1653 32 ED     3988 C      MOV CL,BH
1655 8B F1     3989 C      XOR CH,CH
1657 D1 E6     3990 C      MOV SI,CX
1659 BB 8A 0450 R 3991 C      SAL SI,1
165D 33 D6     3992 C      MOV AX,[SI+ OFFSET CURSOR_POSN] ; * 2 FOR WORD OFFSET
165F E3 08     3993 C      XOR BX,BX    ; ROW/COLUMN OF THAT PAGE
1661 03 1E 044C R 3994 C      JCXZ P5      ; SET START ADDRESS TO 0
1665 E2 FA     3995 C      P4: ADD BX,CRT_LEN ; NO PAGE
1667         3996 C      LOOP P4      ; PAGE_LOOP
1669         3997 C      P5:          ; LENGTH OF BUFFER
166B E8 1146 R  3998 C      CALL POSITION ; NO PAGE
166A 03 D8     3999 C      ADD BX,AX    ; DETERMINE LOC IN REGEN
166D C3       4000 C      RET         ; ADD TO START OF REGEN
166E         4001 C
                4002 C      FIND_POSITION ENDP
                4003 C
                4004 C      -----
                4005 C      ; EXPAND MED_COLOR
                4006 C      ; THIS ROUTINE EXPANDS THE LOW 2 BITS IN BL TO
                4007 C      ; FILL THE ENTIRE BX REGISTER
                4008 C      ENTRY
                4009 C      ; BL = COLOR TO BE USED ( LOW 2 BITS )
                4010 C      ; EXIT
                4011 C      ; BX = COLOR TO BE USED ( 8 REPLICATIONS OF THE
                4012 C      ; 2 COLOR BITS )
                4013 C      -----
166D 4014 C      S19 PROC NEAR
166D 80 E3 03  4015 C      AND BL,3
1670 8A C3     4016 C      MOV AL,BL
1672 51       4017 C      PUSH CX
1673 B9 0003   4018 C      MOV CX,3
1676         4019 C      S20:
1676 D0 E0     4020 C      SAL AL,1
1678 D0 E0     4021 C      SAL AL,1
167A 0A D8     4022 C      OR BL,AL
167C E2 F8     4023 C      LOOP S20
167E 8A FB     4024 C      MOV BH,BL
1680 59       4025 C      POP CX
1681 C3       4026 C      RET
1682         4027 C      S19 ENDP
                4028 C
                4029 C      -----
                4030 C      ; EXPAND BYTE
                4031 C      ; THIS ROUTINE TAKES THE BYTE IN AL AND DOUBLES
                4032 C      ; ALL OF THE BITS, TURNING THE 8 BITS INTO

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1682      4033 C ; 16 BITS. THE RESULT IS LEFT IN AX
1683      4034 C ;
1684      4035 C S21 PROC NEAR
1685      4036 C PUSH DX ; SAVE REGISTERS
1686      4037 C PUSH CX ;
1687      4038 C PUSH BX ;
1688      4039 C SUB DX,DX ;
1689      4040 C MOV CX,1 ; MASK REGISTER
1690      4041 C S22: ;
1691      4042 C MOV BX,AX ; BASE INTO TEMP
1692      4043 C AND BX,CX ; USE MASK TO EXTRACT BIT
1693      4044 C OR DX,BX ; PUT INTO RESULT REGISTER
1694      4045 C SHL AX,1 ;
1695      4046 C SHL CX,1 ; SHIFT BASE AND MASK BY 1
1696      4047 C MOV AX,AX ; SAVE TO TEMP
1697      4048 C AND BX,CX ; EXTRACT THE SAME BIT
1698      4049 C OR DX,BX ; PUT INTO RESULT
1699      4050 C SHL CX,1 ; SHIFT ONLY MASK NOW
1700      4051 C JNC S22 ; MOVING TO NEXT COME
1701      4052 C JNC S22 ; USE MASK BIT COMING OUT
1702      4053 C MOV AX,DX ; TO TERMINATE
1703      4054 C POP BX ; RESULT TO PARAM REGISTER
1704      4055 C POP CX ;
1705      4056 C POP DX ; RECOVER REGISTERS
1706      4057 C RET ; ALL DONE
1707      4058 C
1708      4059 C S21 ENDP
1709      4060 C
1710      4061 C S26 PROC NEAR
1711      4062 C MOV AX,CURSOR_POSN ; GET CURRENT CURSOR
1712      4063 C GRAPH_POSN LABEL NEAR ;
1713      4064 C PUSH BX ; SAVE REGISTER
1714      4065 C MOV BX,AX ; SAVE A COPY OF CURSOR
1715      4066 C MOV AL,AH ; GET ROWS TO AL
1716      4067 C MUL BYTE PTR CRT_COLS ; MULTIPLY BY BYTES/COLUMN
1717      4068 C SHL AX,1 ; *4 SINCE 4 ROWS/BYTE
1718      4069 C SHL AX,1 ;
1719      4070 C SUB BH,BH ; ISOLATE COLUMN VALUE
1720      4071 C ADD AX,BX ; DETERMINE OFFSET
1721      4072 C POP BX ; RECOVER POINTER
1722      4073 C RET ; ALL DONE
1723      4074 C
1724      4075 C S26 ENDP
1725      4076 C
1726      4077 C ; GR_CUR :
1727      4078 C ; ENTRY :
1728      4079 C ; BH = DISPLAY PAGE :
1729      4080 C ; EXIT :
1730      4081 C ; AX = CURSOR POSITION FOR REQUESTED PAGE :
1731      4082 C ;
1732      4083 C GR_CUR:
1733      4084 C ASSUME DS:ABS0 ;
1734      4085 C PUSH BX ; SAVE REGISTER
1735      4086 C MOV BL,BH ; GET TO LOW BYTE
1736      4087 C SUB BH,BH ; ZERO HIGH BYTE
1737      4088 C SAL AX,1 ; *2 FOR WORD COUNT
1738      4089 C MOV AX,[BX + OFFSET CURSOR_POSN] ; CURSOR, REQUESTED PAGE
1739      4090 C POP BX ; RECOVER REGISTER
1740      4091 C
1741      4092 C ; GRX_PSN :
1742      4093 C ; ENTRY :
1743      4094 C ; AX = CURSOR POSITION IN DESIRED PAGE :
1744      4095 C ; BH = DESIRED PAGE :
1745      4096 C ; EXIT :
1746      4097 C ; AX = BYTE OFFSET INTO REGEN :
1747      4098 C ;
1748      4099 C GRX_PSN PROC NEAR
1749      4100 C PUSH BX ; SAVE
1750      4101 C PUSH CX ; SAVE
1751      4102 C PUSH DX ; SAVE
1752      4103 C SUB CH,CH ; ZERO
1753      4104 C MOV CL,BH ; PAGE NUMBER
1754      4105 C MOV BX,AX ; ROW, COLUMN
1755      4106 C MOV AL,AH ; ROW
1756      4107 C MUL BYTE PTR CRT_COLS ; ROW * COLUMNS/ROW
1757      4108 C MUL POINTS ; BYTES PER ROW
1758      4109 C SUB BH,BH ; ZERO TO LEAVE COL VALUE
1759      4110 C ADD AX,BX ; ADD IN COLUMN
1760      4111 C MOV BX,CRT_LEN ; PAGE LENGTH
1761      4112 C JCXZ GP_2 ; NO PAGE OFFSET
1762      4113 C
1763      4114 C GP_3: ADD AX,BX ; ADD IN THE PAGE LENGTH
1764      4115 C LOOP GP_3 ; DO FOR NUMBER OF PAGES
1765      4116 C
1766      4117 C GP_2: POP DX ; RECOVER
1767      4118 C POP CX ; RECOVER
1768      4119 C POP BX ; RECOVER
1769      4120 C RET
1770      4121 C GRX_PSN ENDP
1771      4122 C
1772      4123 C MK_ES:
1773      4124 C MOV SI,0B800H
1774      4125 C MOV DI,EQUIP_FLAG
1775      4126 C AND D1,0300H
1776      4127 C CMP D1,0300H
1777      4128 C JNE P6_A
1778      4129 C MOV SI,0B000H
1779      4130 C P6_A:
1780      4131 C MOV ES,SI
1781      4132 C RET
1782      4133 C
1783      4134 C ; READ_AC_CURRENT :
1784      4135 C ; THIS ROUTINE READS THE ATTRIBUTE AND CHARACTER :
1785      4136 C ; AT THE CURRENT CURSOR POSITION AND RETURNS THEM :
1786      4137 C ; TO THE CALLER :
1787      4138 C ;
1788      4139 C ; INPUT :
1789      4140 C ; (AH) = CURRENT CRT MODE
1790      4141 C ; (BH) = DISPLAY PAGE ( ALPHA MODES ONLY )
1791      4142 C ; (DS) = DATA SEGMENT
1792      4143 C ; (ES) = REGEN SEGMENT
1793      4144 C ;
1794      4145 C ; OUTPUT :
1795      4146 C ; (AL) = CHAR READ
1796      4147 C ; (AH) = ATTRIBUTE READ
1797      4148 C ;
1798      4149 C ;
1799      4149 C READ_AC_CURRENT PROC NEAR
1800      4150 C CALL MK_ES
1801      4151 C CALL FIRD_POSITION
1802      4152 C MOV SI,BX ; ADDRESSING IN SI
1803      4153 C
1804      4154 C MOV DX,ADDR_6845 ; GET BASE ADDRESS
1805      4155 C ADD DX,6 ; POINT AT STATUS PORT
1806      4156 C
1807      4157 C TEST INFO,4
1808      4158 C

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1715 06          4159      C      PUSH   ES
1716 1F          4160      C      POP    DS                ; SEGMENT FOR QUICK ACCESS
                                C      JZ     P3A
1717 74 0B      4162      C      ;-----WAIT FOR HORIZONTAL RETRACE
                                C      P2:   IN     AL,DX                ; WAIT FOR RETRACE LOW
                                C      TEST  AL,1                ; GET STATUS
                                C      JNZ   P2                    ; IS HORIZ RETRACE LOW
                                C      CLI     P2                    ; WAIT UNTIL IT IS
                                C      ; NO MORE INTERRUPTS
                                C      P3:   IN     AL,DX                ; WAIT FOR RETRACE HIGH
                                C      TEST  AL,1                ; GET STATUS
                                C      JZ     P3                    ; IS IT HIGH
                                C      ; WAIT UNTIL IT IS
1718          4174      C      P3A:  LODSW  V_RET                ; GET THE CHAR/ATTR
1719          4177      C      READ_AC_CURRENT ENDP
1720          4178
1721          4179
1722          4180
1723          4181      C      ;-----MED_READ_BYTE
1724          4182      C      THIS ROUTINE WILL TAKE 2 BYTES FROM THE REGEN
1725          4183      C      BUFFER, COMPARE AGAINST THE CURRENT FOREGROUND
1726          4184      C      COLOR, AND PLACE THE CORRESPONDING ON/OFF BIT
1727          4185      C      PATTERN INTO THE CURRENT POSITION IN THE SAVE
1728          4186      C      AREA
1729          4187
1730          4188      C      ENTRY  SI,DS = POINTER TO REGEN AREA OF INTEREST
1731          4189      C      BX = EXPANDED FOREGROUND COLOR
1732          4190      C      BP = POINTER TO SAVE AREA
1733          4191      C      ; EXIT
1734          4192      C      BP IS INCREMENT AFTER SAVE
1735          4193      C      ;-----
1736          4194      C      S23:  PROC   NEAR
1737          4195      C      MOV   AH,[SI]                ; GET FIRST BYTE
1738          4196      C      MOV   AL,[SI+1]            ; GET SECOND BYTE
1739          4197      C      MOV   CX,0C0000H        ; 2 BIT MASK TO TEST
1740          4198      C      ; THE ENTRIES
1741          4199      C      ; RESULT REGISTER
1742          4200      C      S24:  MOV   DL,0
1743          4201      C      TEST  AX,CX                ; IS THIS BACKGROUND?
1744          4202      C      CLC
1745          4203      C      ; CLEAR CARRY IN HOPES
1746          4204      C      JZ     S25                ; THAT IT IS
1747          4205      C      STC                    ; IF 0, IT IS BACKGROUND
1748          4206      C      ; WASN'T, SO SET CARRY
1749          4207      C      S25:  RCL   DL,1                ; MOVE THAT BIT INTO THE
1750          4208      C      SHR   CX,1                ; RESULT
1751          4209      C      SHR   CX,1                ; MOVE THE MASK TO THE
1752          4210      C      ; RIGHT BY 2 BITS
1753          4211      C      JNC   S24                ; DO IT AGAIN IF MASK
1754          4212      C      ; DIDN'T FALL OUT
1755          4213      C      MOV   [BP],DL            ; STORE RESULT IN SAVE
1756          4214      C      INC   BP                    ; ADJUST POINTER
1757          4215      C      RET
1758          4216      C      S23:  ENDP
1759          4217
1760          4218
1761          4219      C      GRAPHICS_READ PROC NEAR
1762          4220      C      CALL  MK_ES
1763          4221      C      CALL  S26                ; CONVERTED TO OFFSET
1764          4222      C      MOV   SI,AX                ; SAVE IN SI
1765          4223      C      SUB   SP,8                ; ALLOCATE SPACE TO SAVE
1766          4224      C      ; THE READ CODE POINT
1767          4225      C      MOV   BP,SP                ; POINTER TO SAVE AREA
1768          4226
1769          4227      C      ;----- DETERMINE GRAPHICS MODES
1770          4228
1771          4229      C      CMP   CRT_MODE,6
1772          4230      C      PUSH  ES
1773          4231      C      POP   DS
1774          4232      C      JC    S13P                ; POINT TO REGEN SEGMENT
1775          4233      C      ; MEDIUM RESOLUTION
1776          4234
1777          4235      C      ;----- HIGH RESOLUTION READ
1778          4236
1779          4237      C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1780          4238
1781          4239      C      S12P:  MOV   DH,4                ; NUMBER OF PASSES
1782          4240      C      MOV   AL,[SI]                ; GET FIRST BYTE
1783          4241      C      MOV   [BP],AL            ; SAVE IN STORAGE AREA
1784          4242      C      INC   BP                    ; NEXT LOCATION
1785          4243      C      MOV   AL,[SI+2000H]        ; GET LOWER REGION BYTE
1786          4244      C      MOV   [BP],AL            ; ADJUST AND STORE
1787          4245      C      INC   BP
1788          4246      C      ADD   SI,80                ; POINTER INTO REGEN
1789          4247      C      DEC   DH                    ; LOOP CONTROL
1790          4248      C      JNZ   S12P                ; DO IT SOME MORE
1791          4249      C      JMP   S15P                ; GO MATCH THE SAVED CODE
1792          4250      C      ; POINTS
1793          4251
1794          4252      C      ;----- MEDIUM RESOLUTION READ
1795          4253
1796          4254      C      S13P:  SAL   SI,1                ; MED RES READ
1797          4255      C      MOV   DH,4                ; OFFSET#2, 2 BYTES/CHAR
1798          4256      C      ; NUMBER OF PASSES
1799          4257
1800          4258      C      S14P:  CALL  S23                ; GET PAIR BYTES
1801          4259      C      ; INTO SINGLE SAVE
1802          4260      C      ADD   SI,2000H            ; GO TO LOWER REGION
1803          4261      C      CALL  S23                ; GET THIS PAIR INTO SAVE
1804          4262      C      SUB   SI,2000H-80          ; ADJUST POINTER BACK INTO
1805          4263      C      DEC   DH                    ; UPPER
1806          4264      C      JNZ   S14P                ; KEEP GOING UNTIL 8 DONE
1807          4265
1808          4266      C      ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
1809          4267
1810          4268      C      S15P:
1811          4269      C      PUSH  DS                ; FIND_CHAR
1812          4270      C      CALL  DDS
1813          4271      C      LES   DI,GRX_SET
1814          4272      C      POP   DS
1815          4273      C      SUB   BP,8                ; ESTABLISH ADDRESSING
1816          4274
1817          4275      C      MOV   SI,BP                ; ADJUST POINTER TO
1818          4276      C      CLD                    ; BEGINNING OF SAVE AREA
1819          4277      C      MOV   AL,0                ; ENSURE DIRECTION
1820          4278      C      S16P:
1821          4279      C      PUSH  SS                ; CURRENT CODE POINT BEING
1822          4280      C      POP   DS                ; MATCHED
1823          4281      C      MOV   SS                ; ADDRESSING TO STACK
1824          4282      C      POP   DS                ; FOR THE STRING COMPARE
1825          4283      C      MOV   DX,128             ; NUMBER TO TEST AGAINST
1826          4284      C      S17P:  PUSH  SI
1827          4285      C      PUSH  DI
1828          4286      C      ; SAVE SAVE AREA POINTER
1829          4287      C      ; SAVE CODE POINTER

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17A3 B9 0008      4285      MOV     CX,8           ; NUMBER OF BYTES TO MATCH
17A6 F3 / A6      4286      REPE   D              ; COMPARE THE 8 BYTES
17A8 5F           4287      C      D              ; RECOVER THE POINTERS
17A9 5E           4288      POP    SI              ;
17AA 74 1D        4289      JZ     S18P            ; IF ZERO FLAG SET,
17AC FE C0        4290      C      C              ; THE MATCH OCCURRED
17AE 83 C7 08     4291      INC    AL              ; NO MATCH, MOVE TO NEXT
17B1 4A           4292      ADD    DI,8           ; NEXT CODE POINT
17B2 75 ED        4293      DEC    DX              ; LOOP CONTROL
17B3             4294      JNZ   S17P            ; DO ALL OF THEM
17B4             4295      C      C              ;
17B4             4296      C ;---- CHAR NOT MATCHED, MIGHT BE IN USER SUPPLIED SECOND HALF
17B4 3C 00        4297      CMP    AL,0           ; AL <= 0 IF ONLY 1ST
17B6 74 11        4299      JBE   S18P            ; HALF SCANNED
17B7             4300      C ; IF = 0, THEN ALL HAS
17B7             4301      C ; BEEN SCANNED
17B8 E8 0CFE R    4302      ASSUME DS:ABS0
17BB C4 3E 007C R 4303      CALL  DDIS            ;
17BF 8C C0        4304      LES    DI,EXT_PTR     ; GET POINTER
17C1 0B C7        4305      MOV    AX,ES          ; SEE IF THE PNTR EXISTS
17C3 74 04        4306      OR     AX,DI          ; IF ALL 0, DOESN'T EXIST
17C5 B0 80        4307      JZ     S18P            ; NO SENSE LOOKING
17C7 EB D3        4308      MOV    AL,128         ; ORIGIN FOR SECOND HALF
17C8             4309      JMP    S16P           ; GO BACK AND TRY FOR IT
17C9             4310      C ;
17C9             4311      C ;---- CHARACTER IS FOUND ( AL=0 IF NOT FOUND )
17C9             4312      C ;
17C9 83 C4 08     4313      S18P:  ADD    SP,8           ; READJUST THE STACK,
17CC E9 219E R    4314      C ; THROW AWAY SAVE
17CF             4315      C ; ALL DONE
17CF             4316      C ;
17CF             4317      C GRAPHICS_READ      ENDP
17CF             4318      C ;
17CF             4319      C ;
17CF             4320      C ;---- READ CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
17CF             4321      C ;
17CF E9 1701 R    4322      C ;
17CF             4323      C ;
17CF             4324      C ;
17D2             4325      C ;
17D2             4326      C ;
17D2 8A 26 0449 R 4327      C ;
17D6 80 FC 07     4328      C ;
17D9 74 F4        4329      C ;
17DB 80 FC 03     4330      C ;
17DE 76 EF        4331      C ;
17E0 80 FC 06     4332      C ;
17E3 77 03       4333      C ;
17E5 E9 1745 R    4334      C ;
17E8             4335      C ;
17E8 80 FC 0F     4336      C ;
17EB 72 52       4337      C ;
17ED EB 14F7 R    4338      C ;
17F0 72 4D        4339      C ;
17F2 EB 0A        4340      C ;
17F4 80 FC 0D     4341      C ;
17F7 73 46       4342      C ;
17F9 B0 00        4343      C ;
17FB E9 219E R    4344      C ;
17FC             4345      C ;
17FE             4346      C ;
17FE             4347      C ;
17FE             4348      C ;
17FE BA A000     4349      C ;
1801 8C C2        4350      C ;
1804 EB 168A R    4351      C ;
1806 8B F0        4352      C ;
1808 8B 1E 0485 R 4353      C ;
180C 2B E3        4354      C ;
180E 2B EC        4355      C ;
180F             4356      C ;
180F             4357      C ;
180F             4358      C ;
1810             4359      C ;
1810 53           4360      C ;
1811 24 01        4361      C ;
1813 BA C8        4362      C ;
1815 B0 05        4363      C ;
1817 D2 E0        4364      C ;
1819 B4 07        4365      C ;
181B B6 03        4366      C ;
181D B2 CE        4367      C ;
181F E8 0D15 R   4368      C ;
1822 B6 0518     4369      C ;
1825 E8 0D15 R   4370      C ;
1828             4371      C ;
1828 26; 8A 04    4372      C ;
182B F6 D0        4373      C ;
182D 88 46 00     4374      C ;
1830 45           4375      C ;
1831 03 36 044A R 4376      C ;
1835 48           4377      C ;
1836 75 F0        4378      C ;
1838 5B           4379      C ;
1839 B8 0510     4380      C ;
183C EB 32 90     4381      C ;
183F             4382      C ;
183F             4383      C ;
183F             4384      C ;
183F BA A000     4385      C ;
1842 8C C2        4386      C ;
1844 EB 168A R    4387      C ;
1847 8B F0        4388      C ;
1849 8B 1E 0485 R 4389      C ;
184D 2B E3        4390      C ;
184F             4391      C ;
184F             4392      C ;
184F             4393      C ;
184F             4394      C ;
184F             4395      C ;
184F             4396      C ;
184F             4397      C ;
1851 B6 03        4398      C ;
1853 B2 CE        4399      C ;
1855 B8 0508     4400      C ;
1858 E8 0D15 R   4401      C ;
185B 53           4402      C ;
185C             4403      C ;
185C 26; 8A 04    4404      C ;
185F F6 D0        4405      C ;
1861 88 46 00     4406      C ;
1864 45           4407      C ;
1865 03 36 044A R 4408      C ;
1869 48           4409      C ;
186A 75 F0        4410      C ;
186B             4411      C ;
186B             4412      C ;
186B             4413      C ;
186B             4414      C ;
186B             4415      C ;
186B             4416      C ;
186B             4417      C ;
186B             4418      C ;
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186B             4643      C ;
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186B             4654      C ;
186B             4655      C ;
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186B             4664      C ;
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186B             4666      C ;
186B             4667      C ;
186B             4668      C ;
186B             4669      C ;
186B             4670      C ;
186B             4671      C ;
186B             4672      C ;
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186B             4674      C ;
186B             4675      C ;
186B             4676      C ;
186B             4677      C ;
186B             4678      C ;
186B             4679      C ;
186B             4680      C ;
186B             4681      C ;
186B             4682      C ;
186B             4683      C ;
186B             4684      C ;
186B             4685      C ;
186B             4686      C ;
186B             4687      C ;
186B             4688      C ;
186B             4689      C ;
186B             4690      C ;
186B             4691      C ;
186B             4692      C ;
186B             4693      C ;
186B             4694      C ;
186B             4695      C ;
186B             4696      C ;
186B             4697      C ;
186B             4698      C ;
186B             4699      C ;
186B             4700      C ;
186B             4701      C ;
186B             4702      C ;
186B             4703      C ;
186B             4704      C ;
186B             4705      C ;
186B             4706      C ;
186B             4707      C ;
186B             4708      C ;
186B             4709      C ;
186B             4710      C ;
186B             4711      C ;
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186B             4714      C ;
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186B             4716      C ;
186B             4717      C ;
186B             4718      C ;
186B             4719      C ;
186B             4720      C ;
186B             4721      C ;
186B             4722      C ;
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186B             4746      C ;
186B             4747      C ;
186B             4748      C ;
186B             4749      C ;
186B             4750      C ;
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186B             4752      C ;
186B             4753      C ;
186B             4754      C ;
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186B             4756      C ;
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186B             4758      C ;
186B             4759      C ;
186B             4760      C ;
186B             4761      C ;
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186B             4764      C ;
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186B             4766      C ;
186B             4767      C ;
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186B             4769      C ;
186B             4770      C ;
186B             4771      C ;
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186B             4774      C ;
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186B             4776      C ;
186B             4777      C ;
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186B             4779      C ;
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186B             4789      C ;
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186B             4791      C ;
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186B             4794      C ;
186B             4795      C ;
186B             4796      C ;
186B             4797      C ;
186B             4798      C ;
186B             4799      C ;
186B             4800      C ;
186B             4801      C ;
186B             4802      C ;
186B             4803      C ;
186B             4804      C ;
186B             4805      C ;
186B             4806      C ;
186B             4807      C ;
186B             4808      C ;
186B             4809      C ;
186B             4810      C ;
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186B             4816      C ;
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186B             4849      C ;
186B             4850      C ;
186B             4851      C ;
186B             4852      C ;
186B             4853      C ;
186B             4854      C ;
186B             4855      C ;
186B             4856      C ;
186B             4857      C ;
186B             4858      C ;
186B             4859      C ;
186B             4860      C ;
186B             4861      C ;
186B             4862      C ;
186B             4863      C ;
186B             4864      C ;
186B             4865      C ;
186B             4866      C ;
186B             4867      C ;
186B             4868      C ;
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186B             4870      C ;
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186B             4873      C ;
186B             4874      C ;
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186B             4876      C ;
186B             4877      C ;
186B             4878      C ;
186B             4879      C ;
186B             4880      C ;
186B             4881      C ;
186B             4882      C ;
186B             4883      C ;
186B             4884      C ;
186B             4885      C ;
186B             4886      C ;
186B             4887      C ;
186B             4888      C ;
186B             4889      C ;
186B             4890      C ;
186B             4891      C ;
186B             4892      C ;
186B             4893      C ;
186B             4894      C ;
186B             4895      C ;
186B             4896      C ;
186B             4897      C ;
186B             4898      C ;
186B             4899      C ;
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186B             4903      C ;
186B             4904      C ;
186B             4905      C ;
186B             4906      C ;
186B             4907      C ;
186B             4908      C ;
186B             4909      C ;
186B             4910      C ;
186B             4911      C ;
186B             4912      C ;
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186B             4915      C ;
186B             4916      C ;
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186B             4918      C ;
186B             4919      C ;
186B             4920      C ;
186B             4921      C ;
186B             4922      C ;
186B             4923      C ;
186B             4924      C ;
186B             4925      C ;
186B             4926      C ;
186B             4927      C ;
186B             4928      C ;
186B             4929      C ;
186B             4930      C ;
186B             4931      C ;
186B             4932      C ;
186B             4933      C ;
186B             4934      C ;
186B             4935      C ;
186B             4936      C ;
186B             4937      C ;
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186B             4950      C ;
186B             4951      C ;
186B             4952      C ;
186B             4953      C ;
186B             4954      C ;
186B             4955      C ;
186B             4956      C ;
186B             4957      C ;
186B             4958      C ;
186B             4959      C ;
186B             4960      C ;
186B             4961      C ;
186B             4962      C ;
186B             4963      C ;
186B             4964      C ;
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186B             4966      C ;
186B             4967      C ;
186B             4968      C ;
186B             4969      C ;
186B             4970      C ;
186B             4971      C ;
186B             4972      C ;
186B             4973      C ;
186B             4974      C ;
186B             4975      C ;
186B             4976      C ;
186B             4977      C ;
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186B             4980      C ;
186B             4981      C ;
186B             4982      C ;
186B             4983      C ;
186B             4984      C ;
186B             4985      C ;
186B             4986      C ;
186B             4987      C ;
186B             4988      C ;
186B             4989      C ;
186B             4990      C ;
186B             4991      C ;
186B             4992      C ;
186B             4993      C ;
186B             4994      C ;
186B             4995      C ;
186B             4996      C ;
186B             4997      C ;
186B             4998      C ;
186B             4999      C ;
186B             5000      C ;

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186C 5B          4411 C POP BX ; RECOVER BYTES PER CHAR
186D B8 0500    4412 C MOV AX,500H ; UNDO READ MODE
1870            4413 C GRX_RD2 ENDP
1870            4414 C GRX_REC2:
1870            4415 C
1870            4416 C ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
1870            4417 C
1870            4418 C
1870            4419 C CALL OUT_DX ; SET READ MODE BACK
1873 C4 3E 010C R 4420 C LES DI,GRX_SET ; GET FONT DEFINITIONS
1877 2B EB       4421 C SUB BP,BX ; ADJUST POINTER TO
1877            4422 C ; BEGINNING OF SAVE AREA
1879 8B F5       4423 C MOV SI,BP ; ENSURE DIRECTION
187B FC         4424 C CLD ; CODE POINT BEING MATCHED
187C 8D 0D       4425 C MOV AL,0 ; ADDRESSING TO STACK
187E 16         4426 C PUSH SS ; FOR THE STRING COMPARE
187F 1F         4427 C POP DS ; NUMBER TO TEST AGAINST
1880 BA 0100     4428 C MOV DX,256D
1883            4429 C
1883 56          4430 C PUSH SI ; SAVE SAVE AREA POINTER
1884 57          4431 C PUSH DI ; SAVE CODE POINTER
1885 8B CB       4432 C MOV CX,BX ; NUMBER OF BYTES TO MATCH
1887 F3/ A6     4433 C REPE CMPSB ; COMPARE THE 8 BYTES
1889 5F         4434 C POP DI ; RECOVER THE POINTERS
188A 5E         4435 C POP SI
188B 74 07       4436 C JZ S18_5 ; IF ZFL SET, THEN MATCH
188D FE C0       4437 C INC AL ; OCCURRED
188F 03 FB       4438 C ADD DI,BX ; NO MATCH ON TO NEXT
1891 4A         4439 C DEC DX ; NEXT CODE POINT
1892 75 EF       4440 C JNZ S17_5 ; LOOP CONTROL
1894            4441 C ; DO ALL OF THEM
1894 03 E3       4442 C ; AL=CHAR, 0 IF NOT FOUND
1896 E9 219E R   4443 C ADD SP,BX ; READJUST THE STACK
1896            4444 C JMP V_RET
1896            4445 C
1896            4446 C ;----- WRITE CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
1896            4447 C
1896            4448 C
1896            4449 C ;----- WRITE_AC_CURRENT
1896            4450 C ; THIS ROUTINE WRITES THE ATTRIBUTE
1896            4451 C ; AND CHARACTER AT THE CURRENT CURSOR
1896            4452 C ; POSITION
1896            4453 C ; INPUT:
1896            4454 C ; (AH) = CURRENT CRT MODE
1896            4455 C ; (BH) = DISPLAY PAGE
1896            4456 C ; (CX) = COUNT OF CHARACTERS TO WRITE
1896            4457 C ; (AL) = CHAR TO WRITE
1896            4458 C ; (BL) = ATTRIBUTE OF CHAR TO WRITE
1896            4459 C ; (DS) = DATA SEGMENT
1896            4460 C ; (ES) = REGEN SEGMENT
1896            4461 C ; OUTPUT
1896            4462 C ; NONE
1896            4463 C
1896            4464 C ;----- AH9:
1896            4465 C ASSUME DS:ABS0
1899 E8 0CFE R    4466 C CALL DIS
189C 8A 26 0449 R 4467 C MOV AH,CRT_MODE
189C            4468 C
189C            4469 C
18A0 80 FC 04    4470 C CMP AH,4 ; IS THIS GRAPHICS
18A3 72 08       4471 C JC P6 ;
18A5 80 FC 07    4472 C CMP AH,7 ; IS THIS BW CARD
18A8 74 03       4473 C JE P6 ;
18AA EB 74 90    4474 C JMP GRAPHICS_WRITE
18AA            4475 C
18AA            4476 C ;----- P6:
18AA            4477 C CALL MK_ES ; WRITE_AC_CONTINUE
18AD E8 16EB R   4478 C MOV AH,BL ; GET ATTRIBUTE TO AH
18AD            4479 C PUSH AX ; SAVE ON STACK
18AD            4480 C PUSH CX ; SAVE WRITE COUNT
18AD            4481 C CALL FIND_POSITION ;
18AD            4482 C MOV DI,BX ; ADDRESS TO DI REGISTER
18AD            4483 C POP CX ; WRITE COUNT
18AD            4484 C POP BX ; CHARACTER IN BX REG
18AD            4485 C MOV DX,ADDR_6845 ; GET BASE ADDRESS
18AD            4486 C ADD DX,6 ; POINT AT STATUS PORT
18AD            4487 C
18AD            4488 C ;----- WAIT FOR HORIZONTAL RETRACE
18AD            4489 C
18AD            4490 C
18AD            4491 C ;----- P7:
18AD            4492 C TEST INFO,4
18AD            4493 C JZ P9A
18AD            4494 C
18AD            4495 C ;----- P8:
18AD            4496 C IN AL,DX ; GET STATUS
18AD            4497 C TEST AL,1 ; IS IT LOW
18AD            4498 C JNZ P8 ; WAIT UNTIL IT IS
18AD            4499 C CLI ; NO MORE INTERRUPTS
18AD            4500 C
18AD            4501 C ;----- P9:
18AD            4502 C IN AL,DX ; GET STATUS
18AD            4503 C TEST AL,1 ; IS IT HIGH
18AD            4504 C JZ P9 ; WAIT UNTIL IT IS
18AD            4505 C
18AD            4506 C ;----- P9A:
18AD            4507 C MOV AX,BX ; RECOVER THE CHAR/ATTR
18AD            4508 C STOSW ; PUT THE CHAR/ATTR
18AD            4509 C STI ; INTERRUPTS BACK ON
18AD            4510 C LOOP P7 ; AS MANY TIMES
18AD            4511 C JMP V_RET
18AD            4512 C
18AD            4513 C ;----- WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION
18AD            4514 C
18AD            4515 C
18AD            4516 C ;----- WRITE_C_CURRENT
18AD            4517 C ; THIS ROUTINE WRITES THE CHARACTER AT
18AD            4518 C ; THE CURRENT CURSOR POSITION, ATTRIBUTE
18AD            4519 C ; UNCHANGED
18AD            4520 C ; INPUT
18AD            4521 C ; (AH) = CURRENT CRT MODE
18AD            4522 C ; (BH) = DISPLAY PAGE
18AD            4523 C ; (CX) = COUNT OF CHARACTERS TO WRITE
18AD            4524 C ; (AL) = CHAR TO WRITE
18AD            4525 C ; (DS) = DATA SEGMENT
18AD            4526 C ; (ES) = REGEN SEGMENT
18AD            4527 C ; OUTPUT
18AD            4528 C ; NONE
18AD            4529 C
18AD            4530 C ;----- AH4:
18AD            4531 C ASSUME DS:ABS0
18DD E8 0CFE R    4532 C CALL DIS
18DD 8A 26 0449 R 4533 C MOV AH,CRT_MODE
18DD            4534 C
18DD            4535 C
18E4 80 FC 04    4536 C CMP AH,4 ; IS THIS GRAPHICS
18E7 72 08       4537 C JC P10 ;
18E9 80 FC 07    4538 C CMP AH,7 ; IS THIS BW CARD
18EC 74 03       4539 C JE P10 ;
18EE EB 30 90    4540 C JMP GRAPHICS_WRITE
18EE            4541 C
18F1 E8 16EB R   4542 C CALL MK_ES
18F1            4543 C
18F1            4544 C
18F1            4545 C ;----- P10:
18F1            4546 C CALL MK_ES

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1971 5F          4663 C POP D1 ; RECOVER REGEN POINTER
1972 47          4664 C INC D1 ; POINT TO NEXT CHAR POS
1973 E2 E3      4665 C LOOP S3 ; MORE CHAR S TO WRITE
1975 E9 219E R 4666 C JMP V_RET
1978           4667 C
1978 26: 32 05  4668 C XOR STOSB AL,ES:[DI]
1978 AA          4669 C LODSB ; STORE THE CODE POINT
197C AC          4670 C ; AGAIN FOR ODD FIELD
197D 26: 32 05 1FFF 4671 C XOR AL,ES:[DI+2000H+1]
1982 EB E0      4672 C JMP S5 ; BACK TO MAINSTREAM
4673 C
4674 C ;----- MEDIUM RESOLUTION WRITE
4675 C
4676 C
1984           4677 C MOV DL,BL ; MED_RES_WRITE
1984 BA D3      4677 C SAL DI,1 ; SAVE HIGH COLOR BIT
1986 D1 E7      4678 C CALL S19 ; OFFSET*2, 2 BYTES/CHAR
1988 E8 166D R  4680 C ; EXPAND BL TO FULL WORD
1988           4681 C ; OF COLOR
1988 57          4681 C PUSH D1 ; SAVE REGEN POINTER
198C 56          4682 C POP SI ; SET THE CODE POINTER
198D B6 04      4683 C MOV DH,4 ; NUMBER OF LOOPS
198F AC          4684 C LODSB ; GET CODE POINT
1990 E8 1682 R  4686 C CALL S21 ; DOUBLE UP ALL THE BITS
1993 23 C3      4687 C AND AX,BX ; CONVERT THEM TO FORE-
; GROUND COLOR (0 BACK)
1995 F6 C2 80  4689 C TEST DL,80H ; IS THIS XOR FUNCTION
1998 74 07      4690 C JZ S10 ; NO, STORE IT IN AS IT IS
199A 26: 32 25  4691 C XOR AH,ES:[DI] ; DO FUNCTION WITH HALF
199D 26: 32 45 01 4692 C XOR AL,ES:[DI+1] ; AND WITH OTHER HALF
19A1           4693 C
19A1 26: 88 25  4694 C MOV ES:[DI+1],AL ; STORE FIRST BYTE
19A4 26: 88 45 01 4695 C MOV ES:[DI],AL ; STORE SECOND BYTE
19A8 AC          4696 C LODSB ; GET CODE POINT
19A9 E8 1682 R  4697 C CALL S21 ; CONVERT TO COLOR
19AC 23 C3      4698 C AND AX,BX ; IS THIS XOR FUNCTION
19AE F6 C2 80  4699 C TEST DL,80H ; NO, JUST STORE THE VALUE
19B1 74 0A      4700 C JZ S11 ; FUNCTION WITH FIRST HALF
19B3 26: 32 A5 2000 4701 C XOR AH,ES:[DI+2000H] ; AND WITH SECOND HALF
19B8 26: 32 85 2001 4702 C XOR AL,ES:[DI+2001H]
19BD           4703 C
19BD 26: 88 A5 2000 4704 C MOV ES:[DI+2000H],AH ; STORE IN SECOND PORTION
19C2 26: 88 85 2001 4705 C MOV ES:[DI+2000H+1],AL ; POINT TO NEXT LOCATION
19C7 83 C7 50  4706 C ADD DI,80
19C9 FE CE      4707 C DH ; KEEP GOING
19CC 75 C1      4708 C JNZ S9 ; RECOVER CODE POINTER
19CE 5E          4709 C POP SI ; RECOVER REGEN POINTER
19C7 5F          4710 C POP DI ; POINT TO NEXT CHAR
19D0 47          4711 C INC D1 ; POINT TO NEXT CHAR
19D1 47          4712 C INC DI ; MORE TO WRITE
19D2 E2 B7      4713 C LOOP S8
19D4 E9 219E R  4714 C JMP V_RET
19D7           4715 C GRAPHICS_WRITE ENDP
4716 C
4717 C
4718 C
4719 C
4720 C
4721 C
4722 C
4723 C
4724 C
4725 C
4726 C
4727 C
4728 C
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4788 C
;-----
; ENTRY
; AL = CHAR TO WRITE
; BH = DISPLAY PAGE
; BL = ATTRIBUTE/COLOR
; CX = COUNT OF CHAR S TO WRITE
;-----
19D7 GRX_WRT PROC NEAR
ASSUME DS:ABSO, ES:NOTHING
CMP AH,0FH
JB NO_ADJ1
CALL MEM_DET
NO_ADJ1
AND BL,10000101B
MOV AH,BL
SHL AH,1
OR BL,AH
NO_ADJ1:
SUB AH,AH
MUL POINTS
PUSH AX
CALL GR_CUR
DI,AX
MOV BP,POINTS
SRLOAD ES,DA000H
MOV DX,DA000H
MOV ES,DX
LDS SI,GRX_SET
POP AX
ADD SI,AX
MOV DH,3
S20A:
TEST BL,080H
JZ NO_XOR
MOV DL,GRAPH_ADDR
MOV AX,0318H
CALL OUT_DX
JMP F_2
NO_XOR:
PUSH DI
DL,SEQ_ADDR
MOV AX,020FH
CALL OUT_DX
SUB AX,AX
PUSH CX
MOV CX,BP
PUSH DS
CALL DDS
S13A:
STOSB
ADD DI,CRT_COLS
DEC DI
LOOP S13A
DS
POP CX
POP DI
F_2:
MOV DL,SEQ_ADDR
MOV AH,02H
AL,BL
CALL OUT_DX
PUSH DI
PUSH BX
PUSH CX
MOV BX,BP
PUSH DS
CALL DDS
ASSUME DS:ABSO
MOV CX,CRT_COLS
POP DS
ASSUME DS:NOTHING
; 640X350 GRAPHICS
; BASE CARD
; B5H, XOR C2 C0 MASK
; EXPAND C0 TO C1, C2 TO C3
; BUILD ?(80H) + {0,3,C,F}
; ZERO
; OFFSET FONT TABLE BASE
; FONT TABLE DISPLACEMENT
; GET OFFSET INTO REGEN
; INTO DESTINATION
; BYTES PER CHAR
; REGEN SEGMENT
; ADDRESSING TO FONTS
; RECOVER OFFSET
; CHARACTER IN TABLE
; TEST FOR XOR
; NO XOR
; GRAPHICS CHIP XOR
; SET REGISTER
; SKIP BLANK
; BLANK BOX FOR CHAR
; SAVE REGEN POINTER
; ENABLE ALL MAPS
; STORE ZERO
; SAVE CHARACTER COUNT
; GET BYTE COUNT
; ZERO REGEN BYTE
; NEXT BYTE OF BOX
; ADJUST
; NEXT BYTE
; RECOVER CHARACTER COUNT
; RECOVER REGEN POINTER
; SET MAP MASK
; FOR COLOR
; SET THE CHIP
; SAVE OFFSET IN REGEN
; SAVE COLOR VALUE
; SAVE CHARACTER COUNT
; LOOP CONTROL, BYTES/CHAR
; SAVE FONT SEGMENT
; SET LOW RAM SEGMENT
; GET COLUMN COUNT
; SET LOW RAM SEGMENT
; RESTORE FONT SEGMENT
; WRITE OUT THE CHARACTER

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1A4C BA 04          4789      C      MOV     AL,DS:[SI]          ; CODE POINT
1A4E 26: BA 25      4790      C      MOV     AH,ES:[DI]       ; LATCH DATA
1A51 26: 88 05      4791      C      MOV     ES:[DI],AL      ; WRITE ONE BYTE OF FONT
1A54 46             4792      C      INC     SI                ; NEXT FONT POINT
1A55 03 F9          4793      C      ADD     DI,CX             ; ONE ROW BELOW LAST POINT
1A57 48             4794      C      DEC     BX                ; BYTES PER CHAR COUNTER
1A58 75 F2          4795      C      JNZ    S1K               ; DO NEXT ROW OF CHARACTER
1A5A 59             4796      C      POP     CX                ; CHARACTER COUNT
1A5B 58             4797      C      POP     BX                ; COLOR VALUE
1A5C 2B F5          4798      C      SUB     SI,BP            ; ADJUST PTR TO FONT TABLE
1A5E 5F             4800      C      POP     DI                ; REGEN POINTER
1A5F 47             4801      C      INC     DI                ; NEXT CHAR POSM IN REGEN
1A60 E2 A6          4802      C      LOOP   S20A             ; WRITE ANOTHER CHARACTER
1A62 B2 CE          4804      C      MOV     DL,GRAPH_ADDR    ;
1A63 B8 0300        4805      C      MOV     AX,0300H        ;
1A67 E8 0D15 R     4806      C      CALL   OUT_DX           ;
1A6A B8 C4          4807      C      MOV     DL,SEQ_ADDR     ;
1A6C B8 020F        4808      C      MOV     AX,020FH       ;
1A6F E8 0D15 R     4809      C      CALL   OUT_DX           ;
1A72 E9 219E R     4810      C      JMP     V_RET            ;
1A75                4811      C      GRX_WRT ENDP          ;
1A75                4812      C      SUBTTL
1A75                4813      C      ;----- SET COLOR PALETTE
1A75                4814      C      ;
1A75                4815      C      ;
1A75                4816      C      ;
1A75                4817      C      ;
1A75                4818      C      AHB:
1A75                4819      C      ASSUME DS:ABSO
1A7A 74 05          4820      C      CMP     BYTE PTR ADDR_6845,084H
1A7C F6 06 0487 R  4821      C      JE      M21_B           ; CALL VALID ONLY FOR COLOR
1A81 74 05          4822      C      TEST  JZ M21_A         ; SEE IF ITS THE OLD COLOR CARD
1A83 CD 42          4823      C      INT  42H             ; IF NOT, HANDLE IT HERE
1A85                4824      C      ; OLD CODE CALL
1A85 E9 219E R     4825      C      JMP     V_RET            ; BACK TO CALLER
1A88                4826      C      ;
1A88 2B C0          4827      C      M21_A: SUB AX,AX
1A8A B8 E8          4828      C      MOV     BP,AX
1A8C C4 3C 04A8 R  4829      C      LES   DI,SAVE_PTR
1A90 83 C7 04       4830      C      ADD   DI,4
1A93 26: C4 3D     4831      C      LES   DI,DWORD PTR ES:[DI]
1A96 8C C0          4832      C      MOV     AX,ES
1A98 0B C7          4833      C      OR    AX,DI
1A9A 74 01          4834      C      JZ     NOT4AHB
1A9C 45             4835      C      INC   BP
1A9D                4836      C      NOT4AHB:
1A9D E8 10C0 R     4837      C      CALL  PAL_INIT
1AA0 0A FF          4838      C      OR    BH,BH
1AA2 75 65          4839      C      JNZ   M20
1AA2                4840      C      ;
1AA2                4841      C      ;----- HANDLE BH = 0 HERE
1AA2                4842      C      ;
1AA2                4843      C      ; ALPHA MODES => BL = OVERSCAN COLOR
1AA2                4844      C      ; GRAPHICS => BL = OVERSCAN AND BACKGROUND COLOR
1AA2                4845      C      ;
1AA2                4846      C      ;----- MOVE INTENSITY BIT FROM D3 TO D4 FOR COMPATIBILITY
1AA4 BA FB          4847      C      MOV     BH,BL
1AA6 A0 0466 R     4848      C      MOV     AL,CRT_PALETTE
1AA7 24 E0          4849      C      AND   AL,08H
1AA8 80 E3 1F      4850      C      AND   BL,01FH
1AAE 0A C3          4851      C      OR    AL,BL
1AB0 A2 0466 R     4852      C      MOV     CRT_PALETTE,AL
1AB3 8A DF          4853      C      MOV     BL,BH
1AB5 80 E7 08      4854      C      AND   BH,08H
1AB8 00 E7          4855      C      SHL   BH,1
1ABA 8A E8          4856      C      MOV     CH,AL
1ABC 80 E5 EF      4857      C      AND   CH,0EFH
1ABF 0A ED          4858      C      OR    CH,CH
1AC1 80 E3 0F      4859      C      AND   BL,0FH
1AC4 8A FB          4860      C      MOV     BH,BL
1AC6 DD E3         4861      C      SHL   BL,1
1AC8 80 E3 10      4862      C      AND   BL,010H
1ACB 80 E7 07      4863      C      AND   BH,07H
1ACE 0A DF          4864      C      OR    BL,BH
1AD0 A0 0449 R     4865      C      MOV     AL,CRT_MODE
1AD3 3C 03          4866      C      CMP   AL,3
1AD5 76 DE          4867      C      JBE   M21
1AD5                4868      C      ;
1AD5                4869      C      ;
1AD5                4870      C      ;----- GRAPHICS MODE DONE HERE (SET PALETTE 0 AND OVERSCAN)
1AD7 B4 00          4871      C      MOV     AH,0
1AD9 BA C3          4872      C      MOV     AL,BL
1ADB E8 1D9F R     4873      C      CALL  PAL_SET
1AD9                4874      C      ;
1AD9                4875      C      ;
1ADE 0B ED          4876      C      OR    BP,BP
1AE0 74 03          4877      C      JZ     M21
1AE2 26: 88 1D     4878      C      MOV     ES:[DI],BL
1AE2                4879      C      ;
1AE2                4880      C      ;----- ALPHA MODE DONE HERE (SET OVERSCAN REGISTER)
1AE5                4881      C      ;
1AE5                4882      C      ;
1AE5                4883      C      M21:
1AE5                4884      C      CMP     CRT_MODE,3      ; CHECK FOR AN ENHANCED MODE
1AE5                4885      C      JA     SET_OVRSC       ; NO CHANCE
1AE5                4886      C      CALL  BRST_DET        ; SEE IF WE ARE ENHANCED
1AE5                4887      C      JC     SKIP_OVRSC     ; THERE IS NO BORDER
1AE5                4888      C      ;
1AE5                4889      C      MOV     AH,011H        ; OVERSCAN REGISTER
1AE5                4890      C      MOV     AL,BL
1AE5                4891      C      CALL  PAL_SET         ; SET THE BORDER
1AE5                4892      C      SKIP_OVRSC:
1AE5                4893      C      OR    BP,BP
1AE5                4894      C      JZ     M21Y
1AE5                4895      C      MOV     ES:[DI][16D],BL
1AE5                4896      C      M21Y:
1AE5                4897      C      MOV     BL,CH
1AE5                4898      C      AND   BL,020H
1AE5                4899      C      MOV     CL,5
1AE5                4900      C      SHR   BL,CL
1AE5                4901      C      ;
1AE5                4902      C      ;----- HANDLE BH = 1 HERE
1AE5                4903      C      ;
1AE5                4904      C      ; ALPHA MODES => NO EFFECT
1AE5                4905      C      ; GRAPHICS => LOW BIT OF BL = 0
1AE5                4906      C      ; PALETTE 0 = BACKGROUND
1AE5                4907      C      ; PALETTE 1 = GREEN
1AE5                4908      C      ; PALETTE 2 = RED
1AE5                4909      C      ; PALETTE 3 = BROWN
1AE5                4910      C      ; => LOW BIT OF BL = 1
1AE5                4911      C      ; PALETTE 0 = BACKGROUND
1AE5                4912      C      ; PALETTE 1 = CYAN
1AE5                4913      C      ; PALETTE 2 = MAGENTA
1AE5                4914      C      ; PALETTE 3 = WHITE

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1809          4915
1809 80 3E 0449 R 03 4916
180E 76 4A          4917
1810          4918
1810 A0 0466 R      4919
1813 24 DF          4920
1815 80 E3 01      4921
1818 74 02          4922
181A 0C 20          4923
181C          4924
181C A2 0466 R      4925
181F 24 10          4926
1821 0C 02          4927
1823 0A D6          4928
1825 84 01          4929
1827 8A C3          4930
1829 E8 1D9F R      4931
182C 08 ED          4932
182E 74 04          4933
1830 26: 88 5D 01  4935
1834          4936
1834          4937
1834 FE C3          4938
1836 FE C3          4939
1838 84 02          4940
183A 8A C3          4941
183C E8 1D9F R      4942
183F 08 ED          4944
1841 74 04          4945
1843 26: 88 5D 02  4946
1847          4947
1847          4948
1847 FE C3          4949
1849 FE C3          4950
184B 84 03          4951
184D 8A C3          4952
184F E8 1D9F R      4953
1852 08 ED          4954
1854 74 04          4955
1856 26: 88 5D 03  4956
185A          4957
185A E8 1D87 R      4958
185D E9 219E R      4959
185E          4960
185E          4961
185E          4962
1860          4963
1860          4964
1860          4965
1860          4966
1860          4967
1860          4968
1860          4969
1860          4970
1860          4971
1860          4972
1860          4973
1860          4974
1860          4975
1860          4976
1860          4977
1860          4978
1860          4979
1860          4980
1865 01 E9          4981
1867 01 E9          4982
1869 01 E9          4983
186A          4984
186B 03 C1          4985
186D 8A DF          4986
186F 2A FF          4987
1871 8B CB          4988
1873 8B 1E 044C R  4989
1877 E3 04          4990
1879          4991
1879 03 C3          4992
187B E2 FC          4993
187D          4994
187D 59          4995
187E 8B D8          4996
1880 80 E1 07      4997
1883 80 80          4998
1885 02 E8          4999
1887 C3            5000
1888          5001
1888          5002
1888          5003
1888          5004
1888          5005
1888          5006
1888          5007
1888          5008
1888          5009
1888          5010
1888          5011
1888          5012
1888          5013
1888          5014
1888          5015
1888 53            5016
1889 50            5017
1889          5018
1889          5019
1889          5020
1889          5021
188A 80 28          5022
188C 52            5023
188D 80 F2 FE      5024
1890 F6 E2          5025
1891          5026
1892 5A            5027
1893 F6 C2 01      5028
1896 74 03          5029
1898 05 2000       5030
189B          5031
189B 8B F0          5032
189D 58            5033
189E 8B D1          5034
189E          5035
189E          5036
189E          5037
189E          5038
189E          5039
189E          5040

M20:
CMP CRT_MODE,3
JBE M80
MOV AL,CRT_PALETTE
AND AL,ODFH
AND BL,1
JZ M22
OR AL,020H

M22:
MOV CRT_PALETTE,AL
AND AL,010H
OR AL,2
OR BL,AL
MOV AH,1
MOV AL,BL
CALL PAL_SET
4932
OR BP,BP
JZ M22Y
MOV ES:[DI][1],BL

M22Y:
INC BL
INC BL
MOV AH,2
MOV AL,BL
CALL PAL_SET
4943
OR BP,BP
JZ M27Y
MOV ES:[DI][2],BL

M27Y:
INC BL
INC BL
MOV AH,3
MOV AL,BL
CALL PAL_SET
4954
OR BP,BP
JZ M80
MOV ES:[DI][3],BL

M80:
CALL PAL_ON
JMP V_RET

C
INCLUDE VDOT.INC
C
SUBTTL VDOT.INC
C
PAGE
C
-----
C
ENTRY
C
DX = ROW
C
CX = COLUMN
C
BH = PAGE
C
EXIT
C
BX = OFFSET INTO REGEN
C
AL = BIT MASK FOR COLUMN BYTE
C
-----
C
DOT_SUP_1 PROC NEAR
C
;---- OFFSET = PAGE OFFSET + ROW * BYTES/ROW + COLUMN/8
C
MUL WORD PTR CRT_COLS ; ROW * BYTES/ROW
PUSH CX ; SAVE COLUMN VALUE
SHR CX,1 ; DIVIDE BY EIGHT TO
SHR CX,1 ; DETERMINE THE BYTE THAT
SHR CX,1 ; THIS DOT IS IN
ADD AX,CX ; GET OFFSET INTO PAGE
MOV BL,BH ; BYTE PAGE INTO BL
SUB BH,BH ; ZERO
MOV CX,BX ; COUNT VALUE
MOV BX,CRT_LEN ; LENGTH OF ONE PAGE
JCKZ DS_2 ; PAGE ZERO

DS_3:
ADD AX,BX ; BUMP TO NEXT PAGE
LOOP DS_3 ; DO FOR THE REST

DS_2:
POP CX ; RECOVER COLUMN VALUE
MOV BX,AX ; REGEN OFFSET
AND CL,07H ; SHIFT COUNT FOR BIT MASK
MOV AL,080H ; MASK BIT
SHR AL,CL ; POSITION MASK BIT
RET

DOT_SUP_1 ENDP

C
-----
C
THIS SUBROUTINE DETERMINES THE REGEN BYTE LOCATION
C
OF THE INDICATED ROW COLUMN VALUE IN GRAPHICS MODE.
C
ENTRY --
C
DX = ROW VALUE (0-199)
C
CX = COLUMN VALUE (0-639)
C
EXIT --
C
SI = OFFSET INTO REGEN BUFFER FOR BYTE OF INTEREST
C
AH = MASK TO STRIP OFF THE BITS OF INTEREST
C
CL = BITS TO SHIFT TO RIGHT JUSTIFY THE MASK IN AH
C
DH = # BITS IN RESULT
C
-----
R3 PROC NEAR
PUSH BX ; SAVE BX DURING OPERATION
PUSH AX ; WILL SAVE AL DURING OPERATION
C
;---- DETERMINE 1ST BYTE IN INDICATED ROW BY MULTIPLYING ROW VALUE BY 40
;---- ( LOW BIT OF ROW DETERMINES EVEN/ODD, 80 BYTES/ROW
MOV AL,40
PUSH DX ; SAVE ROW VALUE
AND DL,0FEH ; STRIP OFF ODD/EVEN BIT
MUL DL,DX ; AX HAS ADDRESS OF 1ST BYTE
POP DX ; OF INDICATED ROW
TEST DL,1 ; RECOVER IT
JZ R4 ; TEST FOR EVEN/ODD
JMP IF EVEN ROW ; TEST FOR EVEN/ODD
; OFFSET TO LOCATION OF ODD ROWS
; EVEN ROW
R4:
MOV SI,AX ; MOVE POINTER TO SI
POP AX ; RECOVER AL VALUE
MOV DX,CX ; COLUMN VALUE TO DX
C
;---- DETERMINE GRAPHICS MODE CURRENTLY IN EFFECT
C
-----
C
SET UP THE REGISTERS ACCORDING TO THE MODE
C
CH = MASK FOR LOW OF COLUMN ADDRESS ( 7/3 FOR HIGH/MED RES ) ;

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5041 C ; CL = # OF ADDRESS BITS IN COLUMN VALUE ( 3/2 FOR H/M) ;
5042 C ; BL = MASK TO SELECT BITS FROM POINTED BYTE (80H/COH FOR H/M) ;
5043 C ; BH = NUMBER OF VALID BITS IN POINTED BYTE ( 1/2 FOR H/M) ;
5044 C -----
5045 C
5046 C MOV BX,200H
5047 C MOV CX,302H ; SET PARMS FOR MED RES
5048 C CMP CRT_MODE,6
5049 C JC R5 ; HANDLE IF MED ARES
5050 C MOV BX,180H
5051 C MOV CX,703H ; SET PARMS FOR HIGH RES
5052 C
5053 C ;----- DETERMINE BIT OFFSET IN BYTE FROM COLUMN MASK
5054 C
5055 C R5:
5056 C AND CH,DL ; ADDRESS OF PEL WITHIN BYTE TO CH
5057 C
5058 C ;----- DETERMINE BYTE OFFSET FOR THIS LOCATION IN COLUMN
5059 C
5060 C SHR DX,CL ; SHIFT BY CORRECT AMOUNT
5061 C ADD SI,DX ; INCREMENT THE POINTER
5062 C MOV DH,BH ; GET THE # OF BITS IN RESULT TO DH
5063 C
5064 C ;----- MULTIPLY BH (VALID BITS IN BYTE) BY CH (BIT OFFSET)
5065 C
5066 C SUB CL,CL ; ZERO INTO STORAGE LOCATION
5067 C
5068 C R6:
5069 C ROR AL,1 ; LEFT JUSTIFY THE VALUE
5070 C ; IN AL (FOR WRITES)
5071 C ADD CL,CH ; ADD IN THE BIT OFFSET VALUE
5072 C DEC BH ; LOOP CONTROL
5073 C JNZ R6 ; ON EXIT, CL HAS SHIFT COUNT
5074 C ; TO RESTORE BITS
5075 C MOV AH,BL ; GET MASK TO AH
5076 C SHR AH,CL ; MOVE THE MASK TO CORRECT LOCATION
5077 C POP BX ; RECOVER REG
5078 C RET ; RETURN WITH EVERYTHING SET UP
5079 C
5080 C
5081 C ;-----
5082 C ; READ DOT -- WRITE DOT
5083 C ; THESE ROUTINES WILL WRITE A DOT, OR READ THE DOT AT
5084 C ; THE INDICATED LOCATION
5085 C ; ENTRY --
5086 C ; DX = ROW (0-199) (THE ACTUAL VALUE DEPENDS ON THE MODE)
5087 C ; CX = COLUMN (0-639) (THE VALUES ARE NOT RANGE CHECKED)
5088 C ; AL = DOT VALUE TO WRITE (1,2 OR 4 BITS DEPENDING ON MODE,
5089 C ; R2D FOR WRITING DOT ONLY, RIGHT JUSTIFIED)
5090 C ; BIT 7 OF AL=1 INDICATES XOR THE VALUE INTO THE LOCATION
5091 C ; DS = DATA SEGMENT
5092 C ; ES = REGEN SEGMENT
5093 C ;
5094 C ; EXIT
5095 C ; AL = DOT VALUE READ, RIGHT JUSTIFIED, READ ONLY
5096 C ;-----
5097 C ;----- WRITE DOT
5098 C
5099 C AHC:
5100 C ASSUME DS:ABSO
5101 C CMP CRT_MODE,7
5102 C JA WRITE_DOT_2
5103 C
5104 C WRITE_DOT PROC NEAR
5105 C ASSUME DS:ABSO,ES:NOTHING
5106 C PUSH DX
5107 C SRLD AX,ES,0800H
5108 C MOV DX,0800H
5109 C MOV ES,DX
5110 C POP DX
5111 C PUSH AX ; SAVE DOT VALUE
5112 C PUSH AX ; TWICE
5113 C CALL R3 ; DETERMINE BYTE POSITION OF THE DOT
5114 C SHR AL,CL ; SHIFT TO SET UP THE BITS FOR OUTPUT
5115 C MOV AH,AL ; STRIP OFF THE OTHER BITS
5116 C CL,ES:[SI] ; GET THE CURRENT BYTE
5117 C POP BX ; RECOVER XOR FLAG
5118 C TEST BL,80H ; IS IT ON
5119 C R2 ; YES, XOR THE DOT
5120 C NOT AH ; SET THE MASK TO REMOVE THE
5121 C AND CL,AH ; INDICATED BITS
5122 C OR AL,CL ; OR IN THE NEW VALUE OF THOSE BITS
5123 C R1: ; FINISH DOT
5124 C MOV ES:[SI],AL ; RESTORE THE BYTE IN MEMORY
5125 C POP AX
5126 C JMP V_RET
5127 C
5128 C R2: ; XOR DOT
5129 C XOR AL,CL ; EXCLUSIVE OR THE DOTS
5130 C JMP WRITE_DOT ; FINISH UP THE WRITING
5131 C
5132 C WRITE_DOT_2 PROC NEAR
5133 C CMP CRT_MODE,0FH
5134 C JB NO_ADJ2
5135 C CALL MEI_DET ; BASE CARD
5136 C JC NO_ADJ2
5137 C AND AL,10000101B ; 85H, XOR C2 CO MASK
5138 C MOV AH,AL ;
5139 C SHL AH,1 ; EXPAND CO TO C1, C2 TO C3
5140 C OR AL,AH ; BUILD ?(80H) + *(0,3,C,F)
5141 C NO_ADJ2:
5142 C PUSH AX
5143 C MOV AX,DX ; ROW VALUE
5144 C ALLD DOT_SUP_1 ; BX-OFFSET, AL=BIT MASK
5145 C MOV DH,3
5146 C MOV DL,GRAPH_ADDR ; GRAPHICS CHIP
5147 C MOV AH,G_BIT_MASK ; BIT MASK REGISTER
5148 C CALL OUT_DX ; SET BIT MASK
5149 C PUSH DX
5150 C SRLD ES,0A000H ; REGEN SEGMENT
5151 C MOV DX,0A00H
5152 C+ POP DX
5153 C POP AX
5154 C MOV ES,DX ; RECOVER COLOR
5155 C MOV CH,AL ; SAVE COLOR
5156 C TEST CH,80H ; SEE IF XOR
5157 C JZ NO_XOR ; NO XOR
5158 C MOV AH,G_DATA_ROT ; DO XOR
5159 C MOV AL,018H ; XOR FUNCTION
5160 C CALL OUT_DX ; SET THE REGISTER
5161 C JMP MD_B ; SKIP THE BLANK
5162 C MD_A: ; BLANK THE DOT
5163 C MOV DL,SEQ_ADDR ; SEQUENCER
5164 C MOV AH,S_MAP ; MAP MASK
5165 C MOV AL,OFFH ; ENABLE ALL MAPS
5166 C CALL OUT_DX ; SET THE REGISTER

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1C41 26: 8A 07      5167 C      MOV     AL,ES:[BX]      ; LATCH DATA
1C44 2A C0          5168 C      SUB     AL,AL          ; ZERO
1C46 26: 88 07      5169 C      MOV     ES:[BX],AL     ; BLANK THE DOT
1C49              5170 C      ;----- SET THE COLOR MAP MASK
1C49 B2 C4          5171 C      MOV     DL,SEQ_ADDR    ; SEQUENCER
1C4B BA 02          5172 C      MOV     AH,S_MAP      ; MAP MASK REGISTER
1C4D BA C5          5173 C      MOV     AL,CR         ; COLOR VALUE
1C4F 2A 0F          5174 C      AND     AL,0FFH       ; VALUES 0-15
1C51 E8 0D15 R     5175 C      CALL   OUT_DX         ; SET IT
1C54 26: 8A 07      5176 C      MOV     AL,ES:[BX]    ; LATCH DATA
1C57 80 FF          5177 C      MOV     AL,0FFH       ; WRITE VALUE
1C59 26: 88 07      5178 C      MOV     ES:[BX],AL    ; SET THE DOT
1C59              5179 C      ;----- NORMALIZE THE ENVIRONMENT
1C59              5180 C      ;-----
1C5C E8 0D15 R     5181 C      CALL   OUT_DX         ; ALL MAPS ON
1C5F B2 CE          5182 C      MOV     DL,GRAPH_ADDR ; GRAPHICS CHIPS
1C61 B4 03          5184 C      MOV     AH,G_DATA_ROT ; XOR REGISTER
1C63 2A C0          5185 C      SUB     AL,AL         ; NORMAL WRITES
1C65 E8 0D15 R     5186 C      CALL   OUT_DX         ; SET IT
1C68 B4 08          5187 C      MOV     AH,G_BIT_MASK ; BIT MASK
1C6A 80 FF          5188 C      MOV     AL,0FFH       ; ALL BITS ON
1C6C E8 0D15 R     5189 C      CALL   OUT_DX         ; SET IT
1C6F E9 219E R     5190 C      JMP     V_RET         ; WRITE DOT DONE
1C72              5191 C      WRITE_DOT_2
1C72              5192 C      ENDP
1C72              5193 C      RD_S   PROC   NEAR
1C72 50              5194 C      ASSUME DS:ABSO
1C73 52              5195 C      PUSH  AX
1C73              5196 C      PUSH  DX
1C73              5197 C      SRLOAD ES,0A000H
1C74 BA A000        5198 C+    MOV     DX,0A000H
1C77 8E C2          5199 C+    MOV     ES,DX
1C79 5A              5200 C      POP   DX
1C7A 58              5201 C      POP   AX
1C7B 8B C2          5202 C      MOV     AX,DX
1C7D E8 1B60 R     5203 C      CALL  DOT_SUP_1
1C80 B5 07          5204 C      MOV     CH,7
1C82 2A E9          5205 C      SUB     CH,DL
1C84 2B D2          5206 C      SUB     DX,DX
1C86 B0 00          5207 C      MOV     AL,0
1C88 C3              5208 C      RET
1C89              5209 C      RD_S   ENDP
1C89              5210 C      ;-----
1C89 8A CD          5211 C      RD_1S  PROC   NEAR
1C8B BA 04          5212 C      MOV     CL,CH
1C8D 52              5213 C      MOV     AH,4
1C8E 8E 03          5214 C      PUSH  DX
1C8E B6 03          5215 C      MOV     DH,3
1C90 B2 CE          5216 C      MOV     DL,GRAPH_ADDR
1C92 E8 0D15 R     5217 C      CALL  OUT_DX
1C95 5A              5218 C      POP   DX
1C96 26: 8A 27        5219 C      MOV     AH,ES:[BX]
1C99 D2 EC          5220 C      SHR   AH,CL
1C9B 80 E4 01       5221 C      AND   AH,1
1C9E C3              5222 C      RET
1C9F              5223 C      RD_1S  ENDP
1C9F              5224 C      ;----- READ DOT
1C9F              5225 C      ;-----
1C9F              5226 C      AHD:
1C9F 80 3E 0449 R 07 5227 C      ASSUME DS:ABSO
1CA4 77 18          5229 C      CMP   CRT_MODE,7
1CA4              5230 C      JA    R_1
1CA4              5231 C      ;-----
1CA6              5232 C      READ_DOT PROC   NEAR
1CA6 52              5233 C      ASSUME DS:ABSO,ES:NOTHING
1CA6 52              5234 C      PUSH  DX
1CA6 52              5235 C      SRLOAD ES,0B800H
1CA7 BA B800        5236 C+    MOV     DX,0B800H
1CAA 8E C2          5237 C+    MOV     ES,DX
1CAC 5A              5238 C      POP   DX
1CAD E8 1B88 R     5239 C      CALL  R3
1CB0 26: 8A 04       5240 C      MOV     AL,ES:[SI]
1CB3 22 C4          5241 C      AND   AL,AH
1CB5 D2 E0          5242 C      SHL  AL,CL
1CB7 8A CE          5243 C      MOV   CL,DH
1CB9 D2 C0          5244 C      ROL  AL,CL
1CBB E9 219E R     5245 C      JMP   V_RET
1CBE              5246 C      READ_DOT ENDP
1CBE              5247 C      ;-----
1CBE 80 3E 0449 R 0F 5248 C      R_1:
1CC3 72 25          5249 C      CMP   CRT_MODE,0FH
1CC5 E8 14F7 R     5250 C      JB   READ_DOT_2
1CC8 72 20          5251 C      CALL MEM_DET
1CC8              5252 C      JC   READ_DOT_2
1CC8              5253 C      ;-----
1CCA              5254 C      READ_DOT_1 PROC   NEAR
1CCA E8 1C72 R     5255 C      ASSUME DS:ABSO,ES:NOTHING
1CCD E8 1C89 R     5256 C      RD_S   PROC   NEAR
1CCD 0A D4          5257 C      CALL  RD_1S
1CCD 0A D4          5258 C      OR   DL,AH
1CD2 D0 E4          5259 C      SHL  AH,1
1CD4 0A D4          5260 C      OR   DL,AH
1CD6 B0 02          5261 C      MOV  AL,2
1CDB E8 1C89 R     5262 C      CALL  RD_1S
1CDB D0 E4          5263 C      SHL  AH,1
1CDD D0 E4          5264 C      SHL  AH,1
1CDF 0A D4          5265 C      OR   DL,AH
1CE1 D0 E4          5266 C      SHL  AH,1
1CE3 0A D4          5267 C      OR   DL,AH
1CE5 8A C2          5268 C      MOV  AL,CL
1CE7 E9 219E R     5269 C      JMP   V_RET
1CEA              5270 C      READ_DOT_1 ENDP
1CEA              5271 C      ;-----
1CEA 80 3E 0449 R 0F 5272 C      R_2:
1CED E8 1C89 R     5273 C      RD_S   PROC   NEAR
1CED 0A D4          5274 C      CALL  RD_1S
1CED 0A D4          5275 C      OR   DL,AH
1CF0 8A C8          5276 C      MOV  AL,CL
1CF2 D2 E4          5277 C      SHL  AH,CL
1CF4 0A D4          5278 C      OR   DL,AH
1CF6 FE C0          5279 C      INC  AL
1CF8 3C 03          5280 C      CMP  AL,3
1CFA 76 F1          5281 C      JBE  RD_2A
1CFC 8A C2          5282 C      MOV  AL,CL
1CFE E9 219E R     5283 C      JMP   V_RET
1C01              5284 C      READ_DOT_2 ENDP
1C01              5285 C      ;-----
1C01              5286 C      ;-----
1C01              5287 C      ;-----
1C01              5288 C      ;-----
1C01              5289 C      ;-----
1C01              5290 C      ;-----
1C01              5291 C      ;-----
1C01              5292 C      ;-----

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5293 C ; TO ZERO, AND THE ROW VALUE IS INCREMENTED. IF THE ROW VALUE ;
5294 C ; LEAVES THE FIELD, THE CURSOR IS PLACED ON THE LAST ROW, FIRST ;
5295 C ; COLUMN, AND THE ENTIRE SCREEN IS SCROLLED UP ONE LINE. WHEN ;
5296 C ; THE SCREEN IS SCROLLED UP, THE ATTRIBUTE FOR FILLING THE NEWLY ;
5297 C ; BLANKED LINE IS READ FROM THE CURSOR POSITION ON THE PREVIOUS ;
5298 C ; LINE BEFORE THE SCROLL, IN CHARACTER MODE. IN GRAPHICS MODE, ;
5299 C ; THE 0 COLOR IS USED. ;
5300 C ; ENTRY ;
5301 C ; (AH) = CURRENT CRT MODE ;
5302 C ; (AL) = CHARACTER TO BE WRITTEN ;
5303 C ; NOTE THAT BACK SPACE, CAR RET, BELL AND LINE FEED ARE HANDLED ;
5304 C ; AS COMMANDS RATHER THAN AS DISPLAYABLE GRAPHICS ;
5305 C ; (BL) = FOREGROUND COLOR FOR CHAR WRITE IF CURRENTLY IN A ;
5306 C ; GRAPHICS MODE ;
5307 C ; EXIT ;
5308 C ; ALL REGISTERS SAVED ;
5309 C ;----- ;
1D01 C AHE: ;
5310 C ; ASSUME CS:CODE,DS:ABS0 ;
5311 C ; PUSH AX ; ; SAVE REGISTERS ;
5312 C ; MOV BH,ACTIVE_PAGE ; ; GET THE ACTIVE PAGE ;
5313 C ; PUSH BX ; ; SAVE ;
5314 C ; MOV BL,BH ; ; GET PAGE TO BL ;
5315 C ; XOR BH,BH ; ; CLEAR HIGH BYTE ;
5316 C ; SAL BX,1 ; ; *2 FOR WORD OFFSET ;
5317 C ; MOV DX,[BX + OFFSET CURSOR_POSN] ; ; CURSOR, ACTIVE PAGE ;
5318 C ; POP BX ; ; RECOVER ;
5319 C ; ;
5320 C ;----- DX NOW HAS THE CURRENT CURSOR POSITION ;
5321 C ; ;
5322 C ; ;
5323 C ; CMP AL,0DH ; ; IS IT CARRIAGE RETURN ;
5324 C ; JE UP ; ; CAR_RET ;
5325 C ; CMP AL,0AH ; ; IS IT A LINE FEED ;
5326 C ; JE U10 ; ; LINE_FEED ;
5327 C ; CMP AL,08H ; ; IS IT A BACKSPACE ;
5328 C ; JE UB ; ; BACK_SPACE ;
5329 C ; CMP AL,07H ; ; IS IT A BELL ;
5330 C ; JE U11 ; ; BELL ;
5331 C ; ;
5332 C ;----- WRITE THE CHAR TO THE SCREEN ;
5333 C ; ;
5334 C ; MOV AH,10 ; ; WRITE CHAR ONLY ;
5335 C ; MOV CX,1 ; ; ONLY ONE CHAR ;
5336 C ; INT 10H ; ; WRITE THE CHAR ;
5337 C ; ;
5338 C ;----- POSITION THE CURSOR FOR NEXT CHAR ;
5339 C ; ;
5340 C ; INC DL ; ; ;
5341 C ; CMP DL,BYTE PTR CRT_COLS ; ; TEST FOR COLUMN OVERFLOW ;
5342 C ; JNZ U7 ; ; SET CURSOR ;
5343 C ; SUB DL,DL ; ; COLUMN FOR CURSOR ;
5344 C ; CMP DH,ROWS ; ; ;
5345 C ; JNZ U6 ; ; SET_CURSOR_INC ;
5346 C ; ;
5347 C ;----- SCROLL REQUIRED ;
5348 C ; ;
5349 C ; U1: ;
5350 C ; CALL SET_CPOS ; ; SET THE CURSOR ;
5351 C ; ;
5352 C ;----- DETERMINE VALUE TO FILL WITH DURING SCROLL ;
5353 C ; ;
5354 C ; MOV AL,CRT_MODE ; ; GET THE CURRENT MODE ;
5355 C ; CMP AL,4 ; ; ;
5356 C ; JB U2 ; ; READ-CURSOR ;
5357 C ; SUB BH,BH ; ; FILL WITH BACKGROUND ;
5358 C ; CMP AL,7 ; ; ;
5359 C ; JNE U3 ; ; ;
5360 C ; U2: ;
5361 C ; MOV AH,8 ; ; ;
5362 C ; INT 10H ; ; READ CHAR/ATTR ;
5363 C ; MOV BH,AH ; ; STORE IN BH ;
5364 C ; ; ;
5365 C ; U3: ;
5366 C ; MOV AX,GO1H ; ; SCROLL-UP ;
5367 C ; SUB CX,CX ; ; SCROLL ONE LINE ;
5368 C ; MOV DH,ROWS ; ; UPPER LEFT CORNER ;
5369 C ; DEC DL,BYTE PTR CRT_COLS ; ; LOWER RIGHT ROW ;
5370 C ; ; ; ;
5371 C ; U4: ;
5372 C ; INT 10H ; ; LOWER RIGHT COLUMN ;
5373 C ; ; ;
5374 C ; U5: ;
5375 C ; POP AX ; ; VIDEO-CALL-RETURN ;
5376 C ; JMP V_RET ; ; SCROLL UP THE SCREEN ;
5377 C ; ; ;
5378 C ; U6: ;
5379 C ; INC DH ; ; TTY-RETURN ;
5380 C ; ; ;
5381 C ; U7: ;
5382 C ; MOV AH,2 ; ; RESTORE THE CHARACTER ;
5383 C ; JMP U4 ; ; RETURN TO CALLER ;
5384 C ; ; ;
5385 C ; ; ;
5386 C ; U8: ;
5387 C ; OR DL,DL ; ; NEXT ROW ;
5388 C ; JZ U7 ; ; SET-CURSOR ;
5389 C ; DEC DL ; ; ;
5390 C ; JMP U7 ; ; ESTABLISH THE NEW CURSOR ;
5391 C ; ; ;
5392 C ;----- BACK SPACE FOUND ;
5393 C ; ;
5394 C ; U8: ;
5395 C ; OR DL,DL ; ; ALREADY AT END OF LINE ;
5396 C ; JZ U7 ; ; SET CURSOR ;
5397 C ; DEC DL ; ; NO -- JUST MOVE IT BACK ;
5398 C ; JMP U7 ; ; SET_CURSOR ;
5399 C ; ; ;
5400 C ;----- CARRIAGE RETURN FOUND ;
5401 C ; ;
5402 C ; U9: ;
5403 C ; SUB DL,DL ; ; MOVE TO FIRST COLUMN ;
5404 C ; JMP U7 ; ; SET_CURSOR ;
5405 C ; ; ;
5406 C ;----- LINE FEED FOUND ;
5407 C ; ;
5408 C ; U10: ;
5409 C ; CMP DH,ROWS ; ; BOTTOM OF SCREEN ;
5410 C ; JNE U6 ; ; YES, SCROLL THE SCREEN ;
5411 C ; JMP U1 ; ; NO, JUST SET THE CURSOR ;
5412 C ; ; ;
5413 C ;----- BELL FOUND ;
5414 C ; ;
5415 C ; U11: ;
5416 C ; MOV BL,2 ; ; SET UP COUNT FOR BEEP ;
5417 C ; CAL BEEP ; ; SOUND THE POD BELL ;
5418 C ; JMP U5 ; ; TTY_RETURN ;
5419 C ; ; ;
5420 C ;----- CURRENT VIDEO STATE ;
5421 C ; ;
5422 C ; UHF: ;
5423 C ; ASSUME DS:ABS0 ; ; ;
5424 C ; MOV AH,BYTE PTR CRT_COLS ; ; GET NUMBER OF COLUMNS ;
5425 C ; MOV BH,ACTIVE_PAGE ; ; ;
5426 C ; MOV AL,INFO ; ; ;
5427 C ; AND AL,080H ; ; ;
5428 C ; OR AL,CRT_MODE ; ; ;

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1D96 5F          5419 C      POP      POP
1D97 5E          5420 C      POP      DI
1D98 59          5421 C      POP      CX
1D99 59          5422 C      POP      CX
1DA0 5A          5423 C      POP      DX
1D9B 1F          5424 C      POP      DS
1D9C 07          5425 C      POP      ES
1D9D 5D          5426 C      POP      BP
1D9E CF          5427 C      IRET
                    5428 C
                    5429 C
                    5430 C      SUBTTL
                    5431
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1D9F            5434 PAL_SET PROC NEAR
1D9F 50          5435 PUSH   AX
1DA0 E8 0D05 R  5436 CALL   WHAT_BASE
1DA3 FA          5437 CLI
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1E40 C8 3E 04A8 R      5545      LES    D1,SAVE_PTR
1E44 83 C7 04         5546      ADD    D1,4
1E47 26: C4 3D       5548      LES    D1,DMWORD PTR ES:[D1]      ; ES:D1 PTR TO PAL SAVE AREA
1E4A 8C C0           5549      MOV    AX,ES
1E4C 0B C7           5550      OR     AX,D1
1E4E 74 09           5551      JZ     TLO_3
1E50 1F             5552      POP    DS
1E51 1E             5553      PUSH  DS
1E52 8B F2         5554      MOV    SI,DX
1E54 B9 0011        5555      MOV    CX,17D
1E57 F3/ AH        5556      REP
1E59 07             5557      TLO_3: POP    ES
1E5A 1F             5558      POP    DS
1E5B 8B DA         5559      MOV    BX,DX
1E5D E8 1DC0 R     5560      CALL  PAL_INIT
1E60 2A E4         5561      SUB    AH,AH
1E62             5562      BM_2A: MOV    AL,ES:[BX]
1E62 26: 8A 07     5563      CALL  PAL_SET
1E65 E8 1D9F R     5564      INC   AH
1E68 FE C4         5565      INC   BX
1E6B 80 FC 10      5566      CMP   AH,010H
1E6E 72 F2         5567      JB    BM_2A
1E70 FE C4         5568      INC   AH
1E72 26: 8A 07     5569      MOV    AL,ES:[BX]
1E75 E8 1D9F R     5570      CALL  PAL_SET
1E78 E9 219E R     5571      CALL  PAL_ON
1E7E             5572      JMP   V_RET
1E7E FE CC         5573      BM_3:  DEC   AH
1E80 75 29         5574      JNZ   BM_4
5582             5575      ;----- TOGGLE INTENSIFY/BLINKING BIT
5583             5576      5584      PUSH  BX
5584             5577      CALL  MAKE_BASE
5585             5578      ADD   BX,010H + LN_4
5586 83 C3 33       5579      MOV   AL,ES:[BX]
5587 26: 5A 07     5580      POP   BX
1E8C 5B           5581      OR    BL,BL
1E8F 75 0A         5582      JNZ   BM_6
5592             5583      ;----- ENABLE INTENSIFY
5593             5584      5594      AND   CRT_MODE_SET,11011111B
5594             5585      AND   AL,07FH
1E91 80 26 0465 R DF 5586      JMP   BM_7
1E96 24 F7         5587      BM_6:  DEC   BL
1E98 EB 0C 90       5588      JNZ   BM_7
1E9B             5589      ;----- ENABLE BLINK
1E9B FE CB         5590      OR    CRT_MODE_SET,020H
1E9D 75 07         5591      OR    AL,0BH
1E9F 80 0E 0465 R R20 5592      BM_7:  MOV   AH,P_MODE
1EA4 OC 08         5593      CALL  PAL_SET
1EA6 B4 10         5594      BM_4:  JMP   V_RET
1EA8 E8 1D9F R     5595      C     INCLUDE VCHGEN,INC
1EAB E9 219E R     5596      C     SUBTTL VCHGEN,INC
1EAC             5597      C     PAGE
1EAE             5598      C     ;-----
1EAE 50           5599      C     ; ENTRY
1EAF 55           5600      C     AL = 0 USER SPECIFIED FONT
1EB0 53           5601      C     ; 1 8 X 14 FONT
1EB1 51           5602      C     ; 2 8 X 8 DOUBLE DOT
1EB2 52           5603      C     ; BL = BLOCK TO LOAD
1EB3 06           5604      C     ;-----
1EBA E8 0CFE R     5605      CH_GEN: PUSH  AX
1EB7 AD 0449 R     5606      C     ; SAVE THE INVOLVED REGS
1EBA 50           5607      C     PUSH  BP
1EBB 3C 07         5608      C     PUSH  BX
1EBD 74 07         5609      C     PUSH  CX
1EBF C6 06 0449 R OB 5610      C     PUSH  DX
1EC4 EB 05         5611      C     PUSH  ES
1EC6 C6 06 0449 R OC 5612      C     ASSUME DS:ABSO
1ECB E8 0DAB R     5613      C     CALL  DDS
1EC8 EB 0CFE R     5614      C     MOV   AL,CRT_MODE
1EC9 E8 0CFE R     5615      C     PUSH  AX
1ED1 58           5616      C     CMP   AL,7
1ED2 A2 0449 R     5617      C     JE    H14
1ED5 07           5618      C     MOV   CRT_MODE,OBH
1ED6 5A           5619      C     JMP  SHORT H15
1ED7 59           5620      C     H14:  MOV   CRT_MODE, OCH
1ED8 58           5621      C     ; MONOCHROME VALUES
1ED9 5D           5622      C     H15:  CALL  SET_REGS
1EDA 58           5623      C     CALL  DDS
1EDB 0A C0         5624      C     POP   AX
1EDD 74 17         5625      C     MOV   CRT_MODE,AL
1EDF 0E           5626      C     ; RESTORE REGS THAT WERE
1EE0 07           5627      C     POP   ES
1EE1 2B 02         5628      C     POP   DX
1EE3 B9 0100       5629      C     POP   CX
1EE6 FE C8         5630      C     POP   BX
1EE8 75 07         5631      C     POP   BP
1EEA B7 0E         5632      C     POP   AX
1EEC BD 0000 E     5633      C     OR    AL,AL
1EEF EB 05         5634      C     JZ    DO_MAP2
1EF1             5635      C     PUSH  CS
1EF1 B7 08         5636      C     POP   ES
1EF3 BD 0000 E     5637      C     SUB   DX,DX
1EF4             5638      C     MOV   CX,0256D
1EF5             5639      C     DEC   AL
1EF6             5640      C     JNZ   H7
1EF7             5641      C     MOV   BH,014D
1EF8             5642      C     BP,OFFSET CGMM
1EF9             5643      C     JMP  SHORT DO_MAP2
1EF1 B7 08         5644      C     H7:  MOV   BH,8
1EF3 BD 0000 E     5645      C     MOV   BP,OFFSET CGDDOT
1EF4             5646      C     ; 8 X 8 FONT
1EF5             5647      C     ; ROM 8 X 8 DOUBLE DOT
1EF6             5648      C     ;-----
1EF7             5649      C     ; ALPHA CHARACTER GENERATOR LOAD
1EF8             5650      C
1EF9             5651      C
1EFA             5652      C
1EFB             5653      C
1EFC             5654      C
1EFD             5655      C
1EFE             5656      C
1EFF             5657      C
1F00             5658      C
1F01             5659      C
1F02             5660      C
1F03             5661      C
1F04             5662      C
1F05             5663      C
1F06             5664      C
1F07             5665      C
1F08             5666      C
1F09             5667      C
1F0A             5668      C
1F0B             5669      C
1F0C             5670      C

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5671 C ;
5672 C ; ENTRY
5673 C ; ES:BP - POINTER TO TABLE
5674 C ; CX - COUNT OF CHARS
5675 C ; DX - CHAR COUNT OFFSET INTO MAP 2
5676 C ; BH - BYTES PER CHARACTER
5677 C ; BL - MAP 2 BLOCK TO LOAD
5678 C ;-----
5679 C ; DO_MAP2:
5680 C ;
5681 C ; PUSH ES
5682 C ; POP DS
5683 C ; PUSH DX
5684 C ; SRLoad ES,0A000H
5685 C ; DX,0A000H
5686 C+ ; MOV ES,DX
5687 C ; POP DX
5688 C ; PUSH CX
5689 C ; MOV CL,5
5690 C ; SHL DX,CL
5691 C ; POP CX
5692 C ; OR BL,BL
5693 C ; JZ H3
5694 C ;
5695 C ; H4: ADD DX,04000H
5696 C ; DEC BL
5697 C ; JNZ H4
5698 C ;
5699 C ; H3: MOV AL,BH
5700 C ; AH,AH
5701 C ; MOV DI,DX
5702 C ; MOV SI,BP
5703 C ; JCXZ LD_OVER
5704 C ;
5705 C ; LD: PUSH CX
5706 C ; CX,AX
5707 C ; REP MOVSB
5708 C ; SUB DI,AX
5709 C ; ADD DI,020H
5710 C ; CX
5711 C ; LOOP LD
5712 C ; LD_OVER: RET
5713 C ;
5714 C ; BRK_1:
5715 C ; ASSUME DS:ABSO
5716 C ; CALL DDS
5717 C ; MOV POINTS,AX
5718 C ; DX,ADDR_6845
5719 C ; CMP CRT_MODE,7
5720 C ; JNE H11A
5721 C ; MOV AH,C_UNDERLN_LOC
5722 C ; CALL OUT_DX
5723 C ;
5724 C ; H11A: DEC AL
5725 C ; MOV AH,C_MAX_SCAN_LN
5726 C ; CALL OUT_DX
5727 C ; DEC AL
5728 C ;
5729 C ; MOV CH,AL
5730 C ; MOV CL,AL
5731 C ; CL
5732 C ; MOV AH,1
5733 C ; INT 10H
5734 C ;
5735 C ; MOV BL,CRT_MODE
5736 C ; MOV AX,350D
5737 C ; CMP BL,3
5738 C ; JA H11
5739 C ; CALL BRST_DET
5740 C ; H1
5741 C ; MOV AX,200D
5742 C ;
5743 C ; H11: JNC
5744 C ; CWD
5745 C ; DIV POINTS
5746 C ; DEC AX
5747 C ; MOV ROWS,AL
5748 C ; INC AL
5749 C ; SUB AH,AH
5750 C ; MUL POINTS
5751 C ; DEC AX
5752 C ; MOV DX,ADDR_6845
5753 C ; MOV AH,C_VRT_DSP_END
5754 C ; CALL OUT_DX
5755 C ; MOV AL,ROWS
5756 C ; INC AL
5757 C ; MUL BYTE PTR CRT_COLS
5758 C ; SHL AX,1
5759 C ; ADD AX,256D
5760 C ; MOV CRT_LEN,AX
5761 C ; CALL PH_5
5762 C ; JMP V_RET
5763 C ;
5764 C ;----- LOADABLE CHARACTER GENERATOR ROUTINES
5765 C ;
5766 C ; AH11: CMP AL,010H
5767 C ; JAE AH11_ALPHA1
5768 C ;
5769 C ;----- ALPHA MODE ACTIVITY HERE
5770 C ;
5771 C ; CMP AL,03H
5772 C ; JAE H1
5773 C ; CALL CH_GEN
5774 C ; CALL SET_REGS
5775 C ; CALL PH_5
5776 C ; ASSUME DS:ABSO
5777 C ; CALL DDS
5778 C ; MOV CK_CURSOR_MODE
5779 C ; MOV AH,1
5780 C ; INT 10H
5781 C ; JMP V_RET
5782 C ;
5783 C ;----- SET THE CHARACTER GENERATOR BLOCK SELECT REGISTER
5784 C ;
5785 C ; H1: JNE H2
5786 C ; MOV DH,3
5787 C ; MOV DL,SEQ_ADDR
5788 C ;
5789 C ;
5790 C ; MOV AX,1
5791 C ; CALL OUT_DX
5792 C ;
5793 C ; MOV AH,S_CGEN
5794 C ; MOV AL,BL
5795 C ; CALL OUT_DX
5796 C ;
5797 C ;
5798 C ;
5799 C ;
5800 C ;
5801 C ;
5802 C ;
5803 C ;
5804 C ;
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5991 C ;
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5993 C ;
5994 C ;
5995 C ;
5996 C ;
5997 C ;
5998 C ;
5999 C ;
6000 C ;

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1FCA BB 0003      5797      C      MOV      AX,3
1FCD EB 0015 R    5798      C      CALL     OUT_DX      ; AH=S_RESET, AL=3
1FDD 00      5799      C
1FDD E9 219E R    5800      C      JMP      V_RET      ; RETURN TO CALLER
1F03      5801      C
1F03 3C 20      5802      C      AH11_ALPHA1:
1F05 73 26      5803      C      ASSUME   DS:ABSO
1F05      5804      C      CMP      AL,020H
1F05      5805      C      JAE     AH11_GRAPHICS
1F05      5806      C
1F05      5807      C      ;----- ALPHA MODE ACTIVITY HERE
1F05      5808      C
1F07 2C 10      5809      C      SUB      AL,010H
1F09 3C 02      5810      C      CMP      AL,02H
1F09 77 F3      5811      C      JA       H2
1F0D 50      5812      C      PUSH    AX
1F0D 53      5813      C      PUSH    BX
1F0D EB 1EAE R    5814      C      CALL    CH_GEN
1F0E EB 0DAB R    5815      C      CALL    SET_REGS
1F0E 5B      5816      C      POP     BX
1F0E 58      5817      C      POP     AX
1F0E BA E0      5818      C      MOV     AH,AL
1F0F 0A E4      5819      C      OR      AH,AH
1F0F 8A C7      5820      C      MOV     AL,BH
1F0F 74 09      5821      C      H13:   JZ       H13
1F0F B0 08      5822      C      MOV     AL,8
1F11 80 FC 01     5823      C      CMP     AH,1
1F11 75 02     5824      C      JNE     H13
1F11 B0 0E     5825      C      MOV     AL,14D
1F11      5826      C
1F11 2A E4     5827      C      H13:   SUB     AH,AH
1F11 E9 1F29 R    5828      C      JMP     BRL_1
1F11      5829      C
1F11      5830      C      ;----- GRAPHICS MODE ACTIVITY HERE
1F11      5831      C
1F11      5832      C      AH11_GRAPHICS:
1F11      5833      C      ASSUME   DS:ABSO
1F11      5834      C      CMP      AL,030H
1F11      5835      C      JAE     AH11_INFORM
1F11      5836      C      SUB     AL,020H
1F11      5837      C      JNZ     F10
1F11      5838      C
1F11      5839      C      ;----- COMPATIBILITY, UPPER HALF GRAPHICS CHARACTER SET
1F11      5840      C
1F11      5841      C      ASSUME   DS:ABSO
1F11      5842      C      SRLoad  DS,0
1F11      5843      C+     SUB     DX,DX
1F11      5844      C+     MOV     DS,DX
1F11      5845      C
1F11      5846      C      CLI
1F11      5847      C      MOV     WORD PTR EXT_PTR, BP
1F11      5848      C      MOV     WORD PTR EXT_PTR+2, ES
1F11      5849      C
1F11      5850      C      F11:   STI
1F11      5851      C
1F11      5852      C      F10:   JMP     V_RET
1F11      5853      C
1F11      5854      C      ASSUME   DS:ABSO
1F11      5855      C+     PUSH    DX
1F11      5856      C      SRLoad  DS,0
1F11      5857      C+     SUB     DX,DX
1F11      5858      C+     MOV     DS,DX
1F11      5859      C      POP     DX
1F11      5860      C      CMP     AL,03H
1F11      5861      C      JA       F11
1F11      5862      C      DEC     AL
1F11      5863      C      JZ       F19
1F11      5864      C      PUSH    CS
1F11      5865      C      POP     ES
1F11      5866      C      DEC     AL
1F11      5867      C      F13:   MOV     CX,14D
1F11      5868      C      MOV     BP,OFFSET CGMN
1F11      5869      C      JMP     SHORT F19
1F11      5870      C
1F11      5871      C      MOV     CX,8
1F11      5872      C      MOV     BP,OFFSET CGDDOT
1F11      5873      C      F19:   MOV     CX,8
1F11      5874      C      MOV     BP,OFFSET CGDDOT
1F11      5875      C
1F11      5876      C      CLI
1F11      5877      C      MOV     WORD PTR GRX_SET, BP
1F11      5878      C      MOV     WORD PTR GRX_SET+2, ES
1F11      5879      C
1F11      5880      C      STI
1F11      5881      C      ASSUME   DS:ABSO
1F11      5882      C      CALL    DDS
1F11      5883      C      MOV     MOV     POINTS,CX
1F11      5884      C      MOV     AL,BI
1F11      5885      C      MOV     BX,OFFSET RT
1F11      5886      C      OR      AL,AL
1F11      5887      C      JNZ     DR_3
1F11      5888      C      MOV     AL,DL
1F11      5889      C      JMP     DR_1
1F11      5890      C
1F11      5891      C      DR_3:  CMP     AL,3
1F11      5892      C      JBE     DR_2
1F11      5893      C      MOV     AL,2
1F11      5894      C
1F11      5895      C      DR_2:  XLAT   CS:RT
1F11      5896      C
1F11      5897      C      DR_1:  DEC     AL
1F11      5898      C      MOV     ROWS,AL
1F11      5899      C      JMP     V_RET
1F11      5900      C
1F11      5901      C      RT     LABEL BYTE
1F11      5902      C      DB     00D,14D,25D,43D
1F11      5903      C
1F11      5904      C      ;----- INFORMATION RETURN DONE HERE
1F11      5905      C
1F11      5906      C      AH11_INFORM:
1F11      5907      C      ASSUME   DS:ABSO
1F11      5908      C      CMP     AL,030H
1F11      5909      C      JE       F5
1F11      5910      C
1F11      5911      C      F5:   JMP     V_RET
1F11      5912      C
1F11      5913      C      F6:   MOV     CX,POINTS
1F11      5914      C      MOV     DL,ROWS
1F11      5915      C      CMP     BH,7
1F11      5916      C      JA       F7
1F11      5917      C      CMP     BH,1
1F11      5918      C      JA       F7
1F11      5919      C
1F11      5920      C      ASSUME   DS:ABSO
1F11      5921      C      DX     PUSH    DX
1F11      5922      C      SRLoad  DS,0
1F11      5923      C+     SUB     DX,DX
1F11      5924      C+     MOV     DS,DX
1F11      5925      C      POP     DX

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208A 0A FF          5923 C OR BH,BH
208C 75 07          5924 C F9
208E C4 2E 007C R 5925 C LES BP,EXT_PTR
2092 EB 1A 90       5926 C JMP INFORM_OUT
2095                5927 C F9:
2099 C4 2E 010C R 5928 C LES BP,CRX_SET
2099 EB 13 90       5929 C JMP INFORM_OUT
                    5930 C
                    5931 C ;---- HANDLE BH = 2 THRU BH = 5 HERE RETURN ROM TABLE POINTERS
                    5932 C
209C                5933 C
                    5934 C
209C 80 EF 02        5935 C F7:
209F 8A DF          5936 C ASSUME DS:ABS0
20A1 2A FF          5937 C SUB BH,BH
20A3 D1 E3         5938 C SUB BH,BH
20A5 81 C3 20B7 R 5939 C SAL BX,1
20A9 2E: 8B 2F     5940 C ADD BX,OFFSET_TBL_5
20AC 0E            5941 C MOV BP,CS:[BX]
20AD 07            5942 C PUSH CS
                    5943 C POP ES
                    5944 C
20AE                5944 C INFORM_OUT:
20AE 5F            5945 C POP DI
20AF 5E            5946 C POP SI
20B0 5B            5947 C POP BX
20B1 58            5948 C POP AX
20B2 58            5949 C POP AX ; DISCARD SAVED CX
20B3 1F            5950 C POP DS ; DISCARD SAVED DX
20B4 58            5951 C POP AX ; DISCARD SAVED ES
20B5 58            5952 C POP AX ; DISCARD SAVED BP
20B6 CF            5953 C IRET
                    5954 C
                    5955 C ;---- TABLE OF CHARACTER GENERATOR OFFSETS
                    5956 C
20B7                5957 C TBL_5
20B7 0000 E        5958 C LABEL WORD
20B9 0000 E        5959 C DW OFFSET CGMN
20BB 0000 E        5960 C DW OFFSET CDDOT
20BD 0000 E        5961 C DW OFFSET INT_1F_1
                    5962 C DW OFFSET CGMN_FDG
                    5963 C
                    5964 C SUBTTL
                    5965 C
                    5966 C ;---- ALTERNATE SELECT
                    5967 C
20BF                5968 C AH12:
20BF 80 FB 10      5969 C ASSUME DS:ABS0
20C2 72 51         5970 C CMP BL,010H
20C4 74 18         5971 C JB ACT_1
20C6 80 FB 20      5972 C JE BL_020H
20C9 74 03         5973 C CMP ACT_2
20CB E9 219E R     5974 C JMP V_RET
20CE                5975 C ACT_2:
20CE 2B D2         5976 C SRLOAD DS,0
20D0 8E DA         5977 C SUB DX,DX
20D2 FA           5978 C MOV DX,DX
20D2 FA           5979 C CLI
20D3 C7 06 0014 R 21A7 R 5980 C MOV WORD PTR INT5_PTR, OFFSET PRINT_SCREEN
20D9 8C 0E 0016 R 5981 C MOV WORD PTR INT5_PTR+2, CS
20DD FB           5982 C STI
20DE E9 219E R     5983 C JMP V_RET
20E1                5984 C ACT_3:
20E1 8A 3E 04B7 R 5985 C MOV BH,INFO
20E5 80 E7 02      5986 C AND BH,2 ; LOOKING FOR MONOC BIT
20E8 D0 EF         5987 C SHR BH,1 ; ISOLATE
                    5988 C ; ADJUST
20EA A0 04B7 R     5989 C MOV AL,INFO
20ED 24 60         5990 C AND AL,0100000BH ; LOOKING FOR MEMORY
20EF B1 05         5991 C MOV CL,5 ; MEMORY BITS
20F1 D2 E8         5992 C SHR CL,5 ; SHIFT COUNT
20F3 8A D8         5993 C MOV AL,CL ; ADJUST MEM VALUE
                    5994 C ; RETURN REGISTER
20F5 8A 0E 04B8 R 5995 C MOV CL,INFO_3
20F9 8A E9         5996 C MOV CH,CL ; FEATURE/SWICH
20FB 80 E1 0F     5997 C AND CL,0FH ; DUPLICATE IN CH
20FE D0 ED         5998 C SHR CH,1 ; MASK OFF SWITCH VALUE
2100 D0 ED         5999 C SHR CH,1 ; MOVE FEATURE VALUE
2102 D0 ED         6000 C SHR CH,1
2104 D0 ED         6001 C SHR CH,1
2106 80 E5 0F     6002 C AND CH,0FH ; MASK IT
                    6003 C
2109 5F            6004 C POP DI
210A 5E            6005 C POP SI
210B 5A            6006 C POP DX
210C 5A            6007 C POP DX ; DISCARD BX
210D 5A            6008 C POP DX ; DISCARD CX
210E 1F            6009 C POP DS
210F 07            6010 C POP ES
2110 5D            6011 C POP BP
2111 CF            6012 C IRET
2112                6013 C AH12_X:
2112 E9 219E R     6014 C JMP V_RET ; RETURN TO CALLER
2115 45            6015 C ACT_1:
2115 E9 219E R     6016 C STR_OUTZ:
2115 E9 219E R     6017 C JMP V_RET ; RETURN TO CALLER
                    6018 C
                    6019 C ;---- WRITE STRING
                    6020 C
2118                6021 C AH13:
2118 3C 04         6022 C CMP AL,04H
211A 73 F9         6023 C JAE STR_OUTZ ; RANGE CHECK
211C E3 F7         6024 C JCXZ STR_OUTZ ; INVALID PARAMETER
211E 53            6025 C PUSH BX
211F 8A DF         6026 C MOV BL,BH
2121 2A FF         6027 C SUB BH,BH
2123 D1 E3         6028 C AND BX,1
2125 8B B7 0450 R 6029 C MOV SI,[BX + OFFSET CURSOR_POSN]
2129 5B            6030 C POP BX ; *2 FOR WORD OFFSET
212A 56            6031 C PUSH SI ; GET CURSOR POSITION
                    6032 C ; RESTORE
                    6033 C ; CURRENT VALUE ON STACK
212B 50            6033 C PUSH AX
212C 8B 0200      6034 C MOV AX,0200H
212F CD 10        6035 C INT 10H ; SET THE CURSOR POSITION
2131 58            6036 C POP AX
2132                6037 C STR_1:
2132 51            6038 C PUSH CX
2133 53            6039 C PUSH BX
2134 50            6040 C PUSH AX
2135 86 E0        6041 C XCHG AH,AL
2137 26: 8A 46 00 6042 C MOV AL,ES:[BP]
2138 45            6043 C INC BP ; GET THE CHAR TO WRITE
213A 3C 0D        6044 C CMP AL,0DH ; CARRIAGE RETURN
213E 74 3D        6045 C JE STR_CR_LF
2140 3C 0A        6046 C CMP AL,0AH ; LINE FEED
2142 74 39        6047 C JE STR_CR_LF
2144 3C 08        6048 C CMP AL,08H ; BACKSPACE

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2146 74 35 6049 JE STR_CR_LF
2148 3C 07 6050 CMP AL,07H ; BELL
214A 74 31 6051 JE STR_CR_LF
214C B9 0001 6052 MOV CX,1 ; COUNT OF CHARACTERS
214E 80 FC 02 6053 CMP AH,2 ; CHECK WHERE ATTR IS
2152 72 05 6054 JB DO_STR ; NOT IN THE STRING
2154 26: BA 5E 00 6055 MOV BL:[BP] ; GET THE ATTRIBUTE
2158 45 6056 INC BP ; NEXT ITEM IN STRING
2159 6057
2159 B4 09 6058 DO_STR: MOV AH,09H ; WRITE THE CHAR/ATTR
215B CD 10 6059 INT 10H
215D FE C2 6060 INC DL ; NEXT CURSOR POSITION
215F 3A 16 044A R 6061 CMP DL,BYTE PTR CRT_COLS ; COLUMN OVERFLOW
2163 72 11 6062 JB STR_2 ; NOT YET
2165 3A 36 048A R 6063 CMP DH,ROWS
2169 75 07 6064 JNE STR_3
216B B8 0E0A 6065 MOV AX,0E0AH
216E CD 10 6066 INT 10H
2170 FE CE 6067 DEC DH
2172 6068
2172 FE C6 6069 INC DH ; NEXT ROW
2174 2A D2 6070 SUB DL,DL ; COLUMN ZERO
2176 6071
2176 B8 0200 6072 MOV AX,0200H ; SET THE CURSOR
2179 CD 10 6073 INT 10H
217B EB 0E 6074 JMP SHORT STR_4
217D 6075
217D B4 0E 6076 STR_CR_LF: MOV AH,0EH
217F CD 10 6077 INT 10H
2181 BA DF 6078 MOV BL,BH ; GET PAGE TO LOW BYTE
2183 2A FF 6079 SUB BH,BH
2185 D1 E3 6080 BX,1 ; *2 FOR WORD OFFSET
2187 8B 97 0450 R 6081 MOV DX,[BX + OFFSET CURSOR_POS] ; GET CURSOR POSITION
2188 58 6082
2188 58 6083 POP AX
218C 5B 6084 POP BX
218D 59 6085 POP CX
218E E2 A2 6086 LOOP STR_1
2190 5A 6087
2190 5A 6088 POP DX ; RECOVER CURSOR POSITION
2191 3C 01 6089 ; FROM PUSH SI ABOVE
2193 74 09 6090 CMP AL,1
2195 3C 03 6091 JE STR_OUT
2197 74 05 6092 CMP AL,3
2199 B8 0200 6093 JE STR_OUT
219C CD 10 6094 MOV AX,0200H ; SET CURSOR POSITION
219E 6095
219E 6096 STR_OUT: INT 10H
219E 6097
219E 6098 ; ALLOW FALL THROUGH
219E 6099
219E 6100 V_RET PROC NEAR ; VIDEO BIOS RETURN
219E 6101 POP DI
219E 6102 POP SI
21A0 58 6103 POP BX
21A1 59 6104 POP CX
21A2 5A 6105 POP DX
21A3 1F 6106 POP DS
21A4 07 6107 POP ES
21A5 5D 6108 POP BP
21A6 CF 6109 IRET
21A7 6110
21A7 6111 V_RET ENDP
21A7 6112
21A7 6113 COMBO_VIDEO ENDP
21A7 6114
21A7 6115 C INCLUDE VPRSC.INC
21A7 6116 C SUBTTL VPRSC.INC
21A7 6117 C PAGE
21A7 6118 C
21A7 6119 C -----
21A7 6120 C INTERRUPT 5
21A7 6121 C THIS LOGIC WILL BE INVOKED BY INTERRUPT 05H TO PRINT THE
21A7 6122 C SCREEN. THE CURSOR POSITION AT THE TIME THIS ROUTINE IS INVOKED
21A7 6123 C WILL BE SAVED AND RESTORED UPON COMPLETION. THE ROUTINE IS
21A7 6124 C INTENDED TO RUN WITH INTERRUPTS ENABLED. IF A SUBSEQUENT
21A7 6125 C 'PRINT SCREEN' KEY IS DEPRESSED DURING THE TIME THIS ROUTINE
21A7 6126 C IS PRINTING IT WILL BE IGNORED.
21A7 6127 C ADDRESS 50:0 CONTAINS THE STATUS OF THE PRINT SCREEN:
21A7 6128 C
21A7 6129 C 50:0 =0 EITHER PRINT SCREEN HAS NOT BEEN CALLED
21A7 6130 C OR UPON RETURN FROM A CALL THIS INDICATES
21A7 6131 C A SUCCESSFUL OPERATION.
21A7 6132 C =1 PRINT SCREEN IS IN PROGRESS
21A7 6133 C =255 ERROR ENCOUNTERED DURING PRINTING
21A7 6134 C -----
21A7 6135 C PRINT_SCREEN PROC FAR
21A7 6136 C STI ; MUST RUN WITH INTS ENABLED
21A8 1E 6137 PUSH DS ; MUST USE 50:0 FOR DATA
21A9 50 6138 PUSH AX ; AREA STORAGE
21AA 53 6139 PUSH BX
21AB 51 6140 PUSH CX ; USE THIS LATER FOR CURSOR LIMITS
21AC 52 6141 PUSH DX ; WILL HOLD CURRENT CURSOR POS
21AD E8 0CFE R 6142 CALL DDS
21B0 80 3E 0500 R 01 6143 CMP STATUS_BYTE,1 ; SEE IF PRINT ALREADY IN PROGRESS
21B5 74 63 6144 JZ EXIT ; JUMP IF PRINT IN PROGRESS
21B7 C6 06 0500 R 01 6145 MOV STATUS_BYTE,1 ; INDICATE PRINT NOW IN PROGRESS
21BC B4 0F 6146 MOV AH,15 ; WILL REQUEST THE CURRENT MODE
21BE CD 10 6147 INT 10H ; [AL]=MODE (NOT USED)
21BE 6148 ; [AH]=NUMBER COLUMNS/LINE
21BE 6149 ; [BH]=VISUAL PAGE
21BE 6150 C -----
21BE 6151 C AT THIS POINT WE KNOW THE COLUMNS/LINE ARE IN
21BE 6152 C [AX] AND THE PAGE IF APPLICABLE IS IN [BH]. THE STACK
21BE 6153 C HAS DS,AX,BX,CX,DX PUSHED. [AL] HAS VIDEO MODE
21BE 6154 C
21BE 6155 C MOV CL,AH ; WILL MAKE USE OF [CX] REG TO
21BE 6156 C MOV CH,ROWS ; CONTROL ROW & COLUMNS
21BE 6157 C INC CH ; ADJUST
21BE 6158 C CALL CRLF ; CAR RETURN LINE FEED ROUTINE
21BE 6159 C PUSH CX ; SAVE SCREEN BOUNDS
21BE 6160 C MOV AH,3 ; WILL NOW READ THE CURSOR.
21BE 6161 C INT 10H ; AND PRESERVE THE POSITION
21BE 6162 C POP CX ; RECALL SCREEN BOUNDS
21BE 6163 C PUSH DX ; RECALL [BH]=VISUAL PAGE
21BE 6164 C XOR DX,DX ; SET CURSOR POSITION TO {0,0}
21BE 6165 C -----
21BE 6166 C THE LOOP FROM PR110 TO THE INSTRUCTION PRIOR TO PR120
21BE 6167 C IS THE LOOP TO READ EACH CURSOR POSITION FROM THE
21BE 6168 C SCREEN AND PRINT.
21BE 6169 C
21BE 6170 C PR110: MOV AH,2 ; TO INDICATE CURSOR SET REQUEST
21BE 6171 C INT 10H ; NEW CURSOR POS ESTABLISHED
21BE 6172 C MOV AH,8 ; TO INDICATE READ CHARACTER
21BE 6173 C INT 10H ; CHARACTER NOW IN [AL]
21BE 6174 C OR AL,AL ; SEE IF VALID CHAR

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0112	66 66	66	DB	000H,066H,066H,000H,000H,000H	BT_13
0118	00 00 66 66 00 00 00 00	68	DB	000H,000H,07FH,0DBH,0DBH,0DBH,07BH,01BH	TH_14
	7B 1B	69			
0120	1B 1B 1B 00 00 00 00 00	70	DB	01BH,01BH,01BH,000H,000H,000H	BT_14
0126	00 7C 6C 60 3B 6C	71	DB	000H,07CH,0C6H,060H,03BH,06CH,0C6H,0C6H	TH_15
	C6 C6	72			
012E	6C 3B 0C C6 7C 00 73	DB	06CH,03BH,00CH,0C6H,07CH,000H	BT_15	
0134	00 00 00 00 00 00 00 00	74	DB	000H,000H,000H,000H,000H,000H,000H,000H	TH_16
	00 00	75			
013C	FE FE FE FE 00 00 00 76	DB	0FEH,0FEH,0FEH,000H,000H,000H	BT_16	
0142	00 00 1B 3C 7E 1B 77	DB	000H,000H,01BH,03CH,07EH,01BH,01BH,01BH	TH_17	
	1B 1B	78			
014A	7E 3C 1B 7E 00 00 79	DB	07EH,03CH,01BH,07EH,000H,000H	BT_17	
0150	00 00 1B 3C 7E 1B 80	DB	000H,000H,01BH,03CH,07EH,01BH,01BH,01BH	TH_18	
	1B 1B	81			
0158	1B 1B 1B 00 00 00 82	DB	01BH,01BH,01BH,000H,000H,000H	BT_18	
015E	00 00 1B 1B 1B 1B 83	DB	000H,000H,01BH,01BH,01BH,01BH,01BH,01BH	TH_19	
	1B 1B	84			
0166	7E 3C 1B 00 00 00 85	DB	07EH,03CH,01BH,000H,000H,000H	BT_19	
016C	00 00 00 00 1B 0C 86	DB	000H,000H,000H,000H,01BH,00CH,0FEH,00CH	TH_1A	
	FE 0C	87			
0174	1B 00 00 00 00 00 88	DB	01BH,000H,000H,000H,000H,000H	BT_1A	
017A	00 00 00 00 30 60 89	DB	000H,000H,000H,000H,030H,060H,0FEH,060H	TH_1B	
	FE 60	90			
0182	30 00 00 00 00 00 91	DB	030H,000H,000H,000H,000H,000H	BT_1B	
0188	00 00 00 00 00 00 92	DB	000H,000H,000H,000H,000H,0C0H,0C0H,0C0H	TH_1C	
	C0 C0	93			
0190	FE 00 00 00 00 00 94	DB	0FEH,000H,000H,000H,000H,000H	BT_1C	
0196	00 00 00 00 2B 6C 95	DB	000H,000H,000H,000H,02BH,06CH,0FEH,06CH	TH_1D	
	FE 6C	96			
019E	2B 00 00 00 00 00 97	DB	02BH,000H,000H,000H,000H,000H	BT_1D	
01A4	00 00 00 10 3B 3B 98	DB	000H,000H,000H,010H,03BH,03BH,07CH,07CH	TH_1E	
	7C 7C	99			
01AC	FE FE 00 00 00 00 100	DB	0FEH,0FEH,000H,000H,000H,000H	BT_1E	
01B2	00 00 00 FE FE 7C 101	DB	000H,000H,000H,0FEH,0FEH,07CH,07CH,03BH	TH_1F	
	7C 3B	102			
01BA	3B 10 00 00 00 00 103	DB	03BH,010H,000H,000H,000H,000H	BT_1F	
	00 00	104			
01C0	00 00 00 00 00 00 105	DB	000H,000H,000H,000H,000H,000H,000H,000H	TH_20 SP	
	00 00	106			
01C8	00 00 00 00 00 00 107	DB	000H,000H,000H,000H,000H,000H	BT_20 SP	
01CE	00 00 1B 3C 3C 3C 108	DB	000H,000H,01BH,03CH,03CH,03CH,01BH,01BH	TH_21 !	
	1B 1B	109			
01D6	00 1B 1B 00 00 00 110	DB	000H,01BH,01BH,000H,000H,000H	BT_21 !	
01DC	00 66 66 66 24 00 111	DB	000H,066H,066H,066H,024H,000H,000H,000H	TH_22 #	
	00 00	112			
01E4	00 00 00 00 00 00 113	DB	000H,000H,000H,000H,000H,000H	BT_22 #	
01EA	00 00 6C 6C FE 6C 114	DB	000H,000H,06CH,06CH,06CH,0FEH,06CH,06CH,06CH	TH_23 #	
	6C 6C	115			
01F2	FE 6C 6C 00 00 00 116	DB	0FEH,06CH,06CH,000H,000H,000H	BT_23 #	
01F8	1B 1B 7C C6 C2 C0 117	DB	01BH,01BH,07CH,0C6H,0C2H,0C0H,07CH,066H	TH_24 \$	
	7C 06	118			
0200	8C 6C 7C 1B 1B 00 119	DB	086H,0C6H,07CH,01BH,01BH,000H	BT_24 \$	
0206	00 00 00 00 C2 C6 120	DB	000H,000H,000H,000H,0C2H,0C6H,00CH,01BH	TH_25 *	
	0C 1B	121			
020E	30 66 6C 00 00 00 122	DB	030H,066H,0C6H,000H,000H,000H	BT_25 *	
0214	00 00 3B 6C 6C 3B 123	DB	000H,000H,03BH,06CH,06CH,03BH,076H,0DCH	TH_26 &	
	76 DC	124			
021C	CC CC 76 00 00 00 125	DB	0CCH,0CCH,076H,000H,000H,000H	BT_26 &	
0222	00 30 30 30 60 00 126	DB	000H,030H,030H,030H,060H,000H,000H,000H	TH_27 †	
	00 00	127			
022A	00 00 00 00 00 00 128	DB	000H,000H,000H,000H,000H,000H	BT_27 †	
0230	00 00 0C 1B 30 30 129	DB	000H,000H,00CH,01BH,030H,030H,030H,030H	TH_28 (	
	30 30	130			
0238	30 1B 0C 00 00 00 131	DB	030H,01BH,00CH,000H,000H,000H	BT_28 (	
023E	00 00 30 1B 0C 0C 132	DB	000H,000H,030H,01BH,00CH,00CH,00CH,00CH	TH_29 )	
	0C 0C	133			
0246	0C 1B 30 00 00 00 134	DB	00CH,01BH,030H,000H,000H,000H	BT_29 )	
024C	00 00 00 00 66 3C 135	DB	000H,000H,000H,000H,066H,03CH,0FFH,03CH	TH_2A *	
	FF 3C	136			
0254	66 00 00 00 00 00 137	DB	066H,000H,000H,000H,000H,000H	BT_2A *	
025A	00 00 00 00 1B 1B 138	DB	000H,000H,000H,000H,01BH,01BH,07EH,01BH	TH_2B +	
	7E 1B	139			
0262	1B 00 00 00 00 00 140	DB	01BH,000H,000H,000H,000H,000H	BT_2B +	
0268	00 00 00 00 00 00 141	DB	000H,000H,000H,000H,000H,000H,000H,000H	TH_2C ,	
	00 00	142			
0270	1B 1B 1B 30 00 00 143	DB	01BH,01BH,01BH,030H,000H,000H	BT_2C ,	
0276	00 00 00 00 00 00 144	DB	000H,000H,000H,000H,000H,000H,0FEH,000H	TH_2D -	
	FE 00	145			
027E	00 00 00 00 00 00 146	DB	000H,000H,000H,000H,000H,000H	BT_2D -	
0284	00 00 00 00 00 00 147	DB	000H,000H,000H,000H,000H,000H,000H,000H	TH_2E .	
	00 00	148			
028C	00 1B 1B 00 00 00 149	DB	000H,01BH,01BH,000H,000H,000H	BT_2E .	
0292	00 00 02 06 0C 1B 150	DB	000H,000H,002H,006H,00CH,01BH,030H,060H	TH_2F /	
	30 60	151			
029A	0C 80 00 00 00 00 152	DB	0C0H,080H,000H,000H,000H,000H	BT_2F /	
	00 00	153			
02A0	00 00 7C 6C C6 DE 154	DB	000H,000H,07CH,0C6H,0CEH,0DEH,0F6H,0E6H	TH_30 0	
	F6 E6	155			
02AB	C6 C6 7C 00 00 00 156	DB	0C6H,0C6H,07CH,000H,000H,000H	BT_30 0	
02AE	00 00 1B 3B 7B 1B 157	DB	000H,000H,01BH,03BH,07BH,01BH,01BH,01BH	TH_31 1	
	1B 1B	158			
02B6	1B 1B 7E 00 00 00 159	DB	01BH,01BH,07EH,000H,000H,000H	BT_31 1	
02BC	00 00 7C C6 06 0C 160	DB	000H,000H,07CH,0C6H,066H,00CH,01BH,030H	TH_32 2	
	1B 30	161			
02C4	60 C6 FE 00 00 00 162	DB	060H,0C6H,0FEH,000H,000H,000H	BT_32 2	
02CA	00 00 7C C6 06 06 163	DB	000H,000H,07CH,0C6H,066H,066H,03CH,066H	TH_33 3	
	3C 06	164			
02D2	06 C6 7C 00 00 00 165	DB	066H,0C6H,07CH,066H,000H,000H	BT_33 3	
02D8	00 00 0C 1C 3C 4C 166	DB	000H,000H,00CH,01CH,03CH,06CH,0CCH,0FEH	TH_34 4	
	CC FE	167			
02E0	0C 0C 1E 00 00 00 168	DB	00CH,00CH,01EH,000H,000H,000H	BT_34 4	
02E6	00 00 FE C0 C0 C0 169	DB	000H,000H,0FEH,0C0H,0C0H,0C0H,0FCH,066H	TH_35 5	
	FC 06	170			
02EE	06 C6 7C 00 00 00 171	DB	066H,0C6H,07CH,000H,000H,000H	BT_35 5	
02F4	00 00 1B 60 C0 C0 172	DB	000H,000H,03BH,060H,0C0H,0C0H,0FCH,0C6H	TH_36 6	
	FC C6	173			
02FC	C6 C6 7C 00 00 00 174	DB	0C6H,0C6H,07CH,000H,000H,000H	BT_36 6	
0302	00 00 FE C6 06 0C 175	DB	000H,000H,0FEH,0C6H,066H,00CH,01BH,030H	TH_37 7	
	1B 30	176			
030A	30 30 30 00 00 00 177	DB	030H,030H,030H,000H,000H,000H	BT_37 7	
0310	00 00 7C C6 C6 C6 178	DB	000H,000H,07CH,0C6H,0C6H,0C6H,07CH,0C6H	TH_38 8	
	7C C6	179			
0318	C6 C6 7C 00 00 00 180	DB	0C6H,0C6H,07CH,000H,000H,000H	BT_38 8	
031E	00 00 7C C6 C6 C6 181	DB	000H,000H,07CH,0C6H,0C6H,0C6H,07EH,066H	TH_39 9	
	7E 06	182			
0326	06 0C 7B 00 00 00 183	DB	066H,00CH,07BH,000H,000H,000H	BT_39 9	
032C	00 00 1B 1B 1B 00 184	DB	000H,000H,000H,01BH,01BH,000H,000H,000H	TH_3A :	
	00 00	185			
0334	1B 1B 00 00 00 00 186	DB	01BH,01BH,000H,000H,000H,000H	BT_3A :	
033A	00 00 00 1B 1B 00 187	DB	000H,000H,000H,01BH,01BH,000H,000H,000H	TH_3B :	
	00 00	188			
0342	1B 1B 30 00 00 00 189	DB	01BH,01BH,030H,000H,000H,000H	BT_3B :	
0348	00 00 06 0C 1B 30 190	DB	000H,000H,066H,00CH,01BH,030H,060H,030H	TH_3C <	
	60 30	191			

0350 18 0C 06 00 00 00 192  
 0356 00 00 00 00 00 7E 193  
 00 00 194  
 035E 7E 00 00 00 00 00 195  
 0364 00 00 60 30 18 0C 196  
 06 0C 197  
 036C 18 30 60 00 00 00 198  
 0372 00 00 7C 0C C6 0C 199  
 18 200  
 037A 00 18 10 00 00 00 201  
 202  
 0380 00 00 7C C6 C6 DE 203  
 204  
 0388 0C 00 7C 00 00 00 205  
 038E 00 00 10 38 6C C6 206  
 C6 CE FE 207  
 0396 C6 C6 C6 00 00 00 208  
 039C 00 00 FC 66 66 66 209  
 7C 66 210  
 03A4 66 66 FC 00 00 00 211  
 03AA 00 00 3C 66 C2 C0 212  
 C0 C0 213  
 03B2 C2 66 3C 00 00 00 214  
 03B8 00 00 F8 6C 66 66 215  
 66 66 216  
 03C0 66 6C F8 00 00 00 217  
 03C6 00 00 FE 66 62 68 218  
 78 68 219  
 03CE 62 66 FE 00 00 00 220  
 03D4 00 00 FE 66 62 68 221  
 78 68 222  
 03DC 60 60 F0 00 00 00 223  
 03E2 00 00 3C 66 C2 C0 224  
 C0 CE 225  
 03EA C6 66 3A 00 00 00 226  
 03FO 00 00 C6 C6 C6 C6 227  
 FE C6 228  
 03F8 C6 C6 C6 00 00 00 229  
 03FE 00 00 3C 18 18 18 230  
 18 18 231  
 0406 18 18 3C 00 00 00 232  
 040C 00 00 1E 0C 0C 0C 233  
 0C 0C 234  
 0414 CC 78 00 00 00 00 235  
 041A 00 00 E6 66 6C 6C 236  
 78 6C 237  
 0422 C6 66 E6 00 00 00 238  
 0428 00 00 F0 60 60 60 239  
 60 60 240  
 0430 62 66 FE 00 00 00 241  
 0436 00 00 CE EE FE 00 242  
 D6 C6 243  
 043E C6 C6 C6 00 00 00 244  
 0444 00 00 CE E6 FE FE 00 245  
 DE CE 246  
 044C C6 C6 C6 00 00 00 247  
 0452 00 00 38 6C C6 C6 248  
 C6 C6 249  
 045A C6 6C 38 00 00 00 250  
 251  
 0460 00 00 FC 66 66 66 252  
 7C 60 253  
 0468 60 60 F0 00 00 00 254  
 046E 00 00 7C C6 C6 C6 255  
 C6 DE 256  
 047E DE 7C 0C 0E 00 00 257  
 047C 00 00 FC 66 66 66 258  
 7C 66 259  
 0484 66 66 E6 00 00 00 260  
 048A 00 00 7C C6 C6 60 261  
 38 0C 262  
 0492 C6 C6 7C 00 00 00 263  
 0498 00 00 7E 7E 5A 18 264  
 18 18 265  
 04A0 18 18 3C 00 00 00 266  
 04A6 00 00 C6 C6 C6 C6 267  
 C6 C6 268  
 04AE C6 C6 7C 00 00 00 269  
 04B4 00 00 C6 C6 C6 C6 270  
 C6 C6 271  
 04BC 6C 38 10 00 00 00 272  
 04C2 00 00 C6 C6 C6 C6 273  
 D6 D6 274  
 04CA FE 7C 6C 00 00 00 275  
 04D0 00 00 C6 C6 C6 38 276  
 38 38 277  
 04D8 6C C6 C6 00 00 00 278  
 04DE 00 00 66 66 66 66 279  
 3C 18 280  
 04E6 18 18 3C 00 00 00 281  
 04EC 00 00 FE C6 8C 18 282  
 30 60 283  
 04FA C2 C6 FE 00 00 00 284  
 00 00 3C 30 30 30 285  
 30 30 286  
 0502 30 30 3C 00 00 00 287  
 0508 00 00 80 C0 E0 70 288  
 38 1C 289  
 0510 0E 06 02 00 00 00 290  
 0516 00 00 3C 0C 0C 0C 291  
 0C 0C 292  
 051E 0C 0C 3C 00 00 00 293  
 0524 10 38 6C C6 00 00 294  
 00 00 295  
 052C 00 00 00 00 00 00 296  
 0532 00 00 00 00 00 00 297  
 00 00 298  
 053A 00 00 00 00 FF 00 299  
 30 00 300  
 0540 30 30 18 00 00 00 301  
 00 00 302  
 0548 00 00 00 00 00 00 303  
 054E 00 00 00 00 00 78 304  
 0C 7C 305  
 0556 CC CC 76 00 00 00 306  
 055C 00 00 E0 60 70 307  
 6C 66 308  
 0564 66 66 7C 00 00 00 309  
 056A 00 00 00 00 00 7C 310  
 C6 C0 311  
 0572 C0 C6 7C 00 00 00 312  
 0578 00 00 1C 0C 0C 3C 313  
 6C CC 314  
 0580 CC CC 76 00 00 00 315  
 0586 00 00 00 00 00 7C 316  
 C6 FE 317

DB 018H,00CH,066H,000H,000H,000H : BT\_3C <  
 DB 000H,000H,000H,000H,000H,07EH,000H,000H : TH\_3D =  
 DB 07EH,000H,000H,000H,000H,000H : BT\_3D =  
 DB 000H,060H,060H,030H,018H,00CH,006H,00CH : TH\_3E >  
 DB 018H,030H,060H,000H,000H,000H : BT\_3E >  
 DB 000H,000H,07CH,0C6H,0C6H,00C6H,00C6H,018H,018H : TH\_3F ?  
 DB 000H,018H,018H,000H,000H,000H : BT\_3F ?  
 DB 000H,000H,07CH,0C6H,0C6H,0C6H,0DEH,0DEH,0DEH : TH\_40 #  
 DB 00CH,0C0H,07CH,000H,000H,000H : BT\_40 #  
 DB 000H,000H,010H,038H,06CH,0C6H,0C6H,0FEH : TH\_41 A  
 DB 0C6H,0C6H,0C6H,000H,000H,000H : BT\_41 A  
 DB 000H,000H,07CH,066H,066H,066H,07CH,066H : TH\_42 B  
 DB 066H,066H,0FCH,000H,000H,000H : BT\_42 B  
 DB 000H,000H,03CH,066H,0C2H,0C0H,0C0H,0C0H : TH\_43 C  
 DB 0C2H,066H,03CH,000H,000H,000H : BT\_43 C  
 DB 000H,000H,0F8H,0C6H,066H,066H,066H,066H : TH\_44 D  
 DB 066H,066H,0F8H,000H,000H,000H : BT\_44 D  
 DB 000H,000H,0FEH,066H,062H,066H,078H,066H : TH\_45 E  
 DB 062H,066H,0FEH,000H,000H,000H : BT\_45 E  
 DB 000H,000H,0FEH,066H,062H,066H,078H,066H : TH\_46 F  
 DB 060H,060H,0F0H,000H,000H,000H : BT\_46 F  
 DB 000H,000H,03CH,066H,0C2H,0C0H,0C0H,0DEH : TH\_47 G  
 DB 0C6H,066H,03AH,000H,000H,000H : BT\_47 G  
 DB 000H,000H,0C6H,0C6H,0C6H,0C6H,0FEH,0C6H : TH\_48 H  
 DB 0C6H,0C6H,0C6H,000H,000H,000H : BT\_48 H  
 DB 000H,000H,03CH,018H,018H,018H,018H,018H : TH\_49 I  
 DB 018H,018H,03CH,000H,000H,000H : BT\_49 I  
 DB 000H,000H,01EH,00CH,00CH,00CH,00CH,00CH : TH\_4A J  
 DB 0CCH,0CCH,078H,000H,000H,000H : BT\_4A J  
 DB 000H,000H,066H,066H,06CH,06CH,078H,06CH : TH\_4B K  
 DB 06CH,066H,0E6H,000H,000H,000H : BT\_4B K  
 DB 000H,000H,0F0H,060H,060H,060H,060H,060H : TH\_4C L  
 DB 062H,066H,0FEH,000H,000H,000H : BT\_4C L  
 DB 000H,000H,0C6H,0E6H,0FEH,0FEH,0D6H,0C6H : TH\_4D M  
 DB 0C6H,0C6H,0C6H,000H,000H,000H : BT\_4D M  
 DB 000H,000H,0C6H,0E6H,0FEH,0FEH,0DEH,0CEH : TH\_4E N  
 DB 0C6H,0C6H,0C6H,000H,000H,000H : BT\_4E N  
 DB 000H,000H,038H,0C6H,0C6H,0C6H,0C6H,0C6H : TH\_4F O  
 DB 0C6H,06CH,038H,000H,000H,000H : BT\_4F O  
 DB 000H,000H,0FCH,066H,066H,066H,07CH,060H : TH\_50 P  
 DB 060H,060H,0F0H,000H,000H,000H : BT\_50 P  
 DB 000H,000H,07CH,0C6H,0C6H,0C6H,0C6H,0D6H : TH\_51 Q  
 DB 0DEH,07CH,00CH,00EH,000H,000H : BT\_51 Q  
 DB 000H,000H,0FCH,066H,066H,066H,07CH,06CH : TH\_52 R  
 DB 066H,066H,0E6H,000H,000H,000H : BT\_52 R  
 DB 000H,000H,07CH,0C6H,0C6H,066H,060H,038H,00CH : TH\_53 S  
 DB 0C6H,0C6H,07CH,000H,000H,000H : BT\_53 S  
 DB 000H,000H,07EH,07EH,05AH,018H,018H,018H : TH\_54 T  
 DB 018H,018H,03CH,000H,000H,000H : BT\_54 T  
 DB 000H,000H,0C6H,0C6H,0C6H,0C6H,0C6H,0C6H : TH\_55 U  
 DB 0C6H,0C6H,07CH,000H,000H,000H : BT\_55 U  
 DB 000H,000H,0C6H,0C6H,0C6H,0C6H,0C6H,0C6H : TH\_56 V  
 DB 06CH,038H,010H,000H,000H,000H : BT\_56 V  
 DB 000H,000H,0C6H,0C6H,0C6H,0C6H,0D6H,0D6H : TH\_57 W  
 DB 0FEH,07CH,0C6H,000H,000H,000H : BT\_57 W  
 DB 000H,000H,0C6H,0C6H,06CH,038H,038H,038H : TH\_58 X  
 DB 06CH,0C6H,0C6H,000H,000H,000H : BT\_58 X  
 DB 000H,000H,066H,066H,066H,066H,03CH,018H : TH\_59 Y  
 DB 018H,018H,03CH,000H,000H,000H : BT\_59 Y  
 DB 000H,000H,0FEH,0C6H,08CH,018H,030H,060H : TH\_5A Z  
 DB 0C2H,0C6H,0FEH,000H,000H,000H : BT\_5A Z  
 DB 000H,000H,03CH,030H,030H,030H,030H,030H : TH\_5B [  
 DB 030H,030H,03CH,000H,000H,000H : BT\_5B [  
 DB 000H,000H,080H,0C0H,0E0H,070H,038H,01CH : TH\_5C ]  
 DB 00EH,006H,002H,000H,000H,000H : BT\_5C ]  
 DB 000H,000H,03CH,00CH,00CH,00CH,00CH,00CH : TH\_5D ]  
 DB 00CH,00CH,03CH,000H,000H,000H : BT\_5D ]  
 DB 010H,038H,06CH,0C6H,0C6H,000H,000H,000H : TH\_5E ]  
 DB 000H,000H,000H,000H,000H,000H : BT\_5E ]  
 DB 000H,000H,000H,000H,000H,000H,000H : TH\_5F \_  
 DB 000H,000H,000H,000H,0FFH,000H : BT\_5F \_  
 DB 030H,030H,018H,000H,000H,000H,000H,000H : TH\_60 ' 1  
 DB 000H,000H,000H,000H,000H,000H : BT\_60 ' 1  
 DB 000H,000H,000H,000H,000H,078H,00CH,07CH : TH\_61 LOWER\_CASE A  
 DB 0CCH,0CCH,076H,000H,000H,000H : BT\_61 LOWER\_CASE A  
 DB 000H,000H,0E0H,060H,060H,078H,06CH,066H : TH\_62 L.C. B  
 DB 066H,066H,07CH,000H,000H,000H : BT\_62 L.C. B  
 DB 000H,000H,000H,000H,000H,07CH,0C6H,0C0H : TH\_63 L.C. C  
 DB 0C0H,0C6H,07CH,000H,000H,000H : BT\_63 L.C. C  
 DB 000H,000H,01CH,00CH,00CH,03CH,06CH,0CCH : TH\_64 L.C. D  
 DB 0CCH,0CCH,076H,000H,000H,000H : BT\_64 L.C. D  
 DB 000H,000H,000H,000H,000H,07CH,0C6H,0FEH : TH\_65 L.C. E

058E	00	C6	7C	00	00	00	318	DB	0C0H,0C6H,07CH,000H,000H,000H ; BT_65 L.C. E
0594	00	00	38	6C	64	60	319	DB	000H,000H,03BH,06CH,064H,060H,0F0H,060H ; TH_66 L.C. F
							320		
059C	60	60	F0	00	00	00	321	DB	060H,060H,0F0H,000H,000H,000H ; BT_66 L.C. F
05A2	00	00	00	00	00	76	322	DB	000H,000H,000H,000H,000H,076H,0CCH,0CCH ; TH_67 L.C. G
							323		
05AA	CC	7C	0C	78	00	00	324	DB	0CCH,07CH,00CH,0CCH,078H,000H ; BT_67 L.C. G
05B0	00	00	E0	60	60	6C	325	DB	000H,000H,0E0H,060H,060H,0C6H,076H,066H ; TH_68 L.C. H
							326		
05B8	66	66	E6	00	00	00	327	DB	066H,066H,0E6H,000H,000H,000H ; BT_68 L.C. H
05BE	00	00	18	18	00	38	328	DB	000H,000H,018H,018H,000H,038H,018H,018H ; TH_69 L.C. I
							329		
05C6	18	18	3C	00	00	00	330	DB	018H,018H,03CH,000H,000H,000H ; BT_69 L.C. I
05CC	00	00	0C	00	00	0E	331	DB	000H,000H,006H,006H,000H,00E0H,006H,006H ; TH_6A L.C. J
							332		
05D4	06	06	66	66	3C	00	333	DB	006H,006H,066H,066H,03CH,000H ; BT_6A L.C. J
05DA	00	00	E0	60	60	66	334	DB	000H,000H,0E0H,060H,060H,066H,06CH,078H ; TH_6B L.C. K
							335		
05E2	66	66	E6	00	00	00	336	DB	066H,066H,0E6H,000H,000H,000H ; BT_6B L.C. K
05E8	00	00	38	18	18	18	337	DB	000H,000H,038H,018H,018H,018H,018H ; TH_6C L.C. L
							338		
05F0	18	18	3C	00	00	00	339	DB	018H,018H,03CH,000H,000H,000H ; BT_6C L.C. L
05F6	00	00	00	00	00	0E	340	DB	000H,000H,000H,000H,000H,0E0H,0F0H,0D6H ; TH_6D L.C. M
							341		
05FE	D6	D6	C6	00	00	00	342	DB	0D6H,0D6H,0C6H,000H,000H,000H ; BT_6D L.C. M
0604	00	00	00	00	00	DC	343	DB	000H,000H,000H,000H,000H,0DCH,066H,066H ; TH_6E L.C. N
							344		
060C	66	66	66	00	00	00	345	DB	066H,066H,066H,000H,000H,000H ; BT_6E L.C. N
0612	00	00	00	00	00	7C	346	DB	000H,000H,000H,000H,000H,07CH,0C6H,0C6H ; TH_6F L.C. O
							347		
061A	C6	C6	7C	00	00	00	348	DB	0C6H,0C6H,07CH,000H,000H,000H ; BT_6F L.C. O
							349		
0620	00	00	00	00	00	DC	350	DB	000H,000H,000H,000H,000H,0DCH,066H,066H ; TH_70 L.C. P
							351		
0628	66	7C	60	60	F0	00	352	DB	066H,07CH,060H,060H,0F0H,000H ; BT_70 L.C. P
062E	00	00	00	00	00	76	353	DB	000H,000H,000H,000H,000H,076H,0CCH,0CCH ; TH_71 L.C. Q
							354		
0636	CC	7C	0C	0E	1E	00	355	DB	0CCH,07CH,00CH,00CH,01EH,000H ; BT_71 L.C. Q
063C	00	00	00	00	00	DC	356	DB	000H,000H,000H,000H,000H,0DCH,076H,066H ; TH_72 L.C. R
							357		
0644	60	60	F0	00	00	00	358	DB	060H,060H,0F0H,000H,000H,000H ; BT_72 L.C. R
064A	00	00	00	00	00	7C	359	DB	000H,000H,000H,000H,000H,07CH,0C6H,070H ; TH_73 L.C. S
							360		
0652	1C	C6	7C	00	00	00	361	DB	01CH,0C6H,07CH,000H,000H,000H ; BT_73 L.C. S
0658	00	00	10	30	30	FC	362	DB	000H,000H,010H,030H,030H,0FCH,030H,030H ; TH_74 L.C. T
							363		
0660	30	36	1C	00	00	00	364	DB	030H,036H,01CH,000H,000H,000H ; BT_74 L.C. T
0666	00	00	00	00	00	CC	365	DB	000H,000H,000H,000H,000H,0CCH,0CCH,0CCH ; TH_75 L.C. U
							366		
066E	CC	CC	76	00	00	00	367	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_75 L.C. U
0674	00	00	00	00	00	66	368	DB	000H,000H,000H,000H,000H,066H,066H,066H ; TH_76 L.C. V
							369		
067C	66	6C	18	00	00	00	370	DB	066H,03CH,018H,000H,000H,000H ; BT_76 L.C. V
0682	00	00	00	00	00	C6	371	DB	000H,000H,000H,000H,000H,0C6H,0C6H ; TH_77 L.C. W
							372		
068A	D6	FE	6C	00	00	00	373	DB	0D6H,0FEH,06CH,000H,000H,000H ; BT_77 L.C. W
0690	00	00	00	00	00	C6	374	DB	000H,000H,000H,000H,000H,0C6H,06CH,038H ; TH_78 L.C. X
							375		
0698	38	6C	C6	00	00	00	376	DB	038H,06CH,0C6H,000H,000H,000H ; BT_78 L.C. X
069E	00	00	00	00	00	C6	377	DB	000H,000H,000H,000H,000H,0C6H,0C6H,0C6H ; TH_79 L.C. Y
							378		
06A6	C6	7E	06	0C	F8	00	379	DB	0C6H,07EH,006H,00CH,0F8H,000H ; BT_79 L.C. Y
06AC	00	00	00	00	00	FE	380	DB	000H,000H,000H,000H,000H,0FEH,0CCH,018H ; TH_7A L.C. Z
							381		
06B4	30	66	FE	00	00	00	382	DB	030H,066H,0FEH,000H,000H,000H ; BT_7A L.C. Z
06BA	00	00	0E	18	18	18	383	DB	000H,000H,00EH,018H,018H,018H,070H,018H ; TH_7B L.BRACK
							384		
06C2	18	18	0E	00	00	00	385	DB	018H,018H,00EH,000H,000H,000H ; BT_7B L.BRACK
06C8	00	00	18	18	18	18	386	DB	000H,000H,018H,018H,018H,018H,000H,018H ; TH_7C
							387		
06D0	18	18	18	00	00	00	388	DB	018H,018H,018H,000H,000H,000H ; BT_7C
06D6	00	00	70	18	18	18	389	DB	000H,000H,070H,018H,018H,018H,00EH,018H ; TH_7D R BRACK
							390		
06DE	18	18	70	00	00	00	391	DB	018H,018H,070H,000H,000H,000H ; BT_7D R BRACK
06E4	00	00	76	DC	00	00	392	DB	000H,000H,076H,0DCH,000H,000H,000H,000H ; TH_7E TILDE
							393		
06EC	00	00	00	00	00	00	394	DB	000H,000H,000H,000H,000H,000H ; BT_7E TILDE
06F2	00	00	00	00	10	38	395	DB	000H,000H,000H,000H,010H,038H,06CH,0C6H ; TH_7F DELTA
							396		
06FA	C6	FE	00	00	00	00	397	DB	0C6H,0FEH,000H,000H,000H,000H ; BT_7F DELTA
							398		
0700	00	00	3C	66	C2	C0	399	DB	000H,000H,03CH,066H,0C2H,0C0H,0C0H,0C2H ; TH_80
							400		
0708	66	3C	0C	06	7C	00	401	DB	066H,03CH,00CH,006H,07CH,000H ; BT_80
070E	00	00	CC	CC	00	00	402	DB	000H,000H,0CCH,0CCH,000H,0CCH,0CCH,0CCH ; TH_81
							403		
0716	CC	CC	76	00	00	00	404	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_81
071C	00	0C	18	30	00	7C	405	DB	000H,00CH,018H,030H,000H,07CH,0C6H,0FEH ; TH_82
							406		
0724	0C	C6	7C	00	00	00	407	DB	0C0H,0C6H,07CH,000H,000H,000H ; BT_82
072A	00	10	38	6C	00	78	408	DB	000H,010H,038H,06CH,000H,078H,00CH,07CH ; TH_83
							409		
0732	CC	CC	76	00	00	00	410	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_83
0738	00	00	CC	CC	00	78	411	DB	000H,000H,0CCH,0CCH,000H,078H,00CH,07CH ; TH_84
							412		
0740	CC	CC	76	00	00	00	413	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_84
0746	00	60	30	18	00	78	414	DB	000H,060H,030H,018H,000H,078H,00CH,07CH ; TH_85
							415		
074E	CC	CC	76	00	00	00	416	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_85
0754	00	38	6C	38	00	78	417	DB	000H,038H,06CH,038H,000H,078H,00CH,07CH ; TH_86
							418		
075C	CC	CC	76	00	00	00	419	DB	0CCH,0CCH,076H,000H,000H,000H ; BT_86
0762	00	00	00	00	3C	66	420	DB	000H,000H,000H,000H,03CH,066H,060H,066H ; TH_87
							421		
076A	3C	0C	06	3C	00	00	422	DB	03CH,00CH,006H,03CH,000H,000H ; BT_87
0770	00	10	38	6C	00	7C	423	DB	000H,010H,038H,06CH,000H,07CH,0C6H,0FEH ; TH_88
							424		
0778	C6	FE	7C	00	00	00	425	DB	0C0H,0C6H,07CH,000H,000H,000H ; BT_88
077E	00	00	CC	CC	00	7C	426	DB	000H,000H,0CCH,0CCH,000H,07CH,0C6H,0FEH ; TH_89
							427		
0786	C6	FE	7C	00	00	00	427	DB	0C0H,0C6H,07CH,000H,000H,000H ; BT_89
078C	00	C6	70	18	00	7C	429	DB	000H,060H,030H,018H,000H,07CH,0C6H,0FEH ; TH_8A
							430		
0794	C6	FE	7C	00	00	00	431	DB	0C0H,0C6H,07CH,000H,000H,000H ; BT_8A
079A	00	00	66	66	00	38	432	DB	000H,000H,066H,066H,000H,038H,018H,018H ; TH_8B
							433		
07A2	18	18	3C	00	00	00	433	DB	018H,018H,03CH,000H,000H,000H ; BT_8B
07A8	00	18	3C	66	00	38	435	DB	000H,018H,03CH,066H,000H,038H,018H,018H ; TH_8C
							436		
07B0	18	18	3C	00	00	00	436	DB	018H,018H,03CH,000H,000H,000H ; BT_8C
07B6	00	60	30	18	00	38	438	DB	000H,060H,030H,018H,000H,038H,018H,018H ; TH_8D
							439		
07BE	18	18	3C	00	00	00	440	DB	018H,018H,03CH,000H,000H,000H ; BT_8D
07C4	00	C6	C6	10	38	6C	441	DB	000H,0C6H,0C6H,010H,038H,06CH,0C6H,0C6H ; TH_8E
							442		
07CC	FE	C6	C6	00	00	00	443	DB	0FEH,0C6H,0C6H,000H,000H,000H ; BT_8E

0702	38	6C	38	00	38	6C	444	DB	038H,06CH,038H,000H,038H,06CH,0C6H,0C6H,0C6H	; TH_8F
							445			
070A	FE	C6	C6	00	00	00	446	DB	0FEH,0C6H,0C6H,000H,000H,000H	; BT_8F
							447			
07E0	18	30	60	00	FE	66	448	DB	018H,030H,060H,000H,0FEH,066H,060H,07CH	; TH_90
							449			
07E8	60	66	FE	00	00	00	450	DB	060H,066H,0FEH,000H,000H,000H	; BT_90
07EE	00	00	00	00	00	7E	451	DB	000H,000H,000H,000H,0CCH,076H,036H,07EH	; TH_91
							452			
07F6	D8	D8	6E	00	00	00	453	DB	0D8H,0D8H,06EH,000H,000H,000H	; BT_91
07FC	00	00	3E	6C	CC	CC	454	DB	000H,000H,03EH,06CH,0C6H,0CCH,0CCH,0FEH,0CCH	; TH_92
							455			
0804	CC	CC	CC	00	00	00	456	DB	0CCH,0CCH,0CEH,000H,000H,000H	; BT_92
080A	00	10	38	6C	00	7C	457	DB	000H,010H,038H,06CH,0C6H,000H,07CH,0C6H,0C6H	; TH_93
							458			
0812	C6	C6	7C	00	00	00	459	DB	0C6H,0C6H,07CH,000H,000H,000H	; BT_93
0818	00	00	C6	C6	00	7C	460	DB	000H,000H,0C6H,0C6H,000H,07CH,0C6H,0C6H	; TH_94
							461			
0820	C6	C6	7C	00	00	00	462	DB	0C6H,0C6H,07CH,000H,000H,000H	; BT_94
0826	00	60	30	18	00	7C	463	DB	000H,060H,030H,018H,000H,07CH,0C6H,0C6H	; TH_95
							464			
082E	C6	C6	7C	00	00	00	465	DB	0C6H,0C6H,07CH,000H,000H,000H	; BT_95
0834	00	30	78	CC	00	CC	466	DB	000H,030H,078H,0CCH,000H,0CCH,0CCH,0CCH	; TH_96
							467			
083C	CC	CC	7E	00	00	00	468	DB	0CCH,0CCH,076H,000H,000H,000H	; BT_96
0842	00	60	30	18	00	CC	469	DB	000H,060H,030H,018H,000H,0CCH,0CCH,0CCH	; TH_97
							470			
084A	CC	CC	7E	00	00	00	471	DB	0CCH,0CCH,076H,000H,000H,000H	; BT_97
0850	00	00	C6	C6	00	C6	472	DB	000H,000H,0C6H,0C6H,000H,0C6H,0C6H	; TH_98
							473			
0858	C6	C6	7E	06	0C	78	474	DB	0C6H,07EH,006H,00CH,078H,000H	; BT_98
085E	00	C6	C6	38	6C	C6	475	DB	000H,0C6H,0C6H,038H,06CH,0C6H,0C6H	; TH_99
							476			
0866	C6	C6	38	00	00	00	477	DB	0C6H,06CH,038H,000H,000H,000H	; BT_99
086C	00	C6	C6	00	C6	C6	478	DB	000H,0C6H,0C6H,000H,0C6H,0C6H,0C6H	; TH_9A
							479			
0874	CC	CC	7C	00	00	00	480	DB	0C6H,0C6H,07CH,000H,000H,000H	; BT_9A
087A	00	18	18	3C	66	60	481	DB	000H,018H,018H,03CH,066H,060H,060H,066H	; TH_9B
							482			
0882	3C	18	18	00	00	00	483	DB	03CH,018H,018H,000H,000H,000H	; BT_9B
0888	00	38	6C	64	60	F0	484	DB	000H,038H,06CH,064H,060H,0F0H,060H,060H	; TH_9C
							485			
0890	60	E6	FC	00	00	00	486	DB	060H,0E6H,0FCH,000H,000H,000H	; BT_9C
0896	00	00	66	66	3C	18	487	DB	000H,000H,066H,066H,03CH,018H,07EH,018H	; TH_9D
							488			
089E	7E	18	18	00	00	00	489	DB	07EH,018H,018H,000H,000H,000H	; BT_9D
08A4	00	F8	CC	CC	F8	C8	490	DB	000H,0F8H,0CCH,0CCH,0F8H,0C4H,0CCH,0DEH	; TH_9E
							491			
08AC	CC	CC	C6	00	00	00	492	DB	0CCH,0CCH,0C6H,000H,000H,000H	; BT_9E
08B2	00	0E	18	18	18	18	493	DB	000H,00EH,018H,018H,018H,07EH,018H	; TH_9F
							494			
08BA	18	18	18	D8	70	00	495	DB	018H,018H,018H,0D8H,070H,000H	; BT_9F
							496			
08C0	00	18	30	60	00	78	497	DB	000H,018H,030H,060H,000H,078H,00CH,07CH	; TH_A0
							498			
08C8	CC	CC	7E	00	00	00	499	DB	0CCH,0CCH,076H,000H,000H,000H	; BT_A0
08CE	00	0C	18	30	00	38	500	DB	000H,00CH,018H,030H,000H,038H,018H,018H	; TH_A1
							501			
08D6	6E	18	3C	00	00	00	502	DB	018H,018H,03CH,000H,000H,000H	; BT_A1
08DC	00	18	30	60	00	7C	503	DB	000H,018H,030H,060H,000H,07CH,0C6H,0C6H	; TH_A2
							504			
08E4	C6	C6	7C	00	00	00	505	DB	0C6H,0C6H,07CH,000H,000H,000H	; BT_A2
08EA	00	18	30	60	00	CC	506	DB	000H,018H,030H,060H,000H,0CCH,0CCH,0CCH	; TH_A3
							507			
08F2	CC	CC	7E	00	00	00	508	DB	0CCH,0CCH,076H,000H,000H,000H	; BT_A3
08F8	00	00	7E	DC	00	00	509	DB	000H,000H,07EH,0DC,000H,0CCH,066H,066H	; TH_A4
							510			
0900	66	66	66	00	00	00	511	DB	066H,066H,066H,000H,000H,000H	; BT_A4
0906	7E	DC	00	C6	E6	F6	512	DB	076H,0DC,000H,0C6H,060H,0F6H,0FEH,0DEH	; TH_A5
							513			
090E	CE	C6	C6	00	00	00	514	DB	0CEH,0C6H,0C6H,000H,000H,000H	; BT_A5
0914	00	3C	6C	00	3E	00	515	DB	000H,03CH,06CH,06CH,03EH,000H,07EH,000H	; TH_A6
							516			
091C	00	00	00	00	00	00	517	DB	000H,000H,000H,000H,000H,000H	; BT_A6
0922	00	38	6C	38	00	00	518	DB	000H,038H,06CH,06CH,038H,000H,07CH,000H	; TH_A7
							519			
092A	00	00	00	00	00	00	520	DB	000H,000H,000H,000H,000H,000H	; BT_A7
0930	00	00	30	30	00	30	521	DB	000H,000H,030H,030H,000H,030H,030H,060H	; TH_A8
							522			
0938	6C	C6	7C	00	00	00	523	DB	0C6H,0C6H,07CH,000H,000H,000H	; BT_A8
093E	00	00	00	00	00	00	524	DB	000H,000H,000H,000H,000H,0FEH,0C0H	; TH_A9
							525			
0946	00	C0	00	00	00	00	526	DB	0C0H,0C0H,000H,000H,000H,000H	; BT_A9
094C	00	00	00	00	00	00	527	DB	000H,000H,000H,000H,000H,0FEH,006H	; TH_AA
							528			
0954	06	06	00	00	00	00	529	DB	006H,006H,000H,000H,000H,000H	; BT_AA
095A	00	C0	C0	C6	CC	D8	530	DB	000H,0C0H,0C0H,0C6H,0CCH,0D8H,030H,060H	; TH_AB
							531			
0962	DC	8C	06	18	3E	00	532	DB	0DCH,086H,00CH,018H,03EH,000H	; BT_AB
0968	00	C0	C0	C6	CC	D8	533	DB	000H,0C0H,0C0H,0C6H,0CCH,0D8H,030H,066H	; TH_AC
							534			
0970	CE	9E	3E	06	06	00	535	DB	0CEH,09EH,03EH,006H,006H,000H	; BT_AC
0976	00	18	18	00	18	18	536	DB	000H,000H,018H,018H,000H,018H,018H,03CH	; TH_AD
							537			
097E	3C	3C	18	00	00	00	538	DB	03CH,03CH,018H,000H,000H,000H	; BT_AD
0984	00	00	00	00	36	6C	539	DB	000H,000H,000H,000H,036H,06CH,0D8H,06CH	; TH_AE
							540			
098C	36	00	00	00	00	00	541	DB	036H,000H,000H,000H,000H,000H	; BT_AE
0992	00	00	00	00	D8	6C	542	DB	000H,000H,000H,000H,0D8H,06CH,036H,06CH	; TH_AF
							543			
099A	D8	00	00	00	00	00	544	DB	0D8H,000H,000H,000H,000H,000H	; BT_AF
							545			
09A0	11	44	11	44	11	44	546	DB	011H,044H,011H,044H,011H,044H,011H,044H	; TH_B0
							547			
09A8	11	44	11	44	11	44	548	DB	011H,044H,011H,044H,011H,044H	; BT_B0
09AE	55	AA	55	AA	55	AA	549	DB	055H,0AAH,055H,0AAH,055H,0AAH,055H,0AAH	; TH_B1
							550			
09B6	55	AA	55	AA	55	AA	551	DB	055H,0AAH,055H,0AAH,055H,0AAH	; BT_B1
09BC	DD	77	DD	77	DD	77	552	DB	0DDH,077H,0DDH,077H,0DDH,077H,0DDH,077H	; TH_B2
							553			
09C4	DD	77	DD	77	DD	77	554	DB	0DDH,077H,0DDH,077H,0DDH,077H	; BT_B2
09CA	18	18	18	18	18	18	555	DB	018H,018H,018H,018H,018H,018H,018H,018H	; TH_B3
							556			
09D2	18	18	18	18	18	18	557	DB	018H,018H,018H,018H,018H,018H	; BT_B3
09D8	18	18	18	18	18	18	558	DB	018H,018H,018H,018H,018H,018H,0F8H	; TH_B4
							559			
09E0	18	18	18	18	18	18	560	DB	018H,018H,018H,018H,018H,018H	; BT_B4
09E6	18	18	18	18	18	18	561	DB	018H,018H,018H,018H,018H,0F8H,018H,0F8H	; TH_B5
							562			
09EE	18	18	18	18	18	18	563	DB	018H,018H,018H,018H,018H,018H	; BT_B5
09F4	36	36	36	36	36	36	564	DB	036H,036H,036H,036H,036H,036H,0F6H	; TH_B6
							565			
09FC	36	36	36	36	36	36	566	DB	036H,036H,036H,036H,036H,036H	; BT_B6
0A02	00	00	00	00	00	00	567	DB	000H,000H,000H,000H,000H,000H,0FEH	; TH_B7
							568			
0A0A	36	36	36	36	36	36	569	DB	036H,036H,036H,036H,036H,036H	; BT_B7

0A10	00 00 00 00 00 F8	570	DB	000H,000H,000H,000H,000H,0F8H,018H,0F8H	; TH_B8
	18 F8	571			
0A18	18 18 18 18 18 18	572	DB	018H,018H,018H,018H,018H,018H	; BT_B8
0A1E	36 36 36 36 36 36	573	DB	036H,036H,036H,036H,036H,0F6H,006H,0F6H	; TH_B9
	06 F6	574			
0A26	36 36 36 36 36 36	575	DB	036H,036H,036H,036H,036H,036H	; BT_B9
0A2C	36 36 36 36 36 36	576	DB	036H,036H,036H,036H,036H,036H,036H,036H	; TH_BA
	36 36	577			
0A34	36 36 36 36 36 36	578	DB	036H,036H,036H,036H,036H,036H	; BT_BA
0A3A	00 00 00 00 00 FE	579	DB	000H,000H,000H,000H,000H,0FEH,006H,0F6H	; TH_BB
	06 FE	580			
0A42	36 36 36 36 36 36	581	DB	036H,036H,036H,036H,036H,036H	; BT_BB
0A48	36 36 36 36 36 36	582	DB	036H,036H,036H,036H,036H,036H,006H,0FEH	; TH_BC
	06 FE	583			
0A50	00 00 00 00 00 00	584	DB	000H,000H,000H,000H,000H,000H	; BT_BC
0A56	36 36 36 36 36 36	585	DB	036H,036H,036H,036H,036H,036H,036H,0FEH	; TH_BD
	36 FE	586			
0A5E	00 00 00 00 00 00	587	DB	000H,000H,000H,000H,000H,000H	; BT_BD
0A64	18 18 18 18 18 18	588	DB	018H,018H,018H,018H,018H,0F8H,018H,0F8H	; TH_BE
	18 F8	589			
0A6C	00 00 00 00 00 00	590	DB	000H,000H,000H,000H,000H,000H	; BT_BE
0A72	00 00 00 00 00 00	591	DB	000H,000H,000H,000H,000H,000H,000H,0F8H	; TH_BF
	00 F8	592			
0A7A	18 18 18 18 18 18	593	DB	018H,018H,018H,018H,018H,018H	; BT_BF
	18 F8	594			
0A80	18 18 18 18 18 18	595	DB	018H,018H,018H,018H,018H,018H,01FH	; TH_C0
	18 FF	596			
0A88	00 00 00 00 00 00	597	DB	000H,000H,000H,000H,000H,000H	; BT_C0
0A8E	18 18 18 18 18 18	598	DB	018H,018H,018H,018H,018H,018H,0FFH	; TH_C1
	18 FF	599			
0A96	00 00 00 00 00 00	600	DB	000H,000H,000H,000H,000H,000H	; BT_C1
0A9C	00 00 00 00 00 00	601	DB	000H,000H,000H,000H,000H,000H,0FFH	; TH_C2
	00 FF	602			
0AA4	18 18 18 18 18 18	603	DB	018H,018H,018H,018H,018H,018H	; BT_C2
0AAA	18 18 18 18 18 18	604	DB	018H,018H,018H,018H,018H,018H,01FH	; TH_C3
	18 FF	605			
0AB2	18 18 18 18 18 18	606	DB	018H,018H,018H,018H,018H,018H	; BT_C3
0AB8	00 00 00 00 00 00	607	DB	000H,000H,000H,000H,000H,000H,0FFH	; TH_C4
	00 FF	608			
0AC0	00 00 00 00 00 00	609	DB	000H,000H,000H,000H,000H,000H	; BT_C4
0AC6	18 18 18 18 18 18	610	DB	018H,018H,018H,018H,018H,018H,0FFH	; TH_C5
	18 FF	611			
0ACE	18 18 18 18 18 18	612	DB	018H,018H,018H,018H,018H,018H	; BT_C5
0AD4	18 18 18 18 18 1F	613	DB	018H,018H,018H,018H,018H,01FH,018H,01FH	; TH_C6
	18 FF	614			
0ADC	18 18 18 18 18 18	615	DB	018H,018H,018H,018H,018H,018H	; BT_C6
0AE2	36 36 36 36 36 36	616	DB	036H,036H,036H,036H,036H,036H,037H	; TH_C7
	36 3F	617			
0AEA	36 36 36 36 36 36	618	DB	036H,036H,036H,036H,036H,036H	; BT_C7
0AFO	36 36 36 36 36 3F	619	DB	036H,036H,036H,036H,036H,037H,030H,03FH	; TH_C8
	30 3F	620			
0AF8	00 00 00 00 00 00	621	DB	000H,000H,000H,000H,000H,000H	; BT_C8
0AFE	00 00 00 00 00 3F	622	DB	000H,000H,000H,000H,000H,03FH,030H,037H	; TH_C9
	30 3F	623			
0B06	36 36 36 36 36 36	624	DB	036H,036H,036H,036H,036H,036H	; BT_C9
0B0C	36 36 36 36 36 FF	625	DB	036H,036H,036H,036H,036H,0F7H,000H,0FFH	; TH_CA
	00 FF	626			
0B14	00 00 00 00 00 00	627	DB	000H,000H,000H,000H,000H,000H	; BT_CA
0B1A	00 00 00 00 00 FF	628	DB	000H,000H,000H,000H,000H,0FFH,000H,0F7H	; TH_CB
	00 FF	629			
0B22	36 36 36 36 36 36	630	DB	036H,036H,036H,036H,036H,036H	; BT_CB
0B28	36 36 36 36 36 3F	631	DB	036H,036H,036H,036H,036H,037H,030H,037H	; TH_CC
	30 3F	632			
0B30	36 36 36 36 36 36	633	DB	036H,036H,036H,036H,036H,036H	; BT_CC
0B36	00 00 00 00 00 FF	634	DB	000H,000H,000H,000H,000H,0FFH,000H,0FFH	; TH_CD
	00 FF	635			
0B3E	00 00 00 00 00 00	636	DB	000H,000H,000H,000H,000H,000H	; BT_CD
0B44	36 36 36 36 36 FF	637	DB	036H,036H,036H,036H,036H,0F7H,000H,0F7H	; TH_CE
	00 FF	638			
0B4C	36 36 36 36 36 36	639	DB	036H,036H,036H,036H,036H,036H	; BT_CE
0B52	18 18 18 18 18 FF	640	DB	018H,018H,018H,018H,018H,0FFH,000H,0FFH	; TH_CF
	00 FF	641			
0B5A	00 00 00 00 00 00	642	DB	000H,000H,000H,000H,000H,000H	; BT_CF
	00 FF	643			
0B60	36 36 36 36 36 36	644	DB	036H,036H,036H,036H,036H,036H,036H,0FFH	; TH_D0
	36 FF	645			
0B68	00 00 00 00 00 00	646	DB	000H,000H,000H,000H,000H,000H	; BT_D0
0B6E	00 00 00 00 00 FF	647	DB	000H,000H,000H,000H,000H,0FFH,000H,0FFH	; TH_D1
	00 FF	648			
0B76	18 18 18 18 18 18	649	DB	018H,018H,018H,018H,018H,018H	; BT_D1
0B7C	00 00 00 00 00 00	650	DB	000H,000H,000H,000H,000H,000H,0FFH	; TH_D2
	00 FF	651			
0B84	36 36 36 36 36 36	652	DB	036H,036H,036H,036H,036H,036H	; BT_D2
0B8A	36 36 36 36 36 36	653	DB	036H,036H,036H,036H,036H,036H,036H,03FH	; TH_D3
	36 3F	654			
0B92	00 00 00 00 00 00	655	DB	000H,000H,000H,000H,000H,000H	; BT_D3
0B98	18 18 18 18 18 1F	656	DB	018H,018H,018H,018H,018H,01FH,018H,01FH	; TH_D4
	18 FF	657			
0BA0	00 00 00 00 00 00	658	DB	000H,000H,000H,000H,000H,000H	; BT_D4
0BA6	00 00 00 00 00 1F	659	DB	000H,000H,000H,000H,000H,01FH,018H,01FH	; TH_D5
	18 FF	660			
0BAE	18 18 18 18 18 18	661	DB	018H,018H,018H,018H,018H,018H	; BT_D5
0BB4	00 00 00 00 00 00	662	DB	000H,000H,000H,000H,000H,000H,03FH	; TH_D6
	00 3F	663			
0BBC	36 36 36 36 36 36	664	DB	036H,036H,036H,036H,036H,036H	; BT_D6
0BC2	36 36 36 36 36 36	665	DB	036H,036H,036H,036H,036H,036H,036H,0FFH	; TH_D7
	36 FF	666			
0BCA	36 36 36 36 36 36	667	DB	036H,036H,036H,036H,036H,036H	; BT_D7
0BD0	18 18 18 18 18 FF	668	DB	018H,018H,018H,018H,018H,0F8H,018H,0FFH	; TH_D8
	18 FF	669			
0BD8	18 18 18 18 18 18	670	DB	018H,018H,018H,018H,018H,018H	; BT_D8
0BDE	18 18 18 18 18 18	671	DB	018H,018H,018H,018H,018H,018H,0F8H	; TH_D9
	18 F8	672			
0BE6	00 00 00 00 00 00	673	DB	000H,000H,000H,000H,000H,000H	; BT_D9
0BEC	00 00 00 00 00 00	674	DB	000H,000H,000H,000H,000H,000H,01FH	; TH_DA
	00 1F	675			
0BF4	18 18 18 18 18 18	676	DB	018H,018H,018H,018H,018H,018H	; BT_DA
0BF8A	FF FF FF FF FF FF	677	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH	; TH_DB
	FF FF	678			
0C02	FF FF FF FF FF FF	679	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH	; BT_DB
0C08	00 00 00 00 00 00	680	DB	000H,000H,000H,000H,000H,000H,0FFH	; TH_DC
	00 FF	681			
0C10	FF FF FF FF FF FF	682	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH	; BT_DC
0C16	F0 F0 F0 F0 F0 F0	683	DB	0F0H,0F0H,0F0H,0F0H,0F0H,0F0H,0F0H	; TH_DD
	F0 F0	684			
0C1E	F0 F0 F0 F0 F0 F0	685	DB	0F0H,0F0H,0F0H,0F0H,0F0H,0F0H	; BT_DD
0C24	0F 0F 0F 0F 0F 0F	686	DB	00FH,00FH,00FH,00FH,00FH,00FH,00FH,00FH	; TH_DE
	0F 0F	687			
0C2C	0F 0F 0F 0F 0F 0F	688	DB	00FH,00FH,00FH,00FH,00FH,00FH	; BT_DE
0C32	FF FF FF FF FF FF	689	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,000H	; TH_DF
	FF 00	690			
0C3A	00 00 00 00 00 00	691	DB	000H,000H,000H,000H,000H,000H	; BT_DF
	00 FF	692			
0C40	00 00 00 00 00 76	693	DB	000H,000H,000H,000H,000H,076H,0DCH,0D8H	; TH_E0
	DC D8	694			
0C48	D8 DC 76 00 00 00	695	DB	0D8H,0DCH,076H,000H,000H,000H	; BT_E0

0C4E	00 00 00 00 7C C6	696	DB	000H,000H,000H,000H,07CH,0C6H,0FCH,0C6H	; TH_E1
	FC C6	697			
0C56	C6 FC C0 C0 40 00	698	DB	0C6H,0FCH,0C0H,0C0H,040H,000H	; BT_E1
0C5C	00 00 FE C6 C6 00	699	DB	000H,000H,0FEH,0C6H,0C6H,0C0H,0C0H,0C0H	; TH_E2
	C0 C0	700			
0C64	C0 C0 C0 00 00 00	701	DB	0C0H,0C0H,0C0H,000H,000H,000H	; BT_E2
0C6A	00 00 00 00 FE 6C	702	DB	000H,000H,000H,000H,0FEH,0C6H,06CH,06CH	; TH_E3
	6C 6C	703			
0C72	6C 6C 6C 00 00 00	704	DB	06CH,06CH,06CH,000H,000H,000H	; BT_E3
0C78	00 00 FE C6 C6 30	705	DB	000H,000H,0FEH,0C6H,06CH,030H,018H,030H	; TH_E4
	18 30	706			
0C80	60 C6 FE 00 00 00	707	DB	060H,0C6H,0FEH,000H,000H,000H	; BT_E4
0C86	00 00 00 00 00 7E	708	DB	000H,000H,000H,000H,000H,07EH,0DBH,0DBH	; TH_E5
	D8 DB	709			
0C8E	D8 DB 70 00 00 00	710	DB	0DBH,0DBH,070H,000H,000H,000H	; BT_E5
0C94	00 00 00 00 66 66	711	DB	000H,000H,000H,000H,066H,066H,066H,066H	; TH_E6
	66 66	712			
0C9C	7C 60 60 C0 00 00	713	DB	07CH,060H,060H,0C0H,000H,000H	; BT_E6
0CA2	00 00 00 00 76 DC	714	DB	000H,000H,000H,000H,076H,0DCB,018H,018H	; TH_E7
	18 18	715			
0CAA	18 18 18 00 00 00	716	DB	018H,018H,018H,000H,000H,000H	; BT_E7
0CB0	00 00 7E 18 3C 66	717	DB	000H,000H,07EH,018H,03CH,066H,066H,066H	; TH_E8
	66 66	718			
0CB8	3C 18 7E 00 00 00	719	DB	03CH,018H,07EH,000H,000H,000H	; BT_E8
0CBE	00 00 38 6C C6 C6	720	DB	000H,000H,038H,06CH,0C6H,0C6H,0FEH,0C6H	; TH_E9
	FE C6	721			
0CC6	C6 C6 38 00 00 00	722	DB	0C6H,06CH,038H,000H,000H,000H	; BT_E9
0CCC	00 00 38 6C C6 C6	723	DB	000H,000H,038H,06CH,0C6H,0C6H,0C6H,06CH	; TH_EA
	C6 C6	724			
0CD4	6C 6C EE 00 00 00	725	DB	06CH,06CH,0EEH,000H,000H,000H	; BT_EA
0CDA	00 00 1E 30 18 0C	726	DB	000H,000H,01EH,030H,018H,00CH,03EH,066H	; TH_EB
	3E 66	727			
0CE2	66 66 3C 00 00 00	728	DB	066H,066H,03CH,000H,000H,000H	; BT_EB
0CE8	00 00 00 00 00 7E	729	DB	000H,000H,000H,000H,000H,07EH,0DBH,0DBH	; TH_EC
	DB DB	730			
0CF0	7E 00 00 00 00 00	731	DB	07EH,000H,000H,000H,000H,000H	; BT_EC
0CF6	00 00 03 06 7E DB	732	DB	000H,000H,003H,066H,07EH,0DBH,0DBH,0F3H	; TH_ED
	DB F3	733			
0CFE	7E 60 C0 00 00 00	734	DB	07EH,060H,0C0H,000H,000H,000H	; BT_ED
0D04	00 00 1C 30 60 60	735	DB	000H,000H,01CH,030H,060H,060H,07CH,060H	; TH_EE
	7C 60	736			
0D0C	60 00 1C 00 00 00	737	DB	060H,030H,01CH,000H,000H,000H	; BT_EE
0D12	00 00 00 07 C6 C6	738	DB	000H,000H,000H,07CH,0C6H,0C6H,0C6H,0C6H	; TH_EF
	C6 C6	739			
0D1A	C6 C6 C6 00 00 00	740	DB	0C6H,0C6H,0C6H,000H,000H,000H	; BT_EF
	C6 C6	741			
0D20	00 00 00 FE 00 00	742	DB	000H,000H,000H,0FEH,000H,000H,0FEH,000H	; TH_F0
	FE 00	743			
0D28	00 FE 00 00 00 00	744	DB	000H,0FEH,000H,000H,000H,000H	; BT_F0
0D2E	00 00 00 18 7E 7E	745	DB	000H,000H,000H,018H,018H,07EH,018H,018H	; TH_F1
	18 18	746			
0D36	00 00 FF 00 00 00	747	DB	000H,000H,0FFH,000H,000H,000H	; BT_F1
0D3C	00 00 30 18 0C 06	748	DB	000H,000H,030H,018H,00CH,060H,00CH,018H	; TH_F2
	0C 18	749			
0D44	30 00 7E 00 00 00	750	DB	030H,000H,07EH,000H,000H,000H	; BT_F2
0D4A	00 00 0C 18 30 60	751	DB	000H,000H,00CH,018H,030H,060H,030H,018H	; TH_F3
	30 18	752			
0D52	0C 00 7E 00 00 00	753	DB	00CH,000H,07EH,000H,000H,000H	; BT_F3
0D58	00 00 0E 18 1B 18	754	DB	000H,000H,00EH,018H,018H,018H,018H,018H	; TH_F4
	18 18	755			
0D60	08 18 18 18 18 18	756	DB	018H,018H,018H,018H,018H,018H,018H,018H	; BT_F4
0D66	18 18 18 18 18 18	757	DB	018H,018H,018H,018H,018H,018H,018H,018H	; TH_F5
	18 18	758			
0D6E	D8 DB 70 00 00 00	759	DB	0DBH,0DBH,070H,000H,000H,000H	; BT_F5
0D74	00 00 00 18 18 00	760	DB	000H,000H,000H,018H,018H,000H,07EH,000H	; TH_F6
	7E 00	761			
0D7C	18 18 00 00 00 00	762	DB	018H,018H,000H,000H,000H,000H	; BT_F6
0D82	00 00 00 00 76 DC	763	DB	000H,000H,000H,000H,076H,0DCB,000H,076H	; TH_F7
	00 76	764			
0D8A	DC 00 00 00 00 00	765	DB	0DCH,000H,000H,000H,000H,000H	; BT_F7
0D90	00 38 6C 6C 38 00	766	DB	000H,038H,06CH,06CH,038H,000H,000H,000H	; TH_F8
	00 00	767			
0D98	00 00 00 00 00 00	768	DB	000H,000H,000H,000H,000H,000H	; BT_F8
0D9E	00 00 00 00 00 00	769	DB	000H,000H,000H,000H,000H,000H,018H,018H	; TH_F9
	18 18	770			
0DA6	00 00 00 00 00 00	771	DB	000H,000H,000H,000H,000H,000H	; BT_F9
0DAC	00 00 00 00 00 00	772	DB	000H,000H,000H,000H,000H,000H,000H,018H	; TH_FA
	00 18	773			
0DB4	00 00 00 00 00 00	774	DB	000H,000H,000H,000H,000H,000H	; BT_FA
0DBA	00 0F 0C 0C 0C 0C	775	DB	000H,00FH,00CH,00CH,00CH,00CH,00CH,00CH	; TH_FB
	0C EC	776			
0DC2	6C 3C 1C 00 00 00	777	DB	06CH,03CH,01CH,000H,000H,000H	; BT_FB
0DC8	00 DB 6C 6C 6C 6C	778	DB	000H,0DBH,06CH,06CH,06CH,06CH,06CH,000H	; TH_FC
	6C 00	779			
0DD0	00 00 00 00 00 00	780	DB	000H,000H,000H,000H,000H,000H	; BT_FC
0DD6	00 70 DB 30 60 C8	781	DB	000H,070H,0DBH,030H,060H,0C8H,0F8H,000H	; TH_FD
	F8 00	782			
0DDE	00 00 00 00 00 00	783	DB	000H,000H,000H,000H,000H,000H	; BT_FD
0DE4	00 00 00 00 7C 7C	784	DB	000H,000H,000H,000H,07CH,07CH	; TH_FE
	7C 7C	785			
0DEF	7C 7C 00 00 00 00	786	DB	07CH,07CH,000H,000H,000H,000H	; BT_FE
0DF2	00 00 00 00 00 00	787	DB	000H,000H,000H,000H,000H,000H,000H,000H	; TH_FF
	00 00	788			
0DFA	00 00 00 00 00 00	789	DB	000H,000H,000H,000H,000H,000H	; BT_FF
0E00		790	CODE		
		791	END		

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1 PAGE,120
2 SUBTTL MONOCHROME CHARACTER GENERATOR - ALPHA SUPPLEMENT
3 CODE SEGMENT PUBLIC
4 PUBLIC CGMNM_FDC
5 CGMNM_FDC LABEL BYTE
6
7 ; STRUCTURE OF THIS FILE
8 ; DB XXH WHERE XX IS THE HEX CODE FOR THE FOLLOWING CHAR
9 ; DB [BYTES 0 - 13 OF THAT CHARACTER]
10 ;
11 ;
12 ; DB 00H INDICATES NO MORE REPLACEMENTS TO BE DONE
13
14 DB 01DH ;
15 DB 000H,000H,000H,000H,024H,066H,0FFH,066H ; TH_1D
16 DB FF 66 ;
17 DB 024H,000H,000H,000H,000H,000H ; BT_1D
18 DB 022H ;
19 DB 000H,063H,063H,063H,022H,000H,000H,000H,000H ; TH_22 "
20 ;
21 DB 000H,000H,000H,000H,000H,000H ; BT_22 "
22 DB 028H ;
23 DB 000H,000H,000H,018H,018H,018H,0FFH,018H ; TH_2B +
24 DB FF 18 ;
25 DB 018H,018H,000H,000H,000H,000H ; BT_2B +
26 DB 02DH ;
27 DB 000H,000H,000H,000H,000H,000H,0FFH,000H ; TH_2D -
28

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0036	00	00	00	00	00	29	DB	000H,000H,000H,000H,000H,000H	; BT_2D -
003C	4D					30	DB	040H	
003D	00	00	C3	E7	FF	31	DB	000H,000H,0C3H,0E7H,0FFH,0DBH,0C3H,0C3H	; TH_4D M
			C3	C3		32			
0045	C3	C3	C3	00	00	33	DB	0C3H,0C3H,0C3H,000H,000H,000H	; BT_4D M
0048	54	00	00	00	00	34	DB	054H	
004C	00	00	FF	0B	99	35	DB	000H,000H,0FFH,0DBH,099H,018H,018H,018H	; TH_54 T
	18	18				36			
0054	18	18	3C	00	00	37	DB	018H,018H,03CH,000H,000H,000H	; BT_54 T
005A	56	38				38	DB	056H	
005B	00	00	C3	C3	C3	39	DB	000H,000H,0C3H,0C3H,0C3H,0C3H,0C3H,0C3H	; TH_56 V
	C3	C3				40			
0063	66	3C	18	00	00	41	DB	066H,03CH,018H,000H,000H,000H	; BT_56 V
0069	57					42	DB	057H	
006A	00	00	C3	C3	C3	43	DB	000H,000H,0C3H,0C3H,0C3H,0C3H,0DBH,0DBH	; TH_57 W
	DB	DB				44			
0072	FF	66	00	00	00	45	DB	0FFH,066H,066H,000H,000H,000H	; BT_57 W
0078	58					46	DB	058H	
0079	00	00	C3	C3	66	47	DB	000H,000H,0C3H,0C3H,066H,03CH,018H,03CH	; TH_58 X
	18	3C				48			
0081	66	C3	C3	00	00	49	DB	066H,0C3H,0C3H,000H,000H,000H	; BT_58 X
0087	59					50	DB	059H	
0088	00	00	C3	C3	66	51	DB	000H,000H,0C3H,0C3H,0C3H,066H,03CH,018H	; TH_59 Y
	3C	18				52			
0090	18	18	3C	00	00	53	DB	018H,018H,03CH,000H,000H,000H	; BT_59 Y
0096	5A					54	DB	05AH	
0097	00	00	FF	C3	86	55	DB	000H,000H,0FFH,0C3H,086H,00CH,018H,030H	; TH_5A Z
	18	30				56			
009F	61	C3	FF	00	00	57	DB	061H,0C3H,0FFH,000H,000H,000H	; BT_5A Z
00A5	6D					58	DB	06DH	
00A6	00	00	00	00	E6	59	DB	000H,000H,000H,000H,000H,0E6H,0FFH,0DBH	; TH_6D L.C. M
	FF	DB				60			
00AE	DB	DB	DB	00	00	61	DB	0DBH,0DBH,0DBH,000H,000H,000H	; BT_6D L.C. M
00B8	76					62	DB	076H	
00B9	00	00	00	00	C3	63	DB	000H,000H,000H,000H,000H,0C3H,0C3H,0C3H	; TH_76 L.C. V
	C3	C3				64			
00BD	66	3C	18	00	00	65	DB	066H,03CH,018H,000H,000H,000H	; BT_76 L.C. V
00C3	77					66	DB	077H	
00C4	00	00	00	00	C3	67	DB	000H,000H,000H,000H,000H,0C3H,0C3H,0DBH	; TH_77 L.C. W
	C3	DB				68			
00CC	DB	FF	66	00	00	69	DB	0DBH,0FFH,066H,000H,000H,000H	; BT_77 L.C. W
00D2	91					70	DB	091H	
00D3	00	00	00	00	6E	71	DB	000H,000H,000H,000H,06EH,03BH,018H,07EH	; TH_91
	1B	7E				72			
00DB	DB	DC	77	00	00	73	DB	0DBH,0DCH,077H,000H,000H,000H	; BT_91
00E1	9B					74	DB	09BH	
00E2	00	18	18	7E	C3	75	DB	000H,018H,018H,07EH,0C3H,0C0H,0C0H,0C3H	; TH_9B
	CO	C3				76			
00EA	7E	18	18	00	00	77	DB	07EH,018H,018H,000H,000H,000H	; BT_9B
00F0	9D					78	DB	09DH	
00F1	00	00	C3	66	3C	79	DB	000H,000H,0C3H,066H,03CH,018H,0FFH,018H	; TH_9D
	FF	18				80			
00F9	FF	18	18	00	00	81	DB	0FFH,018H,018H,000H,000H,000H	; BT_9D
00FF	9E					82	DB	09EH	
0100	00	FC	66	66	7C	83	DB	000H,0FCB,066H,066H,07CH,062H,066H,06FH	; TH_9E
	66	6F				84			
0108	66	66	66	66	3C	85	DB	066H,066H,0F3H,000H,000H,000H	; BT_9E
010E	F1					86	DB	0F1H	
010F	00	00	18	18	18	87	DB	000H,000H,018H,018H,018H,0FFH,018H,018H	; TH_F1
	18	18				88			
0117	18	00	FF	00	00	89	DB	018H,000H,0FFH,000H,000H,000H	; BT_F1
011D	F6					90	DB	0F6H	
011E	00	18	00	18	00	91	DB	000H,000H,018H,018H,000H,000H,0FFH,000H	; TH_F6
	FF	00				92			
0126	00	18	18	00	00	93	DB	000H,018H,018H,000H,000H,000H	; BT_F6
012C	00					94	DB	000H	; NO MORE
012D	00					95	DB		
						96	DB		

CODE ENDS  
END

1								PAGE 120	
2								SUBTTL	
3								SEGMENT	
4	0000	CODE						PUBLIC	DOUBLE DOT CHARACTER GENERATOR
5	0000	CGDDOT						CGDDOT, INT_1F_1	
6								BYTE	
7									; DOUBLE DOT
8	0000	00	00	00	00	00	00	000H,000H,000H,000H,000H,000H,000H,000H	; D_00
9	0008	7E	81	A5	81	BD	99	07EH,081H,0A5H,081H,08DH,099H,081H,07EH	; D_01
10	0010	7E	FF	DB	FF	C3	E7	07EH,0FFH,0DBH,0FFH,0C3H,0E7H,0FFH,07EH	; D_02
11	0018	6C	FE	FE	FE	7C	38	06CH,0FEH,0FEH,0FEH,07CH,038H,010H,000H	; D_03
12	0020	10	38	7C	FE	7C	38	010H,038H,07CH,0FEH,07CH,038H,010H,000H	; D_04
13	0028	38	7C	38	FE	FE	7C	038H,07CH,038H,0FEH,0FEH,07CH,038H,07CH	; D_05
14	0030	10	38	7C	FE	7C	19	010H,010H,038H,07CH,0FEH,07CH,038H,07CH	; D_06
15	0038	00	00	18	3C	3C	18	000H,000H,018H,03CH,03CH,018H,000H,000H	; D_07
16	0040	FF	FF	E7	C3	C3	E7	0FFH,0FFH,0E7H,0C3H,0C3H,0E7H,0FFH,0FFH	; D_08
17	0048	00	3C	66	66	66	3C	000H,03CH,066H,042H,042H,066H,03CH,000H	; D_09
18	0050	FF	C3	99	80	BD	99	0FFH,0C3H,099H,08DH,08DH,099H,0C3H,0FFH	; D_0A
19	0058	0F	07	0F	0D	CC	CC	00FH,007H,00FH,07DH,0CCH,0CCH,0CCH,078H	; D_0B
20	0060	3C	66	66	66	3C	18	03CH,066H,066H,066H,03CH,018H,07EH,038H,07CH	; D_0C
21	0068	7E	18	3F	30	30	70	03FH,033H,03FH,030H,030H,070H,0F0H,0E0H	; D_0D
22	0070	7F	63	7F	63	63	67	07FH,063H,07FH,063H,063H,067H,066H,0C0H	; D_0E
23	0078	99	5A	3C	E7	E7	3C	099H,05AH,03CH,0E7H,0E7H,03CH,05AH,099H	; D_0F
24	0080	80	E0	F8	FE	F8	E0	080H,0E0H,0F8H,0FEH,0F8H,0E0H,080H,000H	; D_10
25	0088	02	0E	3E	FE	3E	0E	002H,00EH,03EH,0FEH,03EH,00EH,002H,000H	; D_11
26	0090	18	3C	7E	18	18	7E	018H,03CH,07EH,018H,018H,07EH,03CH,018H	; D_12
27	0098	66	66	66	66	66	00	066H,066H,066H,066H,066H,000H,066H,000H	; D_13
28	00A0	7F	DB	DB	7B	1B	1B	07FH,0DBH,0DBH,07BH,018H,018H,018H,000H	; D_14
29	00A8	3E	63	38	6C	38	50	03EH,063H,038H,06CH,06CH,038H,0CCH,018H	; D_15
30	00B0	00	00	00	00	7E	7E	000H,000H,000H,000H,07EH,07EH,07EH,000H	; D_16
31	00B8	18	3C	7E	18	7E	3C	018H,03CH,07EH,018H,07EH,03CH,018H,0FFH	; D_17
32	00C0	18	3C	7E	18	18	18	018H,03CH,07EH,018H,018H,018H,018H,000H	; D_18



18 00	57		
00C8 18 18 18 18 7E 3C	58	DB	018H,018H,018H,018H,07EH,03CH,018H,000H ; D_19
18 00	59		
00D0 00 18 0C FE 0C 18	60	DB	000H,018H,00CH,0FEH,00CH,018H,000H,000H ; D_1A
00 00	61		
00D8 00 30 60 FE 60 30	62	DB	000H,030H,060H,0FEH,060H,030H,000H,000H ; D_1B
00 00	63		
00E0 00 00 C0 C0 C0 FE	64	DB	000H,000H,0C0H,0C0H,0C0H,0FEH,000H,000H ; D_1C
00 00	65		
00E8 00 24 66 FF 66 24	66	DB	000H,024H,066H,0FFH,066H,024H,000H,000H ; D_1D
00 00	67		
00F0 00 18 3C 7E FF FF	68	DB	000H,018H,03CH,07EH,0FFH,0FFH,000H,000H ; D_1E
00 00	69		
00F8 00 FF 7E 3C 18	70	DB	000H,0FFH,0FFH,07EH,03CH,018H,000H,000H ; D_1F
00 00	71		
0100 00 00 00 00 00 00	72	DB	000H,000H,000H,000H,000H,000H,000H,000H ; SP D_20
00 00	73		
0108 30 78 78 30 30 00	74	DB	030H,078H,078H,030H,030H,000H,030H,000H ; ! D_21
00 00	75		
0110 6C 6C 6C 00 00 00	76	DB	06CH,06CH,06CH,000H,000H,000H,000H,000H ; " D_22
00 00	77		
0118 6C 6C FE 6C FE 6C	78	DB	06CH,06CH,0FEH,06CH,0FEH,06CH,06CH,000H ; # D_23
6C 00	79		
0120 30 7C 0C 78 0C F8	80	DB	030H,07CH,0C0H,078H,00CH,0F8H,030H,000H ; \$ D_24
30 00	81		
0128 00 C6 CC 18 30 66	82	DB	000H,0C6H,0CCH,018H,030H,066H,0C6H,000H ; PER CENT D_25
C6 00	83		
0130 78 6C 38 76 DC CC	84	DB	038H,06CH,038H,076H,0DCCH,0CCH,076H,000H ; & D_26
76 00	85		
0138 60 60 C0 00 00 00	86	DB	060H,060H,0C0H,000H,000H,000H,000H,000H ; ' D_27
00 00	87		
0140 18 30 60 60 60 30	88	DB	018H,030H,060H,060H,060H,030H,018H,000H ; ( D_28
18 00	89		
0148 60 30 18 18 18 30	90	DB	060H,030H,018H,018H,018H,030H,060H,000H ; ) D_29
60 00	91		
0150 00 66 3C FF 3C 66	92	DB	000H,066H,03CH,0FFH,03CH,066H,000H,000H ; * D_2A
00 00	93		
0158 00 30 30 FC 30 30	94	DB	000H,030H,030H,0FCH,030H,030H,000H,000H ; + D_2B
00 00	95		
0160 00 00 00 00 00 30	96	DB	000H,000H,000H,000H,000H,030H,030H,060H ; , D_2C
30 60	97		
0168 00 00 00 FC 00 00	98	DB	000H,000H,000H,0FCH,000H,000H,000H,000H ; - D_2D
00 00	99		
0170 00 00 00 00 00 30	100	DB	000H,000H,000H,000H,000H,030H,030H,000H ; . D_2E
30 00	101		
0178 06 0C 18 30 60 C0	102	DB	006H,00CH,018H,030H,060H,0C0H,080H,000H ; / D_2F
80 00	103		
0180 7C C6 CE DE F6 E6	104		
7C 00	105	DB	07CH,0C6H,0CEH,0DEH,0F6H,0E6H,07CH,000H ; 0 D_30
0188 30 70 30 30 30 30	106	DB	030H,070H,030H,030H,030H,030H,0FCH,000H ; 1 D_31
7C 00	107		
0190 78 CC 0C 38 60 CC	108	DB	078H,0CCH,00CH,038H,060H,0CCH,0FCH,000H ; 2 D_32
FC 00	109		
0198 78 CC 0C 38 0C CC	110	DB	078H,0CCH,00CH,038H,00CH,0CCH,078H,000H ; 3 D_33
78 00	111		
01A0 1C 3C 6C CC FE 0C	112	DB	01CH,03CH,06CH,0CCH,0FEH,00CH,01EH,000H ; 4 D_34
1C 00	113		
01A8 FC C0 F8 0C 0C CC	114	DB	0FCH,0C0H,0F8H,00CH,00CH,0CCH,078H,000H ; 5 D_35
78 00	115		
01B0 38 60 C0 F8 CC CC	116	DB	038H,060H,0C0H,0F8H,0CCH,0CCH,078H,000H ; 6 D_36
78 00	117		
01B8 FC CC 0C 18 30 30	118	DB	0FCH,0CCH,00CH,018H,030H,030H,030H,000H ; 7 D_37
30 00	119		
01C0 78 CC CC 78 CC CC	120	DB	078H,0CCH,0CCH,078H,0CCH,0CCH,078H,000H ; 8 D_38
78 00	121		
01C8 78 CC CC 7C 0C 18	122	DB	078H,0CCH,0CCH,07CH,00CH,018H,070H,000H ; 9 D_39
70 00	123		
01D0 00 30 30 00 00 30	124	DB	000H,030H,030H,000H,000H,030H,030H,000H ; : D_3A
30 00	125		
01D8 00 30 30 00 00 30	126	DB	000H,030H,030H,000H,000H,030H,030H,060H ; ; D_3B
30 60	127		
01E0 18 30 60 C0 60 30	128	DB	018H,030H,060H,0C0H,060H,030H,018H,000H ; < D_3C
18 00	129		
01E8 00 00 FC 00 00 FC	130	DB	000H,000H,0FCH,000H,000H,0FCH,000H,000H ; = D_3D
00 00	131		
01F0 60 30 18 0C 18 30	132	DB	060H,030H,018H,00CH,018H,030H,060H,000H ; > D_3E
60 00	133		
01F8 78 CC 0C 18 30 00	134	DB	078H,0CCH,00CH,018H,030H,000H,030H,000H ; ? D_3F
30 00	135		
0200 7C C6 DE DE DE C0	136		
78 00	137	DB	07CH,0C6H,0DEH,0DEH,0DEH,0C0H,078H,000H ; @ D_40
0208 30 78 CC CC FC CC	138	DB	030H,078H,0CCH,0CCH,0FCH,0CCH,0CCH,000H ; A D_41
CC 00	139		
0210 FC 66 66 7C 66 66	140	DB	0FCH,066H,066H,07CH,066H,066H,0FCH,000H ; B D_42
FC 00	141		
0218 3C 66 C0 C0 C0 66	142	DB	03CH,066H,0C0H,0C0H,0C0H,066H,03CH,000H ; C D_43
3C 00	143		
0220 F8 6C 66 66 66 6C	144	DB	0F8H,06CH,066H,066H,066H,06CH,0F8H,000H ; D D_44
F8 00	145		
0228 FE 62 68 78 68 62	146	DB	0FEH,062H,068H,078H,068H,062H,0FEH,000H ; E D_45
FE 00	147		
0230 FE 62 68 78 68 60	148	DB	0FEH,062H,068H,078H,068H,060H,0F0H,000H ; F D_46
FO 00	149		
0238 3C 66 C0 C0 CE 66	150	DB	03CH,066H,0C0H,0C0H,0CEH,066H,03EH,000H ; G D_47
3E 00	151		
0240 CC CC CC FC CC CC	152	DB	0CCH,0CCH,0CCH,0FCH,0CCH,0CCH,0CCH,000H ; H D_48
CC 00	153		
0248 78 30 30 30 30 30	154	DB	078H,030H,030H,030H,030H,030H,078H,000H ; I D_49
78 00	155		
0250 1E 0C 0C 0C CC CC	156	DB	01EH,00CH,00CH,00CH,0CCH,0CCH,078H,000H ; J D_4A
78 00	157		
0258 FE 66 6C 78 6C 66	158	DB	0E6H,066H,06CH,078H,06CH,066H,0E6H,000H ; K D_4B
E6 00	159		
0260 F0 60 60 62 66	160	DB	0F0H,060H,060H,060H,062H,066H,0FEH,000H ; L D_4C
F0 00	161		
0268 C6 EE FE FE D6 C6	162	DB	0C6H,0EEH,0FEH,0FEH,0D6H,0C6H,0C6H,000H ; M D_4D
C6 00	163		
0270 C6 6E F6 DE CE C6	164	DB	0C6H,0E6H,0F6H,0DEH,0CEH,0C6H,0C6H,000H ; N D_4E
C6 00	165		
0278 38 6C C6 C6 C6 C6	166	DB	038H,06CH,0C6H,0C6H,0C6H,06CH,038H,000H ; O D_4F
38 00	167		
0280 FC 66 66 7C 60 60	168	DB	0FCH,066H,066H,07CH,060H,060H,0F0H,000H ; P D_50
FO 00	169		
0288 78 CC CC CC DC 78	170	DB	078H,0CCH,0CCH,0CCH,0CCH,078H,01CH,000H ; Q D_51
1C 00	171		
0290 FC 66 7C 6C 66	172	DB	0FCH,066H,066H,07CH,06CH,066H,0E6H,000H ; R D_52
E6 00	173		
0298 78 CC E0 70 1C CC	174	DB	078H,0CCH,0E0H,070H,01CH,0CCH,078H,000H ; S D_53
78 00	175		
02A0 FC 84 30 30 30 30	176	DB	0FCH,0B4H,030H,030H,030H,030H,078H,000H ; T D_54
78 00	177		
02A8 CC CC CC CC CC CC	178	DB	0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,0FCH,000H ; U D_55
CC 00	179		
	180		
	181		
	182		

	FC	00	183		
0280	CC	CC	CC	CC	78
	30	00	184	DB	OCCH,OCCH,OCCH,OCCH,OCCH,078H,030H,000H ; V D_56
	00	00	185		
0288	C6	C6	C6	D6	FE
	C6	00	186	DB	OC6H,OC6H,OC6H,0D6H,0FEH,0EEH,OC6H,000H ; W D_57
	00	00	187		
02C0	C6	C6	C6	38	38
	C6	00	188	DB	OC6H,OC6H,06CH,038H,038H,06CH,OC6H,000H ; X D_58
	00	00	189		
02C8	CC	CC	CC	78	30
	78	00	191	DB	OCCH,OCCH,OCCH,078H,030H,030H,078H,000H ; Y D_59
	00	00	192		
02D0	FE	C6	8C	18	32
	FE	00	193	DB	0FEH,OC6H,08CH,018H,032H,066H,0FEH,000H ; Z D_5A
	00	00	194		
02D8	78	60	60	60	60
	78	00	195	DB	078H,060H,060H,060H,060H,060H,078H,000H ; [ D_5B
	00	00	196		
02E0	00	60	30	18	0C
	02	00	197	DB	OC0H,060H,030H,018H,00CH,006H,002H,000H ; BACKSLASH D_5C
	00	00	198		
02E8	78	18	18	18	18
	78	00	199	DB	078H,018H,018H,018H,018H,018H,078H,000H ; ] D_5D
	00	00	200		
02F0	00	38	6C	C6	00
	00	00	201	DB	010H,038H,06CH,OC6H,000H,000H,000H,000H ; CIRCUMFLEX D_5E
	00	00	202		
02F8	00	00	00	00	00
	00	FF	203	DB	000H,000H,000H,000H,000H,000H,000H,0FFH ; _ D_5F
	00	00	204		
0300	30	30	18	00	00
	00	00	205	DB	030H,030H,018H,000H,000H,000H,000H,000H ; ^ D_60
	00	00	206		
0308	00	00	78	0C	7C
	76	00	207	DB	000H,000H,078H,00CH,07CH,OCCH,076H,000H ; LOWER CASE A D_61
	00	00	208		
0310	E0	60	60	7C	66
	DC	00	210	DB	0E0H,060H,060H,07CH,066H,066H,0DCH,000H ; L.C. B D_62
	00	00	211		
0318	00	00	78	CC	CC
	78	00	212	DB	000H,000H,078H,OCCH,OCCH,OCCH,078H,000H ; L.C. C D_63
	00	00	213		
0320	1C	0C	0C	7C	CC
	76	00	214	DB	01CH,00CH,00CH,07CH,OCCH,OCCH,076H,000H ; L.C. D D_64
	00	00	215		
0328	00	00	78	CC	FC
	78	00	216	DB	000H,000H,078H,OCCH,0FCH,OC0H,078H,000H ; L.C. E D_65
	00	00	217		
0330	38	6C	60	F0	60
	F0	00	218	DB	038H,06CH,060H,0F0H,060H,060H,0F0H,000H ; L.C. F D_66
	00	00	219		
0338	00	00	76	CC	7C
	0C	F8	220	DB	000H,000H,076H,OCCH,OCCH,07CH,OCCH,00CH,0F8H ; L.C. G D_67
	00	00	221		
0340	E0	60	6C	76	66
	E6	00	222	DB	0E0H,060H,06CH,076H,066H,066H,0E6H,000H ; L.C. H D_68
	00	00	223		
0348	00	00	70	30	30
	78	00	224	DB	030H,000H,070H,030H,030H,030H,078H,000H ; L.C. I D_69
	00	00	225		
0350	0C	00	0C	0C	CC
	CC	78	226	DB	00CH,000H,00CH,00CH,00CH,OCCH,OC6H,078H ; L.C. J D_6A
	00	00	227		
0358	E0	60	66	6C	78
	E6	00	228	DB	0E0H,060H,066H,06CH,078H,066H,066H,000H ; L.C. K D_6B
	00	00	229		
0360	70	30	30	30	30
	78	00	230	DB	070H,030H,030H,030H,030H,030H,078H,000H ; L.C. L D_6C
	00	00	231		
0368	00	00	CC	FE	D6
	C6	00	232	DB	000H,000H,OCCH,0FEH,0FEH,0D6H,OC6H,000H ; L.C. M D_6D
	00	00	233		
0370	00	00	F8	CC	CC
	CC	00	234	DB	000H,000H,0F8H,OCCH,OCCH,OCCH,OCCH,OCCH,000H ; L.C. N D_6E
	00	00	235		
0378	00	00	78	CC	CC
	78	00	236	DB	000H,000H,078H,OCCH,OCCH,OCCH,078H,000H ; L.C. O D_6F
	00	00	237		
0380	00	00	DC	66	66
	60	F0	238	DB	000H,000H,0DCH,066H,066H,07CH,060H,0F0H ; L.C. P D_70
	00	00	239		
0388	00	00	76	CC	7C
	0C	1E	240	DB	000H,000H,076H,OCCH,OCCH,07CH,00CH,01EH ; L.C. Q D_71
	00	00	241		
0390	00	00	DC	76	66
	F0	00	242	DB	000H,000H,0DCH,076H,066H,066H,0F0H,000H ; L.C. R D_72
	00	00	243		
0398	00	00	7C	0C	78
	F8	00	244	DB	000H,000H,07CH,OC0H,078H,00CH,0F8H,000H ; L.C. S D_73
	00	00	245		
03A0	10	30	7C	30	34
	18	00	246	DB	010H,030H,07CH,030H,030H,034H,018H,000H ; L.C. T D_74
	00	00	247		
03A8	00	00	CC	CC	CC
	76	00	248	DB	000H,000H,OCCH,OCCH,OCCH,OCCH,OCCH,076H,000H ; L.C. U D_75
	00	00	249		
03B0	00	00	CC	CC	78
	30	00	251	DB	000H,000H,OCCH,OCCH,OCCH,078H,030H,000H ; L.C. V D_76
	00	00	252		
03B8	00	00	C6	D6	FE
	6C	00	253	DB	000H,000H,OC6H,0D6H,0FEH,0FEH,06CH,000H ; L.C. W D_77
	00	00	254		
03C0	00	00	C6	6C	38
	C6	00	255	DB	000H,000H,OC6H,06CH,038H,06CH,OC6H,000H ; L.C. X D_78
	00	00	256		
03C8	00	00	CC	CC	7C
	0C	F8	257	DB	000H,000H,OCCH,OCCH,OCCH,07CH,00CH,0F8H ; L.C. Y D_79
	00	00	258		
03D0	00	00	FC	98	64
	FC	00	259	DB	000H,000H,0FCH,098H,030H,064H,0FCH,000H ; L.C. Z D_7A
	00	00	260		
03D8	1C	30	E0	30	30
	1C	00	261	DB	01CH,030H,030H,0E0H,030H,030H,01CH,000H ; L. BRAK D_7B
	18	18	262		
03E0	18	18	18	00	18
	18	00	263	DB	018H,018H,018H,000H,018H,018H,018H,000H ;   D_7C
	00	00	264		
03E8	E0	30	30	1C	30
	E0	00	265	DB	0E0H,030H,030H,01CH,030H,030H,0E0H,000H ; R BRAK D_7D
	00	00	266		
03F0	76	DC	00	00	00
	00	00	267	DB	076H,0DCH,000H,000H,000H,000H,000H,000H ; T ILDE D_7E
	00	00	268		
03F8	00	10	38	6C	C6
	FE	00	269	DB	000H,010H,038H,06CH,OC6H,OC6H,06CH,0FEH,000H ; DELTA D_7F
	00	00	270		
0400			271		
			272	INT_1F_1	LABEL BYTE
0400	78	CC	C0	CC	78
	0C	78	273	DB	078H,OCCH,OC0H,OCCH,078H,018H,00CH,078H ; D_80
	00	CC	00	CC	CC
	7E	00	274		
0408	00	00	00	CC	CC
	7E	00	276	DB	000H,OCCH,000H,OCCH,OCCH,OCCH,07EH,000H ; D_81
	1C	00	78	CC	FC
	78	00	277	DB	01CH,000H,078H,OCCH,0FCH,OC0H,078H,000H ; D_82
	00	00	278		
0418	7E	C3	3C	06	3E
	3F	00	279	DB	07EH,0C3H,03CH,006H,03EH,066H,03FH,000H ; D_83
	00	00	280		
0420	CC	00	78	0C	7C
	7E	00	281	DB	OCCH,000H,078H,00CH,07CH,OCCH,07EH,000H ; D_84
	00	00	282		
0428	E0	00	78	0C	7C
	7E	00	284	DB	0E0H,000H,078H,00CH,07CH,OCCH,07EH,000H ; D_85
	00	00	285		
0430	30	30	78	0C	7C
	7E	00	286	DB	030H,030H,078H,00CH,07CH,OCCH,07EH,000H ; D_86
	00	00	287		
0438	00	00	78	0C	78
	0C	38	288	DB	000H,000H,078H,OC0H,OC0H,078H,00CH,038H ; D_87
	00	00	289		
0440	7E	C3	3C	66	7E
	3C	00	290	DB	07EH,0C3H,03CH,066H,07EH,060H,03CH,000H ; D_88
	00	00	291		
0448	00	00	78	CC	FC
	78	00	292	DB	OCCH,000H,078H,OCCH,0FCH,OC0H,078H,000H ; D_89
	00	00	293		
0450	E0	00	78	CC	FC
	78	00	294	DB	0E0H,000H,078H,OCCH,0FCH,OC0H,078H,000H ; D_8A
	00	00	295		
0458	CC	00	70	30	30
	78	00	296	DB	OCCH,000H,070H,030H,030H,030H,078H,000H ; D_8B
	00	00	297		
0460	7C	C6	38	18	18
	3C	00	298	DB	07CH,OC6H,038H,018H,018H,018H,03CH,000H ; D_8C
	00	00	299		
0468	E0	70	30	30	30
	78	00	300	DB	0E0H,000H,070H,030H,030H,030H,078H,000H ; D_8D
	00	00	301		
0470	C6	38	6C	6E	C6
	C6	00	302	DB	OC6H,038H,06CH,OC6H,0FEH,OC6H,OC6H,000H ; D_8E
	00	00	303		
0478	30	30	00	78	CC
	CC	00	304	DB	030H,030H,000H,078H,OCCH,0FCH,OCCH,000H ; D_8F
	00	00	305		
0480	1C	00	FC	60	78
	FC	00	306	DB	01CH,000H,0FCH,060H,078H,060H,0FCH,000H ; D_90
	00	00	307		
0488	00	00	7F	0C	7F
	00	00	308	DB	000H,000H,07FH,00CH,07FH,OCCH,07FH,000H ; D_91

0490	7F 00	309	DB	03EH, 06CH, 0CCH, 0FEH, 0CCH, 0CCH, 0CEH, 000H ;	D_92
	CE 00	310			
0498	78 CC 00 78 CC CC	312	DB	078H, 0CCH, 000H, 078H, 0CCH, 0CCH, 078H, 000H ;	D_93
	78 00	313			
04A0	00 CC 00 78 CC CC	314	DB	000H, 0CCH, 000H, 078H, 0CCH, 0CCH, 078H, 000H ;	D_94
	78 00	315			
04A8	00 ED 00 78 CC CC	316	DB	000H, 0E0H, 000H, 078H, 0CCH, 0CCH, 078H, 000H ;	D_95
	78 00	317			
04B0	78 CC 00 CC CC CC	318	DB	078H, 0CCH, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H ;	D_96
	7E 00	319			
04B8	00 ED 00 CC CC CC	320	DB	000H, 0E0H, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H ;	D_97
	7E 00	321			
04C0	00 CC 00 CC CC 7C	322	DB	000H, 0CCH, 000H, 0CCH, 0CCH, 07CH, 0CCH, 0F8H ;	D_98
	0C F8	323			
04C8	C3 18 3C 66 66 3C	324	DB	0C3H, 018H, 03CH, 066H, 066H, 03CH, 018H, 000H ;	D_99
	18 00	325			
04D0	CC 00 CC CC CC CC	326	DB	0CCH, 000H, 0CCH, 0CCH, 0CCH, 0CCH, 078H, 000H ;	D_9A
	0E 00	327			
04E0	18 18 7E C0 C0 7E	328	DB	018H, 018H, 07EH, 0C0H, 0C0H, 07EH, 018H, 018H ;	D_9B
	18 18	329			
04E8	38 6C 64 F0 60 E6	330	DB	038H, 06CH, 064H, 0F0H, 060H, 0E6H, 0FCH, 000H ;	D_9C
	FC 00	331			
04F0	CC CC 78 FC 30 37	332	DB	0CCH, 0CCH, 078H, 0FCH, 030H, 0FCH, 030H, 030H ;	D_9D
	30 30	333			
04F8	F8 CC CC FA C6 CF	334	DB	0F8H, 0CCH, 0CCH, 0FAH, 0C6H, 0CFH, 0C6H, 0C7H ;	D_9E
	C6 C7	335			
04F8	0E 18 18 3C 18 18	336	DB	0E0H, 018H, 018H, 03CH, 018H, 018H, 008H, 070H ;	D_9F
	08 70	337			
0500	1C 00 78 0C 7C CC	338			
	7E 00	339	DB	01CH, 000H, 078H, 00CH, 07CH, 0CCH, 07EH, 000H ;	D_A0
0508	38 00 70 30 30 30	340	DB	038H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ;	D_A1
	78 00	341			
0510	00 1C 00 78 CC CC	342	DB	000H, 01CH, 000H, 078H, 0CCH, 0CCH, 078H, 000H ;	D_A2
	78 00	343			
0518	00 1C 00 CC CC CC	344	DB	000H, 01CH, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H ;	D_A3
	7E 00	345			
0520	00 F8 00 F8 CC CC	346	DB	000H, 0F8H, 000H, 0F8H, 0CCH, 0CCH, 0CCH, 000H ;	D_A4
	CC 00	347			
0528	08 CC CC EC FC DC	348	DB	0FCH, 000H, 0CCH, 0ECH, 0FCH, 0DCH, 0CCH, 000H ;	D_A5
	CC 00	349			
0530	3C 6C 6E 3E 00 7E	350	DB	03CH, 06CH, 06CH, 03EH, 000H, 07EH, 000H, 000H ;	D_A6
	00 00	351			
0538	38 6C 6C 38 00 7C	352	DB	038H, 06CH, 06CH, 038H, 000H, 07CH, 000H, 000H ;	D_A7
	00 00	353			
0540	30 00 60 C0 CC	354	DB	030H, 000H, 030H, 060H, 0C0H, 0CCH, 078H, 000H ;	D_A8
	78 00	355			
0548	00 00 00 FC C0 C0	356	DB	000H, 000H, 000H, 0FCH, 0C0H, 0C0H, 000H, 000H ;	D_A9
	00 00	357			
0550	00 00 FC 0C 0C	358	DB	000H, 000H, 000H, 0FCH, 0C0H, 0C0H, 000H, 000H ;	D_AA
	00 00	359			
0558	C3 C6 CC DE 33 66	360	DB	0C3H, 0C6H, 0CCH, 0DEH, 033H, 066H, 0CCH, 00FH ;	D_AB
	CC 0F	361			
0560	C3 C6 CC DB 37 6F	362	DB	0C3H, 0C6H, 0CCH, 0DBH, 037H, 06FH, 0CFH, 003H ;	D_AC
	CF 03	363			
0568	18 18 00 18 18 18	364	DB	018H, 018H, 000H, 018H, 018H, 018H, 018H, 000H ;	D_AD
	18 00	365			
0570	00 33 66 CC 66 33	366	DB	000H, 033H, 066H, 0CCH, 066H, 033H, 000H, 000H ;	D_AE
	00 00	367			
0578	00 CC 66 33 66 CC	368	DB	000H, 0CCH, 066H, 033H, 066H, 0CCH, 000H, 000H ;	D_AF
	00 00	369			
		370			
		371			
0580	22 88 22 88 22 88	372	DB	022H, 088H, 022H, 088H, 022H, 088H, 022H, 088H ;	D_B0
	22 88	373			
0588	55 AA 55 AA 55 AA	374	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH, 055H, 0AAH ;	D_B1
	55 AA	375			
0590	DB 77 DB EE DB 77	376	DB	0DBH, 077H, 0DBH, 0EEH, 0DBH, 077H, 0DBH, 0EEH ;	D_B2
	DB EE	377			
0598	18 18 18 18 18 18	378	DB	018H, 018H, 018H, 018H, 018H, 018H, 018H, 018H ;	D_B3
	18 18	379			
05A0	18 18 18 18 F8 18	380	DB	018H, 018H, 018H, 018H, 0F8H, 018H, 018H, 018H ;	D_B4
	18 18	381			
05A8	18 18 F8 18 F8 18	382	DB	018H, 018H, 0F8H, 018H, 0F8H, 018H, 018H, 018H ;	D_B5
	18 18	383			
05B0	36 36 36 36 F6 36	384	DB	036H, 036H, 036H, 036H, 0F6H, 036H, 036H, 036H ;	D_B6
	36 36	385			
05B8	00 00 00 00 FE 36	386	DB	000H, 000H, 000H, 000H, 0FEH, 036H, 036H, 036H ;	D_B7
	36 36	387			
05C0	00 00 F8 18 F8 18	388	DB	000H, 000H, 0F8H, 018H, 0F8H, 018H, 018H, 018H ;	D_B8
	18 18	389			
05C8	36 36 F6 06 F6 36	390	DB	036H, 036H, 0F6H, 006H, 0F6H, 036H, 036H, 036H ;	D_B9
	36 36	391			
05D0	36 36 36 36 36 36	392	DB	036H, 036H, 036H, 036H, 036H, 036H, 036H, 036H ;	D_BA
	36 36	393			
05D8	00 00 FE 06 F6 36	394	DB	000H, 000H, 0FEH, 006H, 0F6H, 036H, 036H, 036H ;	D_BB
	36 36	395			
05E0	36 36 F6 06 FE 00	396	DB	036H, 036H, 0F6H, 006H, 0FEH, 000H, 000H, 000H ;	D_BC
	00 00	397			
05E8	36 36 36 36 FE 00	398	DB	036H, 036H, 036H, 036H, 0FEH, 000H, 000H, 000H ;	D_BD
	00 00	399			
05F0	18 18 F8 18 F8 00	400	DB	018H, 018H, 0F8H, 018H, 0F8H, 000H, 000H, 000H ;	D_BE
	00 00	401			
05F8	00 00 00 F8 18	402	DB	000H, 000H, 000H, 000H, 0F8H, 018H, 018H, 018H ;	D_BF
	18 18	403			
		404			
0600	18 18 18 18 FF 00	405	DB	018H, 018H, 018H, 018H, 01FH, 000H, 000H, 000H ;	D_C0
	00 00	406			
0608	18 18 18 18 FF 00	407	DB	018H, 018H, 018H, 018H, 0FFH, 000H, 000H, 000H ;	D_C1
	00 00	408			
0610	00 00 00 00 FF 18	409	DB	000H, 000H, 000H, 000H, 0FFH, 018H, 018H, 018H ;	D_C2
	18 18	410			
0618	18 18 18 18 FF 18	411	DB	018H, 018H, 018H, 018H, 01FH, 018H, 018H, 018H ;	D_C3
	18 18	412			
0620	00 00 00 00 FF 00	413	DB	000H, 000H, 000H, 000H, 0FFH, 000H, 000H, 000H ;	D_C4
	00 00	414			
0628	18 18 18 18 FF 18	415	DB	018H, 018H, 018H, 018H, 0FFH, 018H, 018H, 018H ;	D_C5
	18 18	416			
0630	18 18 1F 18 1F 18	417	DB	018H, 018H, 01FH, 018H, 01FH, 018H, 018H, 018H ;	D_C6
	18 18	418			
0638	36 36 36 36 37 36	419	DB	036H, 036H, 036H, 036H, 037H, 036H, 036H, 036H ;	D_C7
	36 36	420			
0640	36 36 37 30 3F 00	421	DB	036H, 036H, 037H, 030H, 03FH, 000H, 000H, 000H ;	D_C8
	00 00	422			
0648	00 00 3F 30 37 36	423	DB	000H, 000H, 03FH, 030H, 037H, 036H, 036H, 036H ;	D_C9
	36 36	424			
0650	36 36 F7 00 FF 00	425	DB	036H, 036H, 0F7H, 000H, 0FFH, 000H, 000H, 000H ;	D_CA
	00 00	426			
0658	00 00 FF 00 F7 36	427	DB	000H, 000H, 0FFH, 000H, 0F7H, 036H, 036H, 036H ;	D_CB
	36 36	428			
0660	36 36 37 30 37 36	429	DB	036H, 036H, 037H, 030H, 037H, 036H, 036H, 036H ;	D_CC
	00 00	430			
0668	00 00 00 FF 00	431	DB	000H, 000H, 0FFH, 000H, 0FFH, 000H, 000H, 000H ;	D_CD
	00 00	432			
0670	36 36 F7 00 F7 36	433	DB	036H, 036H, 0F7H, 000H, 0F7H, 036H, 036H, 036H ;	D_CE
	36 36	434			

0678	18 18 FF 00 FF 00	435	DB	018H,018H,0FFH,000H,0FFH,000H,000H,000H ;	D_CF
	00 00	436			
		437			
0680	36 36 36 36 FF 00	438	DB	036H,036H,036H,036H,0FFH,000H,000H,000H ;	D_D0
	00 00	439			
0688	00 00 FF 00 FF 18	440	DB	000H,000H,0FFH,000H,0FFH,018H,018H,018H ;	D_D1
	18 18	441			
0690	00 00 00 00 FF 36	442	DB	000H,000H,000H,000H,0FFH,036H,036H,036H ;	D_D2
	36 36	443			
0698	36 36 36 36 3F 00	444	DB	036H,036H,036H,036H,03FH,000H,000H,000H ;	D_D3
	00 00	445			
06A0	18 18 1F 18 1F 00	446	DB	018H,018H,01FH,018H,01FH,000H,000H,000H ;	D_D4
	00 00	447			
06A8	00 00 1F 18 1F 18	448	DB	000H,000H,01FH,018H,018H,018H,018H,018H ;	D_D5
	18 18	449			
06B0	00 00 00 00 3F 36	450	DB	000H,000H,000H,000H,03FH,036H,036H,036H ;	D_D6
	36 36	451			
06B8	36 36 36 36 FF 36	452	DB	036H,036H,036H,036H,0FFH,036H,036H,036H ;	D_D7
	36 36	453			
06C0	18 18 FF 18 FF 18	454	DB	018H,018H,0FFH,018H,0FFH,018H,018H,018H ;	D_D8
	18 18	455			
06C8	18 18 18 18 F8 00	456	DB	018H,018H,018H,018H,0F8H,000H,000H,000H ;	D_D9
	00 00	457			
06D0	00 00 00 00 1F 18	458	DB	000H,000H,000H,000H,01FH,018H,018H,018H ;	D_DA
	18 18	459			
06D8	FF FF FF FF FF FF	460	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ;	D_DB
	FF FF	461			
06E0	00 00 00 00 FF FF	462	DB	000H,000H,000H,000H,0FFH,0FFH,0FFH,0FFH ;	D_DC
	FF FF	463			
06E8	F0 F0 F0 F0 F0 F0	464	DB	0F0H,0F0H,0F0H,0F0H,0F0H,0F0H,0F0H,0F0H ;	D_DD
	F0 F0	465			
06F0	0F 0F 0F 0F 0F 0F	466	DB	00FH,00FH,00FH,00FH,00FH,00FH,00FH,00FH ;	D_DE
	0F 0F	467			
06F8	FF FF FF FF 00 00	468	DB	0FFH,0FFH,0FFH,0FFH,000H,000H,000H,000H ;	D_DF
	00 00	469			
		470			
0700	00 00 76 DC C8 DC	471	DB	000H,000H,076H,0DCH,0C8H,0DCH,076H,000H ;	D_E0
	76 00	472			
0708	00 78 CC F8 CC F8	473	DB	000H,078H,0CCH,0F8H,0CCH,0F8H,0CCH,0CCH ;	D_E1
	CC 00	474			
0710	00 FC CC C0 C0 C0	475	DB	000H,0FCH,0CCH,0CCH,0CCH,0CCH,0CCH,000H ;	D_E2
	C0 00	476			
0718	00 FE 6C 6C 6C 6C	477	DB	000H,0FEH,06CH,06CH,06CH,06CH,06CH,000H ;	D_E3
	6C 00	478			
0720	FC CC 60 30 60 CC	479	DB	0FCH,0CCH,060H,030H,060H,0CCH,0FCH,000H ;	D_E4
	FC 00	480			
0728	00 00 7E D8 D8 D8	481	DB	000H,000H,07EH,0DBH,0DBH,0DBH,070H,000H ;	D_E5
	70 00	482			
0730	00 66 66 66 66 7C	483	DB	000H,066H,066H,066H,066H,06CH,060H,0CCH ;	D_E6
	60 00	484			
0738	00 76 DC 18 18 18	485	DB	000H,076H,0DCH,018H,018H,018H,018H,000H ;	D_E7
	18 00	486			
0740	FC 30 78 CC CC 78	487	DB	0FCH,030H,078H,0CCH,0CCH,078H,030H,0FCH ;	D_E8
	30 FC	488			
0748	38 6C C6 FE 6C 6C	489	DB	038H,06CH,0C6H,0FEH,0C6H,06CH,038H,000H ;	D_E9
	38 00	490			
0750	38 6C C6 C6 6C 6C	491	DB	038H,06CH,0C6H,0C6H,06CH,06CH,0EEH,000H ;	D_EA
	EE 00	492			
0758	1C 30 18 7C CC CC	493	DB	01CH,030H,018H,07CH,0CCH,0CCH,078H,000H ;	D_EB
	78 00	494			
0760	00 00 7E D8 D8 7E	495	DB	000H,000H,07EH,0DBH,0DBH,07EH,000H,000H ;	D_EC
	00 00	496			
0768	06 0C 7E D8 D8 7E	497	DB	006H,00CH,07EH,0DBH,0DBH,07EH,060H,0CCH ;	D_ED
	60 00	498			
0770	38 60 C0 F8 C0 60	499	DB	038H,060H,0C0H,0F8H,0C0H,060H,038H,000H ;	D_EE
	38 00	500			
0778	78 CC CC CC CC CC	501	DB	078H,0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,000H ;	D_EF
	CC 00	502			
		503			
0780	00 FC 00 FC 00 FC	504	DB	000H,0FCH,000H,0FCH,000H,0FCH,000H,000H ;	D_F0
	00 00	505			
0788	30 30 FC 30 30 00	506	DB	030H,030H,0FCH,030H,030H,000H,0FCH,000H ;	D_F1
	FC 00	507			
0790	60 30 18 30 60 00	508	DB	060H,030H,018H,030H,060H,000H,0FCH,000H ;	D_F2
	FC 00	509			
0798	18 30 60 30 18 00	510	DB	018H,030H,060H,030H,018H,000H,0FCH,000H ;	D_F3
	FC 00	511			
07A0	0E 18 18 18 18 18	512	DB	00EH,018H,018H,018H,018H,018H,018H,018H ;	D_F4
	18 18	513			
07A8	18 18 18 18 18 D8	514	DB	018H,018H,018H,018H,018H,0DBH,0DBH,070H ;	D_F5
	D8 70	515			
07B0	30 30 00 FC 00 30	516	DB	030H,030H,000H,0FCH,000H,030H,030H,000H ;	D_F6
	30 00	517			
07B8	00 76 DC 00 76 DC	518	DB	000H,076H,0DCH,000H,076H,0DCH,000H,000H ;	D_F7
	00 00	519			
07C0	38 6C 6C 38 00 00	520	DB	038H,06CH,06CH,038H,000H,000H,000H,000H ;	D_F8
	00 00	521			
07C8	00 00 00 18 18 00	522	DB	000H,000H,000H,018H,018H,000H,000H,000H ;	D_F9
	00 00	523			
07D0	00 00 00 00 18 00	524	DB	000H,000H,000H,000H,018H,000H,000H,000H ;	D_FA
	00 00	525			
07D8	0F 0C 0C EC EC 6C	526	DB	00FH,00CH,00CH,00CH,0ECH,06CH,03CH,01CH ;	D_FB
	3C 1C	527			
07E0	78 6C 6C 6C 6C 00	528	DB	078H,06CH,06CH,06CH,06CH,000H,000H,000H ;	D_FC
	00 00	529			
07E8	70 18 30 60 78 00	530	DB	070H,018H,030H,060H,078H,000H,000H,000H ;	D_FD
	00 00	531			
07F0	00 00 3C 3C 3C 3C	532	DB	000H,000H,03CH,03CH,03CH,03CH,000H,000H ;	D_FE
	00 00	533			
07F8	00 00 00 00 00 00	534	DB	000H,000H,000H,000H,000H,000H,000H,000H ;	D_FF
	00 00	535			
		536			
0800		537			

```

CODE      ENDS
END

1          PAGE,120
2          SUBTTL  END ADDRESS
3          CODE   SEGMENT PUBLIC
4          PUBLIC END ADDRESS
5          END ADDRESS LABEL BYTE
6          CODE   ENDS
7          END

```

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