

PIN#	SIGNAL	DIRECTION	DESCRIPTION
1	$\overline{\text{STROBE}}$	IN	STROBE pulse to red data in
2	DATA 1	IN	These signals represent the 1st through 8th bits of parallel data respectively. Each signal is at HIGH level when data is logical 1 and LOW when logical 0.
3	DATA 2	IN	
4	DATA 3	IN	
5	DATA 4	IN	
6	DATA 5	IN	
7	DATA 6	IN	
8	DATA 7	IN	
	DATA 8	IN	
10	$\overline{\text{ACK}}$	OUT	Aprox. 11us pulse rising edge indicates that data has been received. Selector is ready to receive other data.
11	BUSY	OUT	HIGH signal indicates selector cannot receive.
12	-----	-----	Grounded.
13	$\overline{\text{BUSY}}$	OUT	In TAN mode, this signal is the complement of BUSY In EML mode, this is the SELECT signal. A HIGH signal indicates that the channel is selected or locked to.
14 15	NC	NC	Not used.
16	-----	-----	Grounded
17	FG	-----	Frame ground.
18	+5V	-----	DC + 5V
19 30	GND	-----	Signal ground
31	$\overline{\text{INIT}}$	IN	When signal level is LOW, selector is set to initial state and buffer is cleared. A 70us width negative going INIT pulse is sent to currently selected printer at the same time
32	$\overline{\text{FAULT}}$	OUT	Reflects the state of the FAULT

33	INIT	IN	Same as 31 for Tandy Computer
34 36	NC	NC	Not used.

(dkh-08/05/93)

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