

## I/O Address Maps

## Fixed Addresses

Address	R/W	Description
200H	W	Game Port Trigger
200H	R	Game Port Status
201H	W	Game Port Trigger
201H	R	Game Port Status
202H-207H	-	Game Port
388H	W	FM Music Register Address Port
388H	R	FM Music Status Port
389H	W	FM Music Data Port
38AH-39FH	-	Reserved

Addresses Relative to  
Base Port Address 220H or 240H

Offset Address	R/W	Description
00H	W	FM Music Register Address Port
00H	R	FM Music Register Status Port
01H	W	FM Music Data Port
02H	W	Advanced FM Music Status Port
02H	R	Advanced FM Music Register Address Port
03H	W	Advanced FM Music Data Port
04H	W	Mixer Chip Register Address Port
05H	W	Mixer Chip Data Port
05H	R	Mixer Chip Data Port
06H	W	DSP Reset
07H	-	Reserved
08H	W	FM Music Register Address Port
08H	R	FM Music Status Port
09H	W	FM Music Data Port
0AH	R	DSP Read Data Port
0BH	-	Reserved
0CH	W	DSP Write Data or Command
0CH	R	DSP Write Buffer Status (Bit 7)
0DH	W	Reserved
0EH	R	DSP Data Available Status (Bit 7)
0FH-13H	-	Reserved

CD-ROM Addresses Relative to  
CD-ROM Port Address

Address	R/W	Description
0	R	Data Status
1	R	UART Receive
2	R	FIFO
3	R	Drive Status
4	W	Data Control

5	W	UART Transmit
6	W	Test Clock
7	W	Test Control

Mixer Chip Address Map

Index	D7	D6	D5	D4	D3	D2	D1	D0
00H	DATA RESET PORT							
02H	-	-	-	-	MAS-VOL-BOTH			-
04H	VOC-VOL-LEFT			-	VOC-VOL-RIGHT			-
06H	-	FMLC	FMRC	-	FM-VOL-BOTH			-
08H	-	-	-	-	-	-	-	-
0AH	-	-	-	-	-	MIC-VOL		-
0CH	-	-	ANFI	-	ADC			-
0EH	-	-	DNFI	-	-		VSTC	-
20H	-	-	-	-	LSW	CSW	MSW	-
22H	MAS-VOL-LEFT			-	MAS-VOL-RIGHT			-
24H	-	-	-	-	-	-	-	-
26H	FM-VOL-LEFT			-	FM-VOL-RIGHT			-
28H	CD-VOL-LEFT			-	CD-VOL-RIGHT			-
2AH	-	-	-	-	-	-	-	-
2CH	-	-	-	-	-	-	-	-
2EH	LIN-VOL-Left			-	LIN-VOL-RIGHT			-

(smc-02/02/94)